

# **Appendix C**

## Signal Timing Plans & Signal Warrant Worksheets

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F							
	1	2	3	4	5	6	7	8		CLK RST	E	1	2	3	4	5	6	7	8
0 WALK	1	1	1	1	1	1	1	1	0	EV SEL	PERMIT	1	2	3	4	5	6	7	8
1 DONT WALK	1	1	1	1	1	1	1	1	5	RR1 CLR	RED LOCK								
2 MTN GREEN	12	13	1	1	1	1	1	5	0	EVA DLY	YEL LOCK	1							
3 TYPE 3 DET	0	0	0	0	0	0	0	0	5	EVA CLR	V RECALL		2						
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0	EVB DLY	P RECALL								
5 PASSAGE	2.0	1.0	0.9	0.9	0.9	0.9	0.9	1.0	5	EVB CLR	PED PHASES								
6 MAX GAP	2.0	1.0	0.9	0.9	0.9	0.9	0.9	1.0	0	EVC DLY	RT OLA								
7 MIN GAP	2.0	1.0	0.9	0.9	0.9	0.9	0.9	1.0	5	EVC CLR	RT OLB								
8 MAX EXT	25	40	9	9	9	9	9	30	0	EVD DLY	DBL ENTRY								
9 MAX 2									5	EVD CLR	MAX 2 PHASES								
A MAX 3									255	MAX EV	LAG PHASES								
B									5	RR2 CLR	RED REST								
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			REST-IN-WALK								
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0			MAX 3 PHASES								
E YELLOW	4.1	4.1	3.0	3.0	3.0	3.0	3.0	4.1			YEL START UP	2							
F RED	1.0	1.0	0.0	0.0	0.0	0.0	0.0	1.0			FIRST PHASE								
PED XING FT																			
BIKE XING FT	82	98																	

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY



F0C LONG FAILURE	
F0D SHORT FAILURE	0
F0F	5

F0C	3
F01	3
F02	10
F0A	0.0
F0B	0.0
F0C	0.0
F0D	0.0

F0D TB SELECT	1
F03 PED SELECT	0
F04 7 WIRE	0
F05 PERMISSIVE	0
F08 OS SEEKING	1

C05 FLASH TYPE	1
CC2 DOWNLOAD	1

	CONTROL PLANS									Y-COORD		LAG PHASE	FLAG										
	1	2	3	4	5	6	7	8	9	C	D		E	F	1	2	3	4	5	6	7	8	
0	CYCLE LENGTH	90	80	110	80									LAG FZ FREE	2	2	3	4	4	6	6	8	0
1	FZ1 GRN FCTR	22	14	23	22									LAG FZ CP 1	2	2	3	4	4	6	6	8	1
2														LAG FZ CP 2	2	2	3	4	4	6	6	8	2
3	FZ3 GRN FCTR	0	0	0	0									LAG FZ CP 3	2	2	3	4	4	6	6	8	3
4	FZ4 GRN FCTR	0	0	0	0									LAG FZ CP 4	2	2	3	4	4	6	6	8	4
5	FZ5 GRN FCTR	0	0	0	0									LAG FZ CP 5									5
6														LAG FZ CP 6									6
7	FZ7 GRN FCTR	0	0	0	0									LAG FZ CP 7									7
8	FZ8 GRN FCTR	25	38	35	25									LAG FZ CP 8									8
9	MULTI CYCLE	0	0	0	0									LAG FZ CP 9									9
A	OFFSET A	60	74	1	74									LAG C COORD									A
B	OFFSET B													LAG D COORD									B
C	OFFSET C													COORD FAZES	2					6			C
D	FZ 3 EXT																						D
E	FZ 7 EXT																						E
F	OFFSET INTRPT																						F

FEATURE

FEATURE	OFF	ON
1		
2		
3		
4		
5		
6		
7		
8		

LOCATION

LOCATION	OFF	ON
1		
2		1
3		
4		
5		
6		
7		
8		

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER  
 COO = 2

C01 MANUAL CP  
 C02 MASTER CP  
 C03 CURRENT CP  
 C04 LAST CP  
 C07 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

SYSTEM MASTER:  
 RTE 52 EB OFF

D	FLAGS								E	MIN RCL	FLAGS								F	PED RCL	FLAGS													
	1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8						
MAX RCL																																		
0																																		
1									CP 1	1								CP 1																
2									CP 2	1								CP 2																
3									CP 3	1								CP 3																
4									CP 4	1								CP 4																
5									CP 5									CP 5																
6									CP 6									CP 6																
7									CP 7									CP 7																
8									CP 8									CP 8																
9									CP 9									CP 9																
A																		RCL 1																
B																		RCL 2																
C																																		
D																																		
E																																		
F																																		

E	FUNCTION								F	FUNCTION																						
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8															
0									CODE 4																							
1									CODE 5																							
2									C-RECALL																							
3									D-RECALL																							
4									EXCLUSIVE																							
5									2 PED	2																						
6									6 PED							6																
7									4 PED							4																
8									8 PED																							
9																																
A									OLA ON																							
B									OLB ON																							
C									OLC ON																							
D									OLD ON																							
E																																
F																																

LAST POWER FAILURE REGISTER

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD



F+C+E+I+2+3+E+B+ E+PHASES OR TYPE+EVENT NO.		PHASES		PHASES		TYPE	
		C	D	E	F		
0	I1	1	5,6	J1	5	5,6	
1	I2U	2	5,6	J2U	6	5,6	
2	I2L	2	5,6	J2L	6	5,6	
3	I3U	2	5,6	J3U	6	5,6	
4	I3L	2	5	J3L	6	5	
5	I4	2	7,8	J4	6	7,8	
6	I5	3	5,6	J5	7	5,6	
7	I6U	4	5,6	J6U	8	5,6	
8	I6L	4	5,6	J6L	8	5,6	
9	I7U	4	5,6	J7U	8	5,6	
A	I7L	4	5	J7L	8	5	5,6
B	I8	4	7,8	J8	8	1	7,8
C	I9U	1	5,6	J9U	5	1	5,6
D	I9L	3	5,6	J9L	7	1	5,6

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

I FILE				J FILE			
DELAY	CARRYOVER	DELAY	CARRYOVER	DELAY	CARRYOVER	DELAY	CARRYOVER
I1	D10	D30	1.0	J1	D20	D40	
I2U	D11	D31	2.5	J2U	D21	D41	
I2L	D12	D32		J2L	D22	D42	
I3U	D13	D33		J3U	D23	D43	
I3L	D14	D34		J3L	D24	D44	
I4	D15	D35		J4	D25	D45	
I5	D16	D36		J5	D26	D46	
I6U	D17	D37		J6U	D27	D47	2.0
I6L	D18	D38		J6L	D28	D48	2.0
I7U	D19	D39		J7U	D29	D49	
I7L	D1A	D3A		J7L	D2A	D4A	
I8	D1B	D3B		J8	D2B	D4B	
I9U	D1C	D3C		J9U	D2C	D4C	12.0
I9L	D1D	D3D		J9L	D2D	D4D	

DETECTOR SETTINGS

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F								
	1	2	3	4	5	6	7	8		CLK RST	E	FLAGS	1	2	3	4	5	6	7	8
0 WALK	1	1	1	1	1	1	1	1		EV SEL	0	PERMIT	1	2	3	4	5	6	7	8
1 DONT WALK	1	1	1	1	1	1	1	1		RRI CLR	5	RED LOCK								
2 MIN GREEN	1	1	1	5	13	14	1	1		EVA DLY	0	YEL LOCK								
3 TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2						
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL					6			
5 PASSAGE	0.9	0.9	0.9	1.0	1.0	1.0	0.9	0.9		EVB CLR	5	RED PHASES								
6 MAX GAP	0.9	0.9	0.9	1.0	1.0	1.0	0.9	0.9		EVC DLY	0	RT OLA								
7 MIN GAP	0.9	0.9	0.9	1.0	1.0	1.0	0.9	0.9		EVC CLR	5	RT OLB								
8 MAX EXT	9	9	9	25	35	30	9	9		EVD DLY	0	DBL ENTRY								
9 MAX 2										EVD CLR	5	MAX 2 PHASES								
A MAX 3										MAX EV	255	LAG PHASES								
B										RR2 CLR	5	RED REST								
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0				REST-IN-WALK								
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0				MAX 3 PHASES								
E YELLOW	3.0	3.0	3.0	4.1	4.1	4.1	3.0	3.0				YEL START UP		2						
F RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0				FIRST PHASE			4					8
PED XING FT																				
BIKE XING FT					93	115														

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	0
FOE	5
FOF	

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

	CONTROL PLANS									Y-COORD		LAG PHASE			FLAGS							
	1	2	3	4	5	6	7	8	9	C	D	E	F	1	2	3	4	5	6	7	8	
0	CYCLE LENGTH	90	80	110	80									LAG FZ FREE	2	4	4	6	6	7	8	
1	FZ1 GRN FCTR	0	0	0	0									LAG FZ CP 1	2	4	4	6	6	7	8	
2														LAG FZ CP 2	2	4	4	6	6	7	8	
3	FZ3 GRN FCTR	0	0	0	0									LAG FZ CP 3	2	4	4	6	6	7	8	
4	FZ4 GRN FCTR	15	17	15	17									LAG FZ CP 4	2	4	4	6	6	7	8	
5	FZ5 GRN FCTR	30	19	53	20									LAG FZ CP 5								
6														LAG FZ CP 6								
7	FZ7 GRN FCTR	0	0	0	0									LAG FZ CP 7								
8	FZ8 GRN FCTR	0	0	0	0									LAG FZ CP 8								
9	MULTI CYCLE	0	0	0	0									LAG FZ CP 9								
A	OFFSET A	0	35	38	35									LAG C COORD								
B	OFFSET B	0	0	0	0									LAG D COORD								
C	OFFSET C													COORD FAZES	2							
D	FZ 3 EXT																					
E	FZ 7 EXT																					
F	OFFSET INTRPT																					

FEATURE

1	OFF	ON
2		
3		
4		
5		
6		
7		
8		

LOCATION

1	OFF	ON
2		1
3		
4		
5		
6		
7		
8		

COO = 1

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP  
 CO4 LAST CP  
 CO7 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

SYSTEM MASTER:  
 RTE 52 EB OFF

CBB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

D PAGE

E PAGE

D	FLAGS								E	FLAGS								F	FLAGS							
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
MAX																										
RCL								RCL									RCL									
CP 1								CP 1									CP 1									
CP 2								CP 2									CP 2									
CP 3								CP 3									CP 3									
CP 4								CP 4									CP 4									
CP 5								CP 5									CP 5									
CP 6								CP 6									CP 6									
CP 7								CP 7									CP 7									
CP 8								CP 8									CP 8									
CP 9								CP 9									CP 9									
A																	RCL 1									
B																	RCL 2									
C																										
D																										
E																										
F																										

LAST POWER FAILURE REGISTER

HOURL = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOURL = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD

E	FLAGS								F	FLAGS							
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
FUNCTION								FUNCTION									
0								CODE 4									
1								CODE 5									
2								C-RECALL									
3								D-RECALL									
4								EXCLUSIVE									
5								2 PED			X						
6								6 PED				X					
7								4 PED					X				
8								8 PED						X			
9																	
A								OLA ON									
B								OLB ON									
C								OLC ON									
D								OLD ON									
E																	
F																	

**TIME OF DAY ACTIVITY TABLE**

7+EVENT+HR+MIN+ACT+"E"+ON/OFF+DOW LTS		ON/	S	M	T	W	T	F	S	
HR	MIN	ACT	OFF	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

**CONTROL PLAN TIME OF DAY**

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0	07	00	2	A		2	3	4	5	6
1	09	00	1	A		2	3	4	5	6
2	16	00	3	A		2	3	4	5	6
3	18	00	E	A		2	3	4	5	6
4	08	00	4	A	1					7
5	17	00	E		1					7
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

**CONTROL PLAN TIME OF DAY**

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

**ACTIVITY CODE**

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN
- 8 ENERGIZE AUX OUTPUT-YELLOW
- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION

F+C+E+I+2+3+E+B+ E+PHASES or TYPE+EVENT NO.		PHASES		PHASES		PHASES	
		C	D	E	F		
0	I1	1	5,6	J1	5	5,6	
1	I2U	2	5,6	J2U	6	5,6	
2	I2L	2	5,6	J2L	6	5,6	
3	I3U	2	5,6	J3U	6	5,6	
4	I3L	2	5	J3L	6	5,6	
5	I4	2	7,8	J4	6	7,8	5,6
6	I5	3	5,6	J5	7	5,6	
7	I6U	4	5,6	J6U	8	5,6	
8	I6L	4	5,6	J6L	8	5,6	
9	I7U	4	5,6	J7U	8	5,6	
A	I7L	4	5	J7L	8	5	
B	I8	4	7,8	J8	8	7,8	
C	I9U	1	5,6	J9U	5	5,6	
D	I9L	3	5,6	J9L	7	5,6	

**DETECTOR TYPE**

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

DETECTOR SETTINGS						
I FILE		J FILE				
DELAY	CARRYOVER	DELAY	CARRYOVER			
I1	D10	D30	J1	D20	D40	2.5
I2U	D11	D31	J2U	D21	D41	
I2L	D12	D32	J2L	D22	D42	
I3U	D13	D33	J3U	D23	D43	2.5
I3L	D14	D34	J3L	D24	D44	2.5
I4	D15	D35	J4	D25	D45	
I5	D16	D36	J5	D26	D46	
I6U	D17	D37	J6U	D27	D47	
I6L	D18	D38	J6L	D28	D48	
I7U	D19	D39	J7U	D29	D49	
I7L	D1A	D3A	J7L	D2A	D4A	
I8	D1B	D3B	J8	D2B	D4B	
I9U	D1C	D3C	J9U	D2C	D4C	
I9L	D1D	D3D	J9L	D2D	D4D	

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F							
	1	2	3	4	5	6	7	8		CLK RST	E	FLAGS	1	2	3	4	5	6	7
0 WALK	1	1	1	1	1	1	1	1			PERMIT	1	2	3	4	5	6	7	8
1 DONT WALK	1	1	1	1	1	1	1	1			RED LOCK								
2 MIN GREEN	1	11	1	5	5	13	1	1			YEL LOCK								
3 TYPE 3 DET	0	0	0	0	0	0	0	0			V RECALL		2						
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			P RECALL								
5 PASSAGE	0.9	2.0	0.9	1.0	2.0	2.0	0.9	0.9			PEB PHASES								
6 MAX GAP	0.9	2.0	0.9	1.0	2.0	2.0	0.9	0.9			RT OLA								
7 MIN GAP	0.9	2.0	0.9	1.0	2.0	2.0	0.9	0.9			RT OLB								
8 MAX EXT	9	35	9	20	30	35	9	9			DBL ENTRY								
9 MAX 2											MAX 2 PHASES								
A MAX 3											LAG PHASES								
B											RED REST								
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			REST-IN-WALK								
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0			MAX 3 PHASES								
E YELLOW	3.0	5.2	3.0	4.1	3.7	5.2	3.0	3.0			YEL START UP		2						
F RED	1.0	2.0	1.0	1.0	1.0	2.0	1.0	1.0			FIRST PHASE			4					
PED XING FT												1	2	3	4	5	6	7	8
BIKE XING FT		60																	
						103													

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	0
FOE	
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FD0 TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

C05 FLASH TYPE	1
CC2 DOWNLOAD	1



D	FLAGS								E	MIN	RCL	FLAGS								F	PED	RCL	FLAGS														
	1	2	3	4	5	6	7	8				1	2	3	4	5	6	7	8				1	2	3	4	5	6	7								
MAX	1	2	3	4	5	6	7	8																													
RCL																																					
1 CP 1									CP 1																		CP 1										
2 CP 2									CP 2																	CP 2											
3 CP 3									CP 3																	CP 3											
4 CP 4									CP 4																	CP 4											
5 CP 5									CP 5																	CP 5											
6 CP 6									CP 6																	CP 6											
7 CP 7									CP 7																	CP 7											
8 CP 8									CP 8																	CP 8											
9 CP 9									CP 9																	CP 9											
A																									RCL 1												
B																									RCL 2												
C																																					
D																																					
E																																					
F																																					

E	FLAGS								F	FUNCTION	FLAGS																								
	1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8																	
FUNCTION																																			
0										CODE 4																									
1										CODE 5																									
2										C-RECALL																									
3										D-RECALL																									
4										EXCLUSIVE																									
5										2 PED																									
6										6 PED																									
7										4 PED																									
8										8 PED																									
9																																			
A										OLA ON																									
B										OLB ON																									
C										OLC ON																									
D										OLD ON																									
E																																			
F																																			

LAST POWER FAILURE REGISTER

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E  
 RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F  
 D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD

TIME OF DAY ACTIVITY TABLE

7+EVENT+HR+MIN+ACT+"E"+ON/OFF+DOW LTS		ON/ OFF	S	M	T	W	T	F	S	
HR	MIN	ACT	OFF	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

ACTIVITY CODE

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN

CO9 = 0 or 1

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0	07	00	1	A	2	3	4	5	6	
1	09	00	E		2	3	4	5	6	
2	15	00	2	A	2	3	4	5	6	
3	18	30	E		2	3	4	5	6	
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

- 8 ENERGIZE AUX OUTPUT-YELLOW

- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION

CO9 = 2

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

		PHASES		PHASES		PHASES		TYPE+EVENT NO.	
		C	D	E		F			
0	I1	1	5,6	J1	5	5,6			
1	I2U	2	5,6	J2U	6	5,6			
2	I2L	2	5,6	J2L	6	5,6			
3	I3U	2	5,6	J3U	6	5,6			
4	I3L	2	5	J3L	6	5			
5	I4	2	7,8	J4	6	7,8			
6	I5	3	5,6	J5	7	5,6			
7	I6U	4	5,6	J6U	8	5,6			
8	I6L	4	5,6	J6L	8	5,6			
9	I7U	4	5,6	J7U	8	5,6			
A	I7L	4	5	J7L	8	5			
B	I8	4	7,8	J8	8	7,8			
C	I9U	1	5,6	J9U	5	5,6			
D	I9L	3	5,6	J9L	7	5,6			

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

- DETECTOR TYPE
- 1 RED LOCK
  - 2 YELLOW LOCK
  - 5 EXTENSION
  - 6 COUNT
  - 7 CALLING
  - 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS			
I FILE		J FILE	
DELAY	CARRIOVER	DELAY	CARRIOVER
I1	D10	D30	D40
I2U	D11	D31	D41
I2L	D12	D32	D42
I3U	D13	D33	D43
I3L	D14	D34	D44
I4	D15	D35	D45
I5	D16	D36	D46
I6U	D17	D37	D47
I6L	D18	D38	D48
I7U	D19	D39	D49
I7L	D1A	D3A	D4A
I8	D1B	D3B	D4B
I9U	D1C	D3C	D4C
I9L	D1D	D3D	D4D

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F							
	1	2	3	4	5	6	7	8		CLK RST	E	FLAGS	1	2	3	4	5	6	7
0 WALK	1	1	1	1	1	1	1	1				1	2	3	4	5	6	7	8
1 DONT WALK	1	1	1	1	1	1	1	1	CLK RST	0	PERMIT	1	2	3	4	5	6	7	8
2 MIN GREEN	5	14	1	5	5	12	1	1											
3 TYPE 3 DET	0	0	0	0	0	0	0	0					2						
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0											
5 PASSAGE	2.0	2.0	0.9	1.0	1.0	2.0	0.9	0.9											
6 MAX GAP	2.0	2.0	0.9	1.0	1.0	2.0	0.9	0.9											
7 MIN GAP	2.0	2.0	0.9	1.0	1.0	2.0	0.9	0.9											
8 MAX EXT	20	30	9	20	30	30	9	9											
9 MAX 2	35																		
A MAX 3																			
B																			
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0											
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0											
E YELLOW	3.7	5.2	3.0	4.1	3.7	5.2	3.0	3.0					2						
F RED	1.0	2.0	1.0	1.0	1.0	2.0	1.0	1.0											
PED XING FT																			
BIKE XING FT		104																	

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	0
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

	CONTROL PLANS									Y-COORD		LAG PHASE		FLAGS								
	1	2	3	4	5	6	7	8	9	C	D	E	F	1	2	3	4	5	6	7	8	
0	CYCLE LENGTH	110	110										LAG FZ FREE		2		4		6		8	0
1	FZ1 GRN FCTR	20	40									GAPOUT CP1	LAG FZ CP 1		2		4		6		8	1
2												GAPOUT CP2	LAG FZ CP 2		2		4		6		8	2
3	FZ3 GRN FCTR	0	0									GAPOUT CP3	LAG FZ CP 3									3
4	FZ4 GRN FCTR	25	25									GAPOUT CP4	LAG FZ CP 4									4
5	FZ5 GRN FCTR	10	10									GAPOUT CP5	LAG FZ CP 5									5
6												GAPOUT CP6	LAG FZ CP 6									6
7	FZ7 GRN FCTR	0	0									GAPOUT CP7	LAG FZ CP 7									7
8	FZ8 GRN FCTR	0	0									GAPOUT CP8	LAG FZ CP 8									8
9	MULTI CYCLE	0	0									GAPOUT CP9	LAG FZ CP 9									9
A	OFFSET A	90	55									OFFSET	LAG C COORD									A
B	OFFSET B												LAG D COORD									B
C	OFFSET C												COORD FAZES		2				6			C
D	FZ 3 EXT																					D
E	FZ 7 EXT																					E
F	OFFSET INTRPT																					F

FEATURE

C01 MANUAL CP  
 C02 MASTER CP  
 C03 CURRENT CP  
 C04 LAST CP  
 C07 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

SYSTEM MASTER:  
 RTE 52 EB RAMP

1	2	3	4	5	6	7	8
OFF	ON						

1	2	3	4	5	6	7	8
OFF	ON						

COO = 1

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER



TIME OF DAY ACTIVITY TABLE

7+EVENT+HR+MIN+ACT+"E"+ON/OFF+DOW LTS		ON/OFF	S	M	T	W	T	F	S
HR	MIN	ACT	1	2	3	4	5	6	7
0	15	00	X		2	3	4	5	6
1	18	30			2	3	4	5	6
2									
3									
4									
5									
6									
7									
8									
9									
A									
B									
C									
D									
E									
F									

ACTIVITY CODE

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN

CO9 = 0 of 1

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S
HR	MIN	CP	OS	1	2	3	4	5
0	07	00	1	A		2	3	4
1	09	00	E			2	3	4
2	15	00	2	A		2	3	4
3	18	30	E			2	3	4
4								
5								
6								
7								
8								
9								
A								
B								
C								
D								
E								
F								

- 8 ENERGIZE AUX OUTPUT-YELLOW

- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION

CO9 = 2

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S
HR	MIN	CP	OS	1	2	3	4	5
0								
1								
2								
3								
4								
5								
6								
7								
8								
9								
A								
B								
C								
D								
E								
F								

F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.						
	PHASES		TYPE	PHASES		TYPE
	C	D		E	F	
0	I1	1	5,6	J1	5	5,6
1	I2U	2	5,6	J2U	6	5,6
2	I2L	2	5,6	J2L	6	5,6
3	I3U	2	5,6	J3U	6	5,6
4	I3L	2	5	J3L	6	5,6
5	I4	2	7,8	J4	6	7,8
6	I5	3	5,6	J5	7	5,6
7	I6U	4	5,6	J6U	8	5,6
8	I6L	4	5,6	J6L	8	5,6
9	I7U	4	5,6	J7U	8	5,6
A	I7L	4	5	J7L	8	5
B	I8	4	7,8	J8	8	7,8
C	I9U	1	5,6	J9U	5	5,6
D	I9L	3	5,6	J9L	7	5,6

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

DETECTOR SETTINGS							
I FILE				J FILE			
DELAY	CARRYOVER	DELAY	CARRYOVER	DELAY	CARRYOVER	DELAY	CARRYOVER
I1	D10	D30	J1	D20	D40		
I2U	D11	D31	J2U	D21	D41	2.0	
I2L	D12	D32	J2L	D22	D42	2.0	
I3U	D13	D33	J3U	D23	D43	1.0	
I3L	D14	D34	J3L	D24	D44	1.0	
I4	D15	D35	J4	D25	D45		
I5	D16	D36	J5	D26	D46		
I6U	D17	D37	J6U	D27	D47		
I6L	D18	D38	J6L	D28	D48		
I7U	D19	D39	J7U	D29	D49		
I7L	D1A	D3A	J7L	D2A	D4A		
I8	D1B	D3B	J8	D2B	D4B		
I9U	D1C	D3C	J9U	D2C	D4C	15.0	
I9L	D1D	D3D	J9L	D2D	D4D	15.0	

# INTERSECTION: CONVOY St @ COPLEY PARK PI

223 Program

Group Assignment:  
Field Master Assignment:

N/S Street Name: CONVOY St  
E/W Street Name: COPLEY PARK PI

Last Database Change:  
System Ref. Number:

Row	Phase #	CONVOY St							
		1	2	3	4	5	6	7	8
0	Ped Walk		↑		↔	↔	↓	↔	
1	Ped FDW				↔	↔	↓	↔	
2	Min Green		10		6	6	10		
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn		5.3		2.0	2.0	5.7		
6	Max Gap		5.3		2.0	2.0	5.7		
7	Min Gap		0.2		2.0	2.0	0.2		
8	Max Limit		60		40	40	60		
D	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.6				0.5		
E	Yellow		3.9		3.4	3.4	3.9		
F	Red Clear		1.0		1.0	1.0	1.0		
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

F + E + Row

	F	Row
Permit	2_4567_	0
Red Lock		1
Yellow Lock		2
Min Recall	2_6_	3
Ped Recall		4
Peds (View)	4_6_	5
Rest In Walk		6
Red Rest		7
Dbl Entry	4_7_	8
Max Recall		9
Soft Recall		A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	2_6_	E
1st Phases	4_	F

Phase Functions <F Page>

F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O
Start / Revert Times		
Drop Number	1	C + 0 + 0
Zone Number	1	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	12	C + 0 + 3
QuicNet Channel	DIGI 34:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	2_4_6_	0

Lag Phases <C Page>

Overlap Timing

Row	9	C	D	0
Overlap A	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap B				
Overlap C				
Overlap D				

<F Page>

F + COLOR +

<D Page>

D + 0 + OVERLAP

Downtime Flash 255 (minutes)

Downtime Before Auto Manual Flash

F + 0 + 8

Disable Ports 234

Disable Communication Ports

D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: WXW

Approved By: EFF

Drawing Number: 27576-10-D

Timing Implemented On: 5/17/2005

Handwritten marks: a circle with 'A' and a circle with 'N'.

INTERSECTION: CONVOY St @ COPLEY PARK PI

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	5
B	Overlap B - Phases	7
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	1

TOD Function

<D Page>

7 + ROW

D + F + ROW

Configuration

<E Page>

E + F + ROW

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

- Assign 5 Outputs
- 1 = Right Turn Overlap
  - 2 = TOD Outputs
  - 3 = EV Beacon - Steady
  - 4 = EV Beacon - Flashing
  - 5 = Special Event Outputs
  - 6 = Phase 3 & 7 Ped
  - 7 = Advanced Warning Sign
  - 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	4
6	Overlap B - Green Omit	6
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4
C	EV-C Phases	6
D	EV-D Phases	
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

- Extra 1 Flags
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Remote Download
  - 6 = Special Event
  - 7 = Pretimed Operation
  - 8 = Split Ring Operation

- IC Select Flags
- 1 =
  - 2 = Modem
  - 3 = 7-Wire Slave
  - 4 = Flash / Free
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity  D+B+0

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

Configuration

E + E + ROW

For access, set F + 9 + E = 1

# INTERSECTION: CONVOY St @ COPLEY PARK PI

223 Program

Row	1 Delay	3 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row
A
B
C
D
E
F

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12 -- -- -- --	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- -- 21 22 23 24	5678
-- -- -- -- -- -- -- --	1234
-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	2 Delay	4 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Row
0
1
2
3
4
5
6
7
8

Detector #	0
System Det. # 1	0
System Det. # 2	0
System Det. # 3	0
System Det. # 4	0
System Det. # 5	0
System Det. # 6	0
System Det. # 7	0
System Det. # 8	0

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

Detector Delay & Carryover <D Page>

D + X (across) + ROW

# INTERSECTION: CONVOY St @ COPLEY PARK PI

223 Program

Coordination Timing By: WXH  
Implemented On: 5/17/2005

Row	Column # ---->	Plan Name ---->	Plan												
			1	2	3	4	5	6	7	8	9				
0		Cycle Length	100	100	90										
1		Phase 1 - ForceOff													
2		Phase 2 - ForceOff	0	0											
3		Phase 3 - ForceOff													
4		Phase 4 - ForceOff	38	38	53										
5		Phase 5 - ForceOff	54	54	15										
6		Phase 6 - ForceOff	0	0											
7		Phase 7 - ForceOff													
8		Phase 8 - ForceOff													
9		Ring Offset													
A		Offset A	10	10	37										
B		Offset B		5											
C		Offset C													
D		Permissive	10	10	9										
E		Hold Release	255	255											
F		Ped Shift	0	0											

Coordination <C Page>  
C + Plan + ROW

FOR OBSERVATION ONLY

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Row	Time	Plan	Offset	Day of Week
0	09 : 00	2 A	A	23456
1	11 : 30	2	BA	23456
2	13 14 : 00 30	2 A	A	23456
3	18 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag
Plan 2	2 6	2	Plan 2 - Lag
Plan 3		3	Plan 3 - Lag
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases  
C + E + FUNCTION #

Lag Phases <C Page>  
C + F + FUNCTION #

Transition Type 0

TBC Transition  
C + D + D

Transition Type  
0 = Shortway  
Non-zero = Lengthen

**INTERSECTION: KEARNY VILLA RD/RUFFIN RD & KEAR VILLA RD/WAXIE WAY**

223 Program

Group Assignment:  
Field Master Assignment:

N/S Street Name: KEARNY VILLA RD, J RD  
E/W Street Name: KEARNY VILLA RD/WAXIE WAY

Last Database Change  
System Ref. Number

Row	Phase #	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk		7		7		7		
1	Ped FDW		14		23		15		
2	Min Green	4	10	4	7	4	10		
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	3.4	2.0	3.2	2.0	2.9		
6	Max Gap	2.0	3.4	2.0	3.2	2.0	2.9		
7	Min Gap	2.0	0.2	2.0	0.2	2.0	0.2		
8	Max Limit	30	60	40	40	30	60		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1		0.1		0.1		
D	Every		0.9		1.0		1.1		
E	Yellow	3.4	4.8	3.9	4.4 4.2	3.4	5.7 5.5		
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0		

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

F + E + Row

	F	Row
Permit	123456	0
Red Lock		1
Yellow Lock		2
Min Recall		3
Ped Recall		4
Peds (View)	2 4 6	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall	2 6	A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	2 6	E
1st Phases	3	F

Phase Functions <F Page>

F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0

Start / Revert Times		
Drop Number		C + 0 + 0
Zone Number		C + 0 + 1
Area Number		C + 0 + 2
Area Address		C + 0 + 3
QuicNet Channel		(QuicNet)

Communication Addresses		
C + F + 0	F	Row
Free Lag	2 4 6	0

Lag Phases <C Page>

Overlap Timing

Row	9	C	D	0
Overlap A	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap B				
Overlap C				
Overlap D				

<F Page>  
F + COLOR +

<D Page>  
D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		

F + 0 + 8

Disable Ports	234
Disable Communication Ports	

D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: LEM  
Approved By: EFF

Drawing Number:  
Timing Implemented On: 3/6/12

4

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	<u>  2  </u>
6	Ped 6P	<u>    6    </u>
7	Ped 4P	<u>    4    </u>
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

TOD Function

7 + ROW

<D Page>

D + F + ROW

Configuration

E + F + ROW

<E Page>

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	<u>  2  5  </u>
B	EV-B Phases	<u>    4    </u>
C	EV-C Phases	<u>  1  6  </u>
D	EV-D Phases	<u>    3    </u>
E	Extra 1 Config. Bits	<u>  1  34  </u>
F	IC Select (Interconnect)	<u>    2    </u>

Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Pretimed Operation  
 8 = Split Ring Operation

IC Select Flags  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

Assign 5 Outputs

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity	<b>0</b>	D+B+0
----------------	----------	-------

**Dial-Up Telephone Communications**

(If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

**TERSECTION: KEARNY VILLA RD/RUFFIN RD & KEARNY VILLA RD/WAXIE WAY 223 Program**

Row	1	3
0	Delay	Carry-over
1		1.8
2		1.8
3		
4		
5		
6		
7		1.8
8		1.8
9		
A	10.0	
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	2	4
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Row	0
0	Detector #
1	System Det. # 1
2	System Det. # 2
3	System Det. # 3
4	System Det. # 4
5	System Det. # 5
6	System Det. # 6
7	System Det. # 7
8	System Det. # 8

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

Detector Delay & Carryover <D Page>

D + X (across) + ROW

# INTERSECTION: KEARNY VILLA RD/RUFFIN RD & KEARNY VILLA RD/WAXIE WAY

223 Program

Coordination Timing By: LEM  
Implemented On: 2/27/2012

Row	Plan Name	Plan								
		1	2	3	4	5	6	7	8	9
0	Cycle Length	110								
1	Phase 1 - ForceOff	65								
2	Phase 2 - ForceOff	0								
3	Phase 3 - ForceOff	15								
4	Phase 4 - ForceOff	49								
5	Phase 5 - ForceOff	65								
6	Phase 6 - ForceOff	0								
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	0								
B	Offset B									
C	Offset C									
D	Permissive	5								
E	Hold Release	255								
F	Ped Shift	0								

Coordination  
C + Plan + ROW <C Page>

**FOR OBSERVATION ONLY**

Master Plan C + A + 2  
Current Plan C + A + 3  
Next Plan C + A + 4  
T.O.D. Plan C + A + 5  
Master Cycle C + A + 0  
Ring A Cycle C + B + 0  
Ring B Cycle C + D + 0  
Min Cycle C + A + E  
Max Cycle C + B + E

Row	Time	Plan	Offset	Day of Week
0	07 : 00	1	A	23456
1	17 : 00	E	A	23456
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag
Plan 2		2	Plan 2 - Lag
Plan 3		3	Plan 3 - Lag
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases  
C + E + FUNCTION #

Lag Phases <C Page>  
C + F + FUNCTION #

Transition Type	0
TBC Transition	
C + D + D	

Transition Type  
0 = Shortway  
Non-zero = Lengthen

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

**8: Ruffin Road and Chesapeake Drive**  
**Signal Timing Sheet Not Available**

**INTERSECTION: CONVOY St @ CONVOY Ct**

Group Assignment:  
Field Master Assignment:

N/S Street Name: CONVOY St  
E/W Street Name: CONVOY Ct

223 gram

Last Database Change:  
System Ref. Number:

Row	Phase #	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk		7	7	7		7		
1	Ped FDW		16	24	26		14		
2	Min Green	4	10	4	4	4	10		
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	4.1	2.0	2.0	2.0	4.1		
6	Max Gap	2.0	4.1	2.0	2.0	2.0	4.1		
7	Min Gap	2.0	0.2	2.0	2.0	2.0	0.2		
8	Max Limit	30	60	40	40	30	60		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.8				0.8		
E	Yellow	3.4	3.9	3.9	3.9	3.4	3.9		
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0		
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing  
F + E + Row

	F	Row
Permit	123456	0
Red Lock		1
Yellow Lock		2
Min Recall		3
Ped Recall		4
Peds (View)	234_6	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall	2_6	A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	2_6	E
1st Phases	3	F

Phase Functions <F Page>  
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0
Start / Revert Times		
Drop Number	2	C + 0 + 0
Zone Number	2	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	8	C + 0 + 3
QuicNet Channel	DIGI:34	(QuicNet)

Communication Addresses

C + F + 0	F	Row
Free Lag	2_4_6	0
Lag Phases <C Page>		

Overlap Timing

	9	C	D	0
Row	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap A	A			
Overlap B	B			
Overlap C	C			
Overlap D	D			

<F Page>  
F + COLOR +

<D Page>  
D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		
F + 0 + 8		

Disable Ports	234
Disable Communication Ports	
D + D + 9	

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: wxw  
Approved By: EFF

Drawing Number: 23235-D  
Timing Implemented On: 5/17/05

(N)

INTERSECTION: CONVOY St @ CONVOY Ct

Row	Time	Function	Day of Week	Column F
0				Phases/Bits
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**T.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	<u>  2  </u>
6	Ped 6P	<u>    6    </u>
7	Ped 4P	<u>    4    </u>
8	Ped 8P	<u>    3    </u>
9	Yellow Flash Phases	
A	Overlap A - Phases	<u>   45   </u>
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	<u>  1  </u>

TOD Function

7 + ROW

<D Page>  
D + F + ROW

Configuration  
E + F + ROW

<E Page>

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

- Assign 5 Outputs
- 1 = Right Turn Overlap
  - 2 = TOD Outputs
  - 3 = EV Beacon - Steady
  - 4 = EV Beacon - Flashing
  - 5 = Special Event Outputs
  - 6 = Phase 3 & 7 Ped
  - 7 = Advanced Warning Sign
  - 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	<u>    4    </u>
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	<u>  2  5  </u>
B	EV-B Phases	
C	EV-C Phases	<u>  1  6  </u>
D	EV-D Phases	
E	Extra 1 Config. Bits	<u>  1  345  </u>
F	IC Select (Interconnect)	<u>    2    </u>

- Extra 1 Flags
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Remote Download
  - 6 = Special Event
  - 7 = Prelimed Operation
  - 8 = Split Ring Operation

- IC Select Flags
- 1 =
  - 2 = Modem
  - 3 = 7-Wire Slave
  - 4 = Flash / Free
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity  D+B+0

**Dial-Up Telephone Communications**  
(If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

For access, set F + 9 + E = 1

Configuration  
E + E + ROW

Row	1 Delay	3 Carry-over
0		
1		1.8
2		
3	10.0	
4		
5		
6		
7		
8		
9		
A	10.0	
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	0 Detector #
0	
1	System Det. # 1 0
2	System Det. # 2 0
3	System Det. # 3 0
4	System Det. # 4 0
5	System Det. # 5 0
6	System Det. # 6 0
7	System Det. # 7 0
8	System Det. # 8 0

System Detectors <D Page>

Row	2 Delay	4 Carry-over
0		
1		1.8
2		
3	10.0	
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

# INTERSECTION: CONVOY St @ CONVOY Ct

223 Program

Coordination Timing By: WXH  
Implemented On: 5/17/05

Row	Plan Name	Plan								
		1	2	3	4	5	6	7	8	9
0	Cycle Length	100	90							
1	Phase 1 - ForceOff	79	51							
2	Phase 2 - ForceOff	0	0							
3	Phase 3 - ForceOff	33	20							
4	Phase 4 - ForceOff	65	40							
5	Phase 5 - ForceOff	79	61							
6	Phase 6 - ForceOff	0	0							
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	5	41							
B	Offset B									
C	Offset C									
D	Permissive	10	9							
E	Hold Release	255	255							
F	Ped Shift	0	0							

Coordination <C Page>  
C + Plan + ROW

**FOR OBSERVATION ONLY**

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Row	Time	Plan	Offset	Day of Week
0	09 : 00	1	A	23456
1	11 : 30	2	A	23456
2	14 : 00	1	A	23456
3	18 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag 2 4 6
Plan 2	2 6	2	Plan 2 - Lag 2 4 6
Plan 3		3	Plan 3 - Lag
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases  
C + E + FUNCTION #

Lag Phases <C Page>  
C + F + FUNCTION #

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

Transition Type	0
TBC Transition	
C + D + D	

Transition Type  
0 = Shortway  
Non-zero = Lengthen

# SECTION: HAZARD WY & RUFFIN RD

# 233 Program

Group Assignment:  
Field Master Assignment:  
System Reference Number:

N/S Str: Ruffin Rd  
E/W Street: Hazard Wy

Last Database Change:

Timing sheets by: REJ  
Approved by: BC  
Timing implemented on: 1/5/2010

Row	Phase							
	1	2	3	4	5	6	7	8
0	Ped Walk	7	7	7	7	7	7	7
1	Ped FDW	10	19	19	19	19	19	19
2	Min-Green	4	10	7	4	10		
3	Type 3 Disconnect							
4	Added per Vehicle							
5	Veh Extension	2.0	5.7	2.0	2.0	5.7		
6	Max Gap	2.0	5.7	2.0	2.0	5.7		
7	Min Gap	2.0	0.2	2.0	2.0	0.2		
8	Max Limit	30	60	40	30	60		
9	Max Limit 2							
A	Adv. / Delay Walk							
B	PE Min Ped FDW	3.4	4.3	3.9	3.4	4.3		
C	Cond Serv Check	1.0	1.0	1.0	1.0	1.0		
D	Reduce Every	0.5	0.5	0.5	0.5	0.5		
E	Yellow Change							
F	Red Clear							

Current Calculated Cycle Length: C/0 + B + F

Phase Timing - Bank 1 <F/1+Phase+Row>

Phase	A	B	C	D
Phase 1				
Phase 2				
Phase 3				
Phase 4				
Phase 5				
Phase 6				
Phase 7				
Phase 8				
Max Initial				
Alternate Walk				
Alternate FDW				
Alternate Initial				
Alternate Extension				

Alternate Timing <F/1+Column+Phase>

Free Lag 2 4 6 <C/1+F+0>

How to Set Page Access Code:  
F/1 - C + 0 + F = 1

Row	Permit	Red Lock	Yellow Lock	Min Recall	Ped Recall	View Set Peds	Rest In Walk	Red Rest	Double Entry	Max Recall	Soft Recall	Max 2	Cond. Service	Man Cntrl Calls	Yellow Start	First Phases
0																
1																
2																
3																
4																
5																
6																
7																
8																
9																
A																
B																
C																
D																
E																
F																

Preempt Timing <F/1+E+Row> Phase Functions <F/1+F+Row>

Drop Number	18	<C/0+0+0>
Zone Number	18	<C/0+0+1>
Area Number	5	<C/0+0+2>
Area Address	117	<C/0+0+3>
QuickNet Channel	COM37	(QuickNet)

Communication Addresses

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>

Start / Revert Times

Notes:

(Outputs specified in Assignable Outputs at E/127+A+E & F)

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

Exclusive Ped Phase

Manual Plan	14	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Manual Selection

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

INTERSECTION: HAZARD WY & RUFFIN RD

Row	0	1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8									
9									
A									
B									
C									
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap

Overlap Assignments <E/29+Column+Row>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Fast Green Flash Phase															
1	Green Flash Phases															
2	Flashing Walk Phases															
3	Guaranteed Passage															
4	Simultaneous Gap Term	12345678														
5	Sequential Timing															
6	Advance Walk Phases															
7	Delay Walk Phases															
8	External Recall															
9	Start-up Overlap Green															
A	Max Extension															
B	Inhibit Ped Reserve															
C	Semi-Actuated															
D	Start-up Overlap Yellow															
E	Start-up Vehicle Calls	12345678														
F	Start-up Ped Calls	12345678														

Specials <F/2+F+Row>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Exclusive Phases															
1	RR-1 Clear Phases															
2	RR-2 Clear Phases															
3	RR-2 Limited Service															
4	Prot / Perm Phases															
5	Flash to PE Circuits															
6	Flash Entry Phases															
7	Disable Yellow Range															
8	Disable Ovp Yel Range															
9	Overlap Yellow Flash															
A	EV-A Phases	2	5													
B	EV-B Phases		4													
C	EV-C Phases	1	6													
D	EV-D Phases		3													
E	Extra 1 Config. Bits	1	345													
F	IC Select (Interconnect)		2													

Configuration <E/125+E+Row>

- Extra 1 Flags**
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Extended Status
  - 6 = International Ped
  - 7 = Flash - Clear Outputs
  - 8 = Split Ring
- IC Select Flags**
- 1 =
  - 2 = Modem
  - 3 = 7-Wire Slave
  - 4 = Flash / Free
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Ext. Permit 1 Phases															
1	Ext. Permit 2 Phases															
2	Exclusive Ped Assign															
3	Preempt Non-Lock	12345678														
4	Ped for 2P Output		2													
5	Ped for 6P Output						6									
6	Ped for 4P Output							4								
7	Ped for 8P Output															
8	Yellow Flash Phases															
9	Low Priority A Phases															
A	Low Priority B Phases															
B	Low Priority C Phases															
C	Low Priority D Phases															
D	Restricted Phases															
E	Extra 2 Config. Bits															
F	Configuration															

Configuration <E/125+F+Row>

- Extra 2 Flags**
- 1 = AWB During Initial
  - 2 = LMU Installed
  - 3 = Disable Min Walk
  - 4 = QuickNet/4 System
  - 5 = Ignore P/P on EV
  - 6 =
  - 7 = Reserved
  - 8 =
- Flash to PE & PE Non-Lock**
- 1 = EV A
  - 2 = EV B
  - 3 = EV C
  - 4 = EV D
  - 5 = RR 1
  - 6 = RR 2
  - 7 = SE 1
  - 8 = SE 2

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	EV-A															
1	EV-B															
2	EV-C															
3	EV-D															
4	RR-1 *															
5	RR-2 *															
6	SE-1															
7	SE-2															
8																
9																
A																
B																
C																
D																
E																
F																

Preemption Priority

(\* RR-1 is always Highest, and RR-2 is always Second Highest )

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Phase 1															
1	Phase 2															
2	Phase 3															
3	Phase 4															
4	Phase 5															
5	Phase 6															
6	Phase 7															
7	Phase 8															
8																
9																
A																
B																
C																
D																
E																
F																

Coordination Transition Minimums

Begin Month	Begin Week	End Month	End Week
0	0	0	0
<C/5+2+A>	<C/5+2+B>	<C/5+2+C>	<C/5+2+D>

Daylight Savings Time

Daylight Savings Date: If set to all zeros, standard dates will be used.

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Row	0	1	2	3	1	2	3	Carry-Over
Detector Name								
C1 Pin Number	39	45 7	2	123 8	Delay		1.8	
212U	40	45 7	6	123 8			1.8	
6J2U	41	45 7	4	123 8	12.0			
416U	42	45 7	8	123 8				
212L	43	45 7	2	123 8			1.8	
6J2L	44	45 7	6	123 8			1.8	
	45	45 7	4	123 8				
	46	45 7	8	123 8				
	47	67	2	123 8				
	48	67	6	123 8				
	49	67	4	123 8				
	50	67	8	123 8				
	55	45 7	5	123 8				
	56	45 7	1	123 8				
	57	45 7	7	123 8				
	58	45 7	3	123 8				

Program Type:

Row	0	1	2	3	4	5	6	7	8
Walk									
Don't Walk									
Phase Green									
Phase Yellow									
Phase Red									
Overlap Green									
Overlap Yellow									
Overlap Red									

Redirect Phase Outputs <E/127+Column+Row>

Cabinet Type	0
--------------	---

<E/125+D+0>

Enable Redirection  
(Enable Redirection = 30)

Max OFF (minutes)	5
Max ON (minutes)	60

<D/0+0+1>  
<D/0+0+2>

Detector Failure Monitor

Row	0	1	2	3	4	5	6	7	Carry-Over
Detector Name									
C1 Pin Number	59	45 7	5	123 8	Delay				
59	60	45 7	1	123 8					
	61	45 7	7	123 8					
	62	45 7	3	123 8					
	63	45 7	2	123 8					
	64	45 7	6	123 8					
	65	45 7	4	123 8					
	66	45 7	8	123 8					
	67	2	2	123 8					
	68	2	6	123 8					
	69	2	4	123 8					
	70	2	8	123 8					
	76	45 7	2	123 8					
	77	45 7	6	123 8					
	78	45 7	4	123 8					
	79	45 7	8	123 8					

Detector Assignments <E/126+Column+Row>

Detector Attributes

- 1 = Full Time Delay
- 2 = Ped Call
- 3 =
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

Det. Assignments

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Row	0	1	2	3	4	5	6	7
Output Port 1								
Output Port 2								
Output Port 3								
Output Port 4								
Output Port 5								
Output Port 6								
Output Port 7								

Dimming <E/125+D+Row>

Row	A	B	C	D	E	F
DELAY-A	1					
DELAY-B	1					
DELAY-C	0					
DELAY-D	0					
DELAY-E	0					
DELAY-F	0					

<D/0+B+Row> (seconds)

Delay Logic Times

Omit Alarm	
------------	--

<C/5+F+0>

Disable Alarm Reporting

Time	0
------	---

<C/5+C+0>

Redial Time (minutes)

(View Redial Timer at E/2+D+6)

Dial-Back Telephone Number <C/5+D+Row>

Row	D
Number of Digits	
1 st Digit	
2 ed Digit	
3 ed Digit	
4 th Digit	
5 th Digit	
6 th Digit	
7 th Digit	
8 th Digit	
9 th Digit	
10 th Digit	
11 th Digit	
12 th Digit	
13 th Digit	
14 th Digit	
15 th Digit	

I.O.D. Functions:

- 0 =
- 1 = Red Lock
- 2 = Yellow Lock
- 3 = Veh Min Recall
- 4 = Ped Recall
- 5 =
- 6 = Rest In Walk
- 7 = Red Rest
- 8 = Double Entry
- 9 = Veh Max Recall
- A = Veh Soft Recall
- B = Maximum 2
- C = Conditional Service
- D = Free Lag Phases
- E = Bit 1 - Local Override
- Bit 4 - Disable Detector
- OFF Monitor
- Bit 7 - Detector Count Monitor
- Bit 8 - Real Time Split Monitor
- F = Output Bits 1 thru 8

Plan Select:

- 1 thru 9 = Coordination Plan 1 thru 9
- 14 or E = Free
- 15 or F = Flash

Month Select:

- 1 = January
- 2 = February
- 3 = March
- 4 = April
- 5 = May
- 6 = June
- 7 = July
- 8 = August
- 9 = September
- A = October
- B = November
- C = December

Cycle Timer:

- Master: C/0 + A + 0
- Ring A: C/0 + B + 0
- Ring B: C/0 + D + 0

Interval Timer:

- Ring A: F/0 + A + Interval
- Row
- Ring B: F/0 + B + Interval
- Row

- Master Plan: C/0 + A + 2
- Current Plan: C/0 + A + 3
- TOD Plan: C/0 + A + 5

Row	Time	Plan	Offset	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Events <9/1.1+Row>  
(Bank 1)

Row	Day	Year	Month	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Dates <8/1.1+Row>  
(Bank 1)

Row	Phases/Bits
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

<E/27+4+Row>

Time	Plan	Day of Week	Holiday Type

TOD Coordination <9/0.1+Row> TOD Function  
(Bank 1)

Time	Plan	Day of Week	Holiday Type

TOD Coordination <9/0.2+Row> Holiday TOD Function  
(Bank 2)

Row	Time	Plan	Offset	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Events <9/1.2+Row>  
(Bank 2)

Row	Day	Year	Month	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Dates <8/1.2+Row>  
(Bank 2)

Row	Phases/Bits
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

<E/28+Row>

Time	Plan	Day of Week	Holiday Type

TOD Coordination <9/0.2+Row> Holiday TOD Function  
(Bank 2)

Time	Plan	Day of Week	Holiday Type

TOD Coordination <9/0.2+Row> Holiday TOD Function  
(Bank 2)

Coord Extra

1 = Programmed WALK Time for Sync Phases  
2 = Always Terminate Sync Phase Peds

Row	Plan Name ---->	Plan																		
		1	2	3	4	5	6	7	8	9										
0	Cycle Length																			
1	Phase 1 - ForceOff																			
2	Phase 2 - ForceOff																			
3	Phase 3 - ForceOff																			
4	Phase 4 - ForceOff																			
5	Phase 5 - ForceOff																			
6	Phase 6 - ForceOff																			
7	Phase 7 - ForceOff																			
8	Phase 8 - ForceOff																			
9	Ring Offset																			
A	Offset 1																			
B	Offset 2																			
C	Offset 3																			
D	Perm 1 - End																			
E	Hold Release																			
F	Zone Offset																			

Coordination - Timing Plans <C/1+Plan+Row>

Row	Plan Name	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
0	Ped Adjustment																	
1	Perm 2 - Start																	
2	Perm 2 - End																	
3	Perm 3 - Start																	
4	Perm 3 - End																	
5	Reservice Time																	
6	Reservice Phases																	
7																		
8	Pretimed Phases																	
9	Max Recall																	
A	Perm 1 Veh Phase																	
B	Perm 1 Ped Phase																	
C	Perm 2 Veh Phase																	
D	Perm 2 Ped Phase																	
E	Perm 3 Veh Phase																	
F	Perm 3 Ped Phase																	

Coordination - Parameters <C/2+Plan+Row>

Row	Plan Name	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Plan 1 - Sync																
1	Plan 2 - Sync																
2	Plan 3 - Sync																
3	Plan 4 - Sync																
4	Plan 5 - Sync																
5	Plan 6 - Sync																
6	Plan 7 - Sync																
7	Plan 8 - Sync																
8	Plan 9 - Sync																
9	NEMA Sync																
A	NEMA Hold																
B																	
C																	
D																	
E																	
F	Coord Extra																

Sync Phases <C/1+E+Row>

Row	Plan Name	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Free Lag																
1	Plan 1 - Lag																
2	Plan 2 - Lag																
3	Plan 3 - Lag																
4	Plan 4 - Lag																
5	Plan 5 - Lag																
6	Plan 6 - Lag																
7	Plan 7 - Lag																
8	Plan 8 - Lag																
9	Plan 9 - Lag																
A	External Lag																
B																	
C																	
D																	
E																	
F																	

Lag Phases <C/1+F+Row>

Coordination Timing By:

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Spec. Funct. 1	NOT-3	Max 2	Prelimed	Set Monday	Dial 2 (7-Wire)	Sim Term
1	Spec. Funct. 2	NOT-4	System Det 1	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A
2	Spec. Funct. 3	OR-4 (a)	System Det 2	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B
3	Spec. Funct. 4	OR-4 (b)	System Det 3	Plan 3	Dimming	Offset 2 (7-Wire)	EV-C
4	NAND-3 (a)	OR-5 (a)	System Det 4	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D
5	NAND-3 (b)	OR-5 (b)	System Det 5	Plan 5	Stop Time	Free (7-Wire)	RR-1
6	NAND-4 (a)	OR-6 (a)	System Det 6	Plan 6	Flash Sense	Flash (7-Wire)	RR-2
7	NAND-4 (b)	OR-6 (b)	System Det 7	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1
8	OR-7 (a)	Fig 3 Diamond	System Det 8	Plan 8	Man. Advance	NOT-1	Spec. Event 2
9	OR-7 (b)	Fig 4 Diamond	Max Inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag
A	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)
B	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)
C	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)
D	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)
E	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)
F	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)

<E/126+Column+Row>

Assignable Inputs

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)
1	Phase ON - 2	Sp Evt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)
2	Phase ON - 3	Sp Evt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)
3	Phase ON - 4	Sp Evt Out 3	Fig 3 Diamond	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)
4	Phase ON - 5	Sp Evt Out 4	Fig 4 Diamond	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)
5	Phase ON - 6	Sp Evt Out 5		Plan 5	AND-2	TOD Out 6	Free (7-Wire)
6	Phase ON - 7	Sp Evt Out 6		Plan 6	AND-3	TOD Out 7	Flash (7-Wire)
7	Phase ON - 8	Sp Evt Out 7		Plan 7	NOT-2	TOD Out 8	Preempt
8	Ph. Check - 1	Sp Evt Out 8	NOT-3	Plan 8	EV-A	Adv. Warm - 1	Low Priority A
9	Ph. Check - 2	Detector Fail	NOT-4	Plan 9	EV-B	Adv. Warm - 2	Low Priority B
A	Ph. Check - 3	Spec. Funct. 1	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C
B	Ph. Check - 4	Spec. Funct. 2	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D
C	Ph. Check - 5	Central Control	OR-6	NAND-3	RR-1	DELAY-C	
D	Ph. Check - 6	Excl. Ped DW	AND-4	NAND-4	RR-2	DELAY-D	
E	Ph. Check - 7	Excl. Ped WK	NAND-1	OR-7	Spec. Event 1	DELAY-E	
F	Ph. Check - 8		NAND-2	OR-8	Spec. Event 2	DELAY-F	

<E/127+Column+Row>

Assignable Outputs

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F								
	1	2	3	4	5	6	7	8		CLK RST	E	FLAGS	1	2	3	4	5	6	7	8
0 WALK	1	7	1	1	1	1	1	1												
1 DONT WALK	1	10	1	1	1	1	1	1												
2 MIN GREEN	1	10	1	5	1	1	1	1												
3 TYPE 3 DET	0	0	0	0	0	0	0	0												
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0												
5 PASSAGE	0.9	2.0	0.9	2.0	0.9	0.9	0.9	0.9												
6 MAX GAP	0.9	2.0	0.9	2.0	0.9	0.9	0.9	0.9												
7 MIN GAP	0.9	2.0	0.9	2.0	0.9	0.9	0.9	0.9												
8 MAX EXT	9	55	9	35	9	9	9	9												
9 MAX 2																				
A MAX 3																				
B																				
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0												
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0												
E YELLOW	3.0	5.0	3.0	4.1	3.0	3.0	3.0	3.0												
F RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0												
PED XING FT		35																		
PED XING FT		42																		

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	0
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

EDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

COS FLASH TYPE	1
CC2 DOWNLOAD	1





TIME OF DAY ACTIVITY TABLE

7+EVENT+HR+MIN+ACT+"E"+ON/OFF+DOW LTS		ON/	S	M	T	W	T	F	S	
HR	MIN	ACT	OFF	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

ACTIVITY CODE

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN

9 PAGE C09 = 0 OR 1

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

- 8 ENERGIZE AUX OUTPUT-YELLOW
- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION

9 PAGE C09 = 2

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.		PHASES		PHASES		PHASES	
		C	D		E		F
0	I1	1	5,6	J1	5	5,6	
1	I2U	2	5,6	J2U	6	5,6	
2	I2L	2	5,6	J2L	6	5,6	
3	I3U	2	5,6	J3U	6	5,6	
4	I3L	2	5,6	J3L	6	5,6	
5	I4	2	7,8	J4	6	7,8	
6	I5	3	5,6	J5	7	5,6	
7	I6U	4	5,6	J6U	8	5,6	
8	I6L	4	5,6	J6L	8	5,6	
9	I7U	4	5,6	J7U	8	5,6	
A	I7L	4	5	J7L	8	5	
B	I8	4	7,8	J8	8	7,8	
C	I9U	1	5,6	J9U	5	5,6	
D	I9L	3	5,6	J9L	7	5,6	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

- DETECTOR TYPE**
- 1 RED LOCK
  - 2 YELLOW LOCK
  - 5 EXTENSION
  - 6 COUNT
  - 7 CALLING
  - 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS			
I FILE		J FILE	
DELAY	CARRYOVER	DELAY	CARRYOVER
I1	D10	D30	D40
I2U	D11	D31	D41
I2L	D12	D32	D42
I3U	D13	D33	D43
I3L	D14	D34	D44
I4	D15	D35	D45
I5	D16	D36	D46
I6U	D17	D37	D47
I6L	D18	D38	D48
I7U	D19	D39	D49
I7L	D1A	D3A	D4A
I8	D1B	D3B	D4B
I9U	D1C	D3C	D4C
I9L	D1D	D3D	D4D

# INTERSECTION: CLAIREMONT MESA BI @ SHAWLINE St.

Group Assignment:  
Field Master Assignment:

N/S Street Name: SHAWLINE St  
E/W Street Name: CLAIREMONT MESA BI

Last Database Change:  
System Ref. Number:

223 Program

Row	Phase #	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW		7	4				7	
2	Min Green	4	10	8	6	4	10		
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	4.6	2.0	2.0	2.0	4.5		
6	Max Gap	2.0	4.6	2.0	2.0	2.0	4.5		
7	Min Gap	2.0	0.2	2.0	2.0	2.0	0.2		
8	Max Limit	30	60	40	40	30	60		
9	Max Limit 2	30	50	30	20	30	50		
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.7				0.7		
E	Yellow	3.4	4.2	3.9	3.9	3.4	4.6		
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0		
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing  
F + E + Row

	F	Row
Permit	123456	0
Red Lock		1
Yellow Lock		2
Min Recall		3
Ped Recall		4
Peds (View)	23 6	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall	2 6	A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	2 6	E
1st Phases	4	F

Phase Functions <F Page>  
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0
Start / Revert Times		
Drop Number	1	C + 0 + 0
Zone Number	1	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	14	C + 0 + 3
QuicNet Channel	DIGI:33	(QuicNet)

Communication Addresses

C + F + 0	F	Row
Free Lag	2 4 6	0

Lag Phases <C Page>

Overlap Timing

	9	C	D	0
Green Clear				
Yellow Change				
Red Clear				
Load-Switch #				

<F Page>  
F + COLOR +

<D Page>  
D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		

F + 0 + 8

Disable Ports	234
Disable Communication Ports	

D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: WXW  
Approved By: EFF

Drawing Number: 30311-9-D  
Timing Implemented On: 11/15/2004

16  
14

INTERSECTION: CLAIEMONT MESA BI @ SHAWLINE St

Row	Time	Function	Day of Week	Column F
0	11 : 45	B	23456	123456
1	14 : 00	B	1234567	
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**T.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	
8	Ped 8P	3
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

TOD Function

7 + ROW

<D Page>  
 D + F + ROW

Configuration

E + F + ROW

<E Page>

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

Assign 5 Outputs

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	
C	EV-C Phases	1 6
D	EV-D Phases	3
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Extra 1 Flags

- 1 = TBC Type 1
- 2 = NEMA Ext. Coord
- 3 = Auto Daylight Savings
- 4 = EV Advance
- 5 = Remote Download
- 6 = Special Event
- 7 = Pretimed Operation
- 8 = Split Ring Operation

IC Select Flags

- 1 =
- 2 = Modem
- 3 = 7-Wire Slave
- 4 = Flash / Free
- 5 =
- 6 = Simplex Master
- 7 = 7-Wire Master
- 8 = Offset Interrupter

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity  0 D+B+0

**Dial-Up Telephone Communications**  
 (If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

Row	1 Delay	3 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8	10.0	
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	0 Detector #
0	
1	System Det. # 1
2	System Det. # 2
3	System Det. # 3
4	System Det. # 4
5	System Det. # 5
6	System Det. # 6
7	System Det. # 7
8	System Det. # 8

System Detectors <D Page>

Row	2 Delay	4 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

# INTERSECTION: CLAIREMONT MESA BL @ SHAWLINE St

223 Program

Coordination Timing By: WXW  
Implemented On: 11/15/2004

Row	Plan Name	Plan								
		1	2	3	4	5	6	7	8	9
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	80	114	89						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff	52	67	60						
4	Phase 4 - ForceOff	14	89	25						
5	Phase 5 - ForceOff	80	31	89						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	93	122	29						
B	Offset B									
C	Offset C									
D	Permissive	11	13	14						
E	Hold Release	255	255	255						
F	Ped Shift	0	0	0						

**FOR OBSERVATION ONLY**

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Coordination  
C + Plan + ROW <C Page>

Row	Time	Plan	Offset	Day of Week
0	06 : 00	1	A	23456
1	10 : 00	2	A	23456
2	11 : 45	E	A	23456
3	14 : 00	3	A	23456
4	19 : 00	E	A	1234567
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag
Plan 2	2 6	2	Plan 2 - Lag
Plan 3	2 6	3	Plan 3 - Lag
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases  
C + E + FUNCTION #

Lag Phases <C Page>  
C + F + FUNCTION #

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

Transition Type	0
TBC Transition	C + D + D
Transition Type	
0 = Shortway	
Non-zero = Lengthen	

# INTERSECTION: CLAIREMONT MESA BI @ RUFFNER R

Group Assignment:  
Field Master Assignment:

N/S Street Name: RUFFNER Rd  
E/W Street Name: CLAIREMONT MESA BI

Last Database Change:  
System Ref. Number:

223 gram

Row	Column # ---> Phase # ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW		7		7		7		
2	Min Green	4	10		4	4	15		
3	Type 3 Limit						10		
4	Add/Veh		1.2				1.2		
5	Veh Extn	2.0	3.9		2.0	2.0	3.9		
6	Max Gap	2.0	3.9		2.0	2.0	3.9		
7	Min Gap	2.0	2.0		2.0	2.0	3.9		
8	Max Limit	30	60		45	30	60		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.8				0.8		
E	Yellow	3.4	4.2		3.9	3.4	4.2		
F	Red Clear	1.0	1.0		1.0	1.0	1.0		
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

F + E + Row

	F
Permit	12_456__
Red Lock	
Yellow Lock	
Min Recall	_2_6_
Ped Recall	
Peds (View)	_2_4_6_
Rest In Walk	
Red Rest	
Dbt Entry	
Max Recall	
Soft Recall	
Max 2	
Cond Serv	
Ped Lock	12345678
Yellow Start	_2_6_
1st Phases	_4_

Phase Functions <F Page>

F + F + Row

Max Initial	21
Red Revert	5.0
All Red Start	0.0

F + 0 + E

F + 0 + F

F + C + 0

Start / Revert Times

Drop Number	2
Zone Number	2
Area Number	5
Area Address	13
QuicNet Channel	DIGI:33

C + 0 + 0

C + 0 + 1

C + 0 + 2

C + 0 + 3

(QuicNet)

Communication Addresses

C + F + 0	F	Row
Free Lag	2_4_6	0

Lag Phases

<C Page>

Overlap Timing

	9	C	D	0
Row	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap A	A			
Overlap B	B			
Overlap C	C			
Overlap D	D			

<F Page>

F + COLOR +

<D Page>

D + 0 + OVERLAP

Downtime Flash	255	(minutes)
----------------	-----	-----------

Downtime Before Auto Manual Flash

F + 0 + 8

Disable Ports	234
---------------	-----

Disable Communication Ports

D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: wxw  
Approved By: EFF

Drawing Number:  
Timing Implemented On: 11/15/2004



INTERSECTION: CLAIREMONT MESA BI @ RUFFNER Rd

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

TOD Function

7 + ROW

<D Page>  
D + F + ROW

Configuration  
E + F + ROW

<E Page>

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	
C	EV-C Phases	1 6
D	EV-D Phases	
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Pretimed Operation  
 8 = Split Ring Operation

IC Select Flags  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Configuration  
E + E + ROW

For access, set F + 9 + E = 1

Time and Date

8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Program Information  
 C + C + 0 = program  
 C + C + F = version

Remote Download  
 C + 0 + 4 = 1-255  
 w/ E + E + E bit 5 on

Disable Parity  D+B+0

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Row	1 Delay	3 Carry-over
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row
A
B
C
D
E
F

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12 -- -- --	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- -- 21 22 23 24	5678
-- -- -- -- -- -- -- --	1234
-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row
0
1
2
3
4
5
6
7
8

0	Detector #
System Det. # 1	0
System Det. # 2	0
System Det. # 3	0
System Det. # 4	0
System Det. # 5	0
System Det. # 6	0
System Det. # 7	0
System Det. # 8	0

System Detectors <D Page>

Row	2 Delay	4 Carry-over
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

# INTERSECTION: CLAIREMONT MESA BL @ RUFFNER Rd

223 Program

Coordination Timing By: WXW  
Implemented On: 11/15/2004

Row	Plan Name	Plan								
		1	2	3	4	5	6	7	8	9
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	105	31	118						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff	78	86	96						
5	Phase 5 - ForceOff	32	122	46						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	103	54	106						
B	Offset B									
C	Offset C									
D	Permissive	11	13	14						
E	Hold Release	255	255	255						
F	Ped Shift	0	0	0						

**FOR OBSERVATION ONLY**

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Coordination <C Page>  
C + Plan + ROW

SE4  
04-24-06

Row	Time	Plan	Offset	Day of Week
0	06 : 00 30	1	A	23456
1	10 : 00	2	A	23456
2	14 : 00	3	A	23456
3	19 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag
Plan 2	2 6	2	Plan 2 - Lag
Plan 3	2 6	3	Plan 3 - Lag
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases  
C + E + FUNCTION #

Lag Phases <C Page>  
C + F + FUNCTION #

Transition Type	0
TBC Transition	
C + D + D	

Transition Type  
0 = Shortway  
Non-zero = Lengthen

# INTERSECTION: CLAIREMONT MESA BI @ CONVOY St

Group Assignment:  
Field Master Assignment:

N/S Street Name: CONVOY St  
E/W Street Name: CLAIREMONT MESA BI

223 gram

Last Database Change:  
System Ref. Number:

Row	Phase #	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk	↙	↘	↖	↓	↗	↔	↘	↗
1	Ped FDW		5		5		5		5
2	Min Green	4	10	4	10	4	10	4	10
3	Type 3 Limit								
4	Add/Veh		1.2				1.2		
5	Veh Extn	2.0	9.2	2.0	4.5	2.0	7.0	2.0	6.7
6	Max Gap	2.0	9.2	2.0	4.5	2.0	7.0	2.0	6.7
7	Min Gap	2.0	2.0	2.0	0.2	2.0	2.0	2.0	6.7
8	Max Limit	30	50	30	50	30	50	30	50
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1		0.1		0.1		0.1
D	Every		0.3		0.7		0.4		0.5
E	Yellow	3.4	4.4	3.4	3.9	3.4	4.2	3.4	4.1
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing  
F + E + Row

	F
Permit	12345678
Red Lock	
Yellow Lock	
Min Recall	2 6
Ped Recall	
Peds (View)	2 4 6 8
Rest In Walk	
Red Rest	
Dbl Entry	
Max Recall	
Soft Recall	
Max 2	
Cond Serv	
Ped Lock	12345678
Yellow Start	2 6
1st Phases	4 8

Phase Functions <F Page>  
F + F + Row

Max Initial	44	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0
Start / Revert Times		
Drop Number	3	C + 0 + 0
Zone Number	3	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	9	C + 0 + 3
QuicNet Channel	DIGI:33	(QuicNet)

Communication Addresses		
C + F + 0	F	Row
Free Lag	2 4 6 8	0

Lag Phases <C Page>  
\* \* PED WALKING RATE = 3.5 FPS

Overlap Timing				
	9	C	D	0
Green Clear				
Yellow Change				
Red Clear				
Load-Switch #				

<F Page>  
F + COLOR +

<D Page>  
D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		

F + 0 + 8

Disable Ports	234
Disable Communication Ports	

D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection  
Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: WXW  
Approved By: EFF  
Drawing Number: 27576-9-D  
Timing Implemented On: 11/15/2004

4/9/15

1618

INTERSECTION: CLAIREMONT MESA BI @ CONVOY St

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	<u>2</u>
6	Ped 6P	<u>6</u>
7	Ped 4P	<u>4</u>
8	Ped 8P	<u>8</u>
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	<u>1-5</u>
F	Assign 5 Outputs	

TOD Function  
7 + ROW

<D Page>  
D + F + ROW

Configuration  
E + F + ROW

<E Page>

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	<u>2 5</u>
B	EV-B Phases	<u>4 7</u>
C	EV-C Phases	<u>1 6</u>
D	EV-D Phases	<u>3 8</u>
E	Extra 1 Config. Bits	<u>1 345</u>
F	IC Select (Interconnect)	<u>2</u>

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

- IC Select Flags  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Configuration  
E + E + ROW

For access, set F + 9 + E = 1

- Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Time and Date

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Program Information

- C + C + 0 = program  
 C + C + F = version

Remote Download

- C + 0 + 4 = 1-255  
 w/ E + E + E bit 5 on

Disable Parity	0	D+B+0
----------------	---	-------

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Row	1	3
0	Delay	Carry-over
1		
2		
3		
4		
5		
6		
7		
8		1.8
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row
A
B
C
D
E
F

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12 -- -- --	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- -- 21 22 23 24	5678
-- -- -- -- -- -- --	1234
-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row
0
1
2
3
4
5
6
7
8

0	Detector #
	System Det. # 1
	System Det. # 2
	System Det. # 3
	System Det. # 4
	System Det. # 5
	System Det. # 6
	System Det. # 7
	System Det. # 8
	0
	0
	0
	0
	0
	0
	0
	0
	0
	0

System Detectors <D Page>

Row	2	4
0	Delay	Carry-over
1		
2		
3		
4		
5		
6		
7		
8		1.8
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	
Max OFF (min)	5 <sup>D+A+E</sup>
Detector Failure Monitor	60 <sup>D+A+F</sup>

Phase Number	
Time Before Yellow	0 <sup>F+C+1</sup>
Advance Warning Beacon - Sign 1	0.0 <sup>F+C+3</sup>

Phase Number	
Time Before Yellow	0 <sup>F+D+1</sup>
Advance Warning Beacon - Sign 2	0.0 <sup>F+D+3</sup>

Long Failure	
Short Failure	0.5 <sup>F+0+6</sup>
Power Cycle Correction (Default = 0.5)	0.5 <sup>F+0+7</sup>

INTERSECTION: CLAIREMONT MESA BL @ CONVOY ST

Coordination Timing By: WXW  
Implemented On: 11/15/2004

4/9/15

Row	Column # --> Plan Name -->	Plan								
		1	2	3	4	5	6	7	8	9
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	95 72	113 121	100						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff	38 18	46 53	32						
4	Phase 4 - ForceOff	73 50	87 95	74						
5	Phase 5 - ForceOff	23 73	22 21	98						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff	38 18	41 40	19						
8	Phase 8 - ForceOff	73 50	87 95	74						
9	Ring Offset									
A	Offset A	52 38	57 105	84						
B	Offset B									
C	Offset C									
D	Permissive	23 11	22 13	21 14						
E	Hold Release	255	255	255						
F	Ped Shift	0	0	0						

FOR OBSERVATION ONLY

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Coordination  
C + Plan + ROW  
<C Page>

Row	Time	Plan	Offset	Day of Week
0	06 : 00	1	A	23456
1	10 : 00	2	A	23456
2	14 : 00	3	A	23456
3	19 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag
Plan 2	2 6	2	Plan 2 - Lag
Plan 3	2 6	3	Plan 3 - Lag
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases  
C + E + FUNCTION #

Lag Phases  
C + F + FUNCTION #

Transition Type  
TBC Transition  
C + D + D

Transition Type  
0 = Shortway  
Non-zero = Lengthen

# INTERSECTION: CLAIREMONT MESA BI @ MERCURY St

223 Program

Group Assignment:  
Field Master Assignment:

N/S Street Name: MERCURY St  
E/W Street Name: CLAIREMONT MESA BI

Last Database Change:  
System Ref. Number:

Row	Phase # ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW		7		7		7		
2	Min Green	6	10		6	6	10		
3	Type 3 Limit								
4	Add/Veh		1.6				1.6		
5	Veh Extn	2.0	6.2		2.0	2.0	6.2		
6	Max Gap	2.0	6.2		2.0	2.0	6.2		
7	Min Gap	2.0	2.0		2.0	2.0	2.0		
8	Max Limit	30	60		45	30	60		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.5				0.5		
E	Yellow	3.4	4.3		3.9	3.4	4.4		
F	Red Clear	1.0	1.0		1.0	1.0	1.0		
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

F + E + Row

	F	Row
Permit	12_456__	0
Red Lock		1
Yellow Lock		2
Min Recall	_2_6__	3
Ped Recall		4
Peds (View)	_2_4_6__	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	_2_6__	E
1st Phases	_4__	F

Phase Functions <F Page>

F + F + Row

Max Initial	32	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O
Start / Revert Times		
Drop Number	4	C + 0 + 0
Zone Number	4	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	7	C + 0 + 3
QuicNet Channel	DIGI:33	(QuicNet)

Communication Addresses		
C + F + O	F	Row
Free Lag	1_4_6__	0

Lag Phases <C Page>

### Overlap Timing

	9	C	D	0
Row	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap A	A			
Overlap B	B			
Overlap C	C			
Overlap D	D			

<F Page>

F + COLOR +

<D Page>

D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		
	F + 0 + 8	

Disable Ports	234
Disable Communication Ports	
	D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection  
Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: WXX

Approved By: EFF

Drawing Number: 16553-2-D

Timing Implemented On: 11/15/2004

17/19

**INTERSECTION: CLAIREMONT MESA BI @ MERCURY St**

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**T.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	<u>2</u>
6	Ped 6P	<u>6</u>
7	Ped 4P	<u>4</u>
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

TOD Function

7 + ROW

<D Page>  
D + F + ROW

Configuration

E + F + ROW

<E Page>

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

Assign 5 Outputs

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	<u>2 5</u>
B	EV-B Phases	
C	EV-C Phases	<u>1 6</u>
D	EV-D Phases	
E	Extra 1 Config. Bits	<u>1 4 5</u>
F	IC Select (Interconnect)	<u>2</u>

Extra 1 Flags

- 1 = TBC Type 1
- 2 = NEMA Ext. Coord
- 3 = Auto Daylight Savings
- 4 = EV Advance
- 5 = Remote Download
- 6 = Special Event
- 7 = Pretimed Operation
- 8 = Split Ring Operation

IC Select Flags

- 1 =
- 2 = Modem
- 3 = 7-Wire Slave
- 4 = Flash / Free
- 5 =
- 6 = Simplex Master
- 7 = 7-Wire Master
- 8 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity	0
----------------	---

D+B+0

**Dial-Up Telephone Communications**

(If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

Configuration

E + E + ROW

For access, set F + 9 + E = 1

# INTERSECTION: CLAIREMONT MESA BI @ MERCURY St

223 Program

Row	1	3
0	Delay	Carry-over
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row
A
B
C
D
E
F

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12 -- -- --	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- -- 21 22 23 24	5678
-- -- -- -- -- -- --	1234
-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	2	4
0	Delay	Carry-over
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Row
0
1
2
3
4
5
6
7
8

0	Detector #
System Det. # 1	0
System Det. # 2	0
System Det. # 3	0
System Det. # 4	0
System Det. # 5	0
System Det. # 6	0
System Det. # 7	0
System Det. # 8	0

System Detectors <D Page>

Max ON (min)		
Max OFF (min)	5	D+A+E
Detector Failure Monitor	60	D+A+F

Phase Number		
Time Before Yellow	0	F+C+1
Advance Warning Beacon - Sign 1	0.0	F+C+3

Phase Number		
Time Before Yellow	0	F+D+1
Advance Warning Beacon - Sign 2	0.0	F+D+3

Long Failure		
Short Failure	0.5	F+0+6
Power Cycle Correction (Default = 0.5)	0.5	F+0+7

Detector Delay & Carryover <D Page>

D + X (across) + ROW

EML  
1/26/10

INTERSECTION: CLAREMONT MESA BL @ MERCURY ST

223 Program

Coordination Timing By: WXW  
Implemented On: 11/15/2004

Column # ---->	Plan Name ---->	Plan								
		1	2	3	4	5	6	7	8	9
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	101	30	<del>88</del> 80						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff	65	81	<del>54</del> 45						
5	Phase 5 - ForceOff	20	105	<del>88</del> 80						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	44	128	<del>40</del> 30						
B	Offset B									
C	Offset C									
D	Permissive	11	13	14						
E	Hold Release	255	255	255						
F	Ped Shift	0	0	0						

FOR OBSERVATION ONLY

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Coordination <C Page>  
C + Plan + ROW

Row	Time	Plan	Offset	Day of Week
0	06 : 00	1	A	23456
1	10 : 00	2	A	23456
2	14 : 00	3	A	23456
3	19 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag 2 45
Plan 2	2 6	2	Plan 2 - Lag 1 4 6
Plan 3	2 6	3	Plan 3 - Lag 2 4 6
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases  
C + E + FUNCTION #

Lag Phases <C Page>  
C + F + FUNCTION #

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

Transition Type	0
TBC Transition	
C + D + D	

Transition Type  
0 = Shortway  
Non-zero = Lengthen

# INTERSECTION: CLAIREMONT MESA BI @ INDUSTRY

Group Assignment:  
Field Master Assignment:

N/S Street Name: INDUSTRY  
EW Street Name: CLAIREMONT MESA BI

223 **ogram**

Last Database Change:  
System Ref. Number:

Row	Phase #	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW		7		7		7		
2	Min Green	4	10		26	4	19		
3	Type 3 Limit				4	4	10		
4	Add/Veh		1.2						
5	Veh Extn	2.0	5.5		3.0	2.0	5.5		
6	Max Gap	2.0	5.5		3.0	2.0	5.5		
7	Min Gap	2.0	2.0		3.0	2.0	5.5		
8	Max Limit	20	60		40	30	60		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.6				0.6		
E	Yellow	3.4	4.2		3.9	3.4	4.5		
F	Red Clear	1.0	1.0		1.0	1.0	1.0		
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

F + E + Row

	F	Row
Permit	12_456__	0
Red Lock		1
Yellow Lock		2
Min Recall	2_6__	3
Ped Recall		4
Peds (View)	2_4_6__	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall	2_6__	A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	2_6__	E
1st Phases	4__	F

Phase Functions <F Page>

F + F + Row

Max Initial	29
Red Revert	5.0
All Red Start	0.0

F + 0 + E  
F + 0 + F  
F + C + 0

Start / Revert Times	
Drop Number	5
Zone Number	5
Area Number	5
Area Address	6
QuicNet Channel	DIGI:33

C + 0 + 0  
C + 0 + 1  
C + 0 + 2  
C + 0 + 3  
(QuicNet)

Communication Addresses

C + F + 0	F	Row
Free Lag	2_4_6	0

Lag Phases <C Page>

Overlap Timing

	9	C	D	0
Green Clear				
Yellow Change				
Red Clear				
Load-Switch #				

<F Page>

F + COLOR +

<D Page>

D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		

F + 0 + 8

Disable Ports	234
Disable Communication Ports	

D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: wxw

Approved By: EFF

Drawing Number:

Timing Implemented On: 1/25/05

18  
20

# INTERSECTION: CLAIREMONT MESA BI @ INDUSTRY

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	<u>  2  </u>
6	Ped 6P	<u>    6    </u>
7	Ped 4P	<u>    4    </u>
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

TOD Function

7 + ROW

<D Page>  
D + F + ROW

Configuration  
E + F + ROW

<E Page>

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

Assign 5 Outputs

- 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	<u>  2  5  </u>
B	EV-B Phases	
C	EV-C Phases	<u>  1  6  </u>
D	EV-D Phases	
E	Extra 1 Config. Bits	<u>  1  345  </u>
F	IC Select (Interconnect)	<u>  2  </u>

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

IC Select Flags

- 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Configuration  
E + E + ROW

For access, set F + 9 + E = 1

Time and Date

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Program Information

- C + C + 0 = program  
 C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255  
 w/E + E + E bit 5 on

Disable Parity	0	D+B+0
----------------	---	-------

**Dial-Up Telephone Communications**  
 (If set to a non-zero value, parity will be disabled)

Row	1 Delay	3 Carry-over
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row
A
B
C
D
E
F

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12 -- -- --	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- -- 21 22 23 24	5678
-- -- -- -- -- -- --	1234
-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row
0
1
2
3
4
5
6
7
8

0	Detector #
System Det. # 1	0
System Det. # 2	0
System Det. # 3	0
System Det. # 4	0
System Det. # 5	0
System Det. # 6	0
System Det. # 7	0
System Det. # 8	0

System Detectors <D Page>

Row	2 Delay	4 Carry-over
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

# INTERSECTION: CLAIREMONT MESA BI @ INDUSTRY

663 CIVILIAN

Coordination Timing By: WXW  
Implemented On: 11/15/2004

Row	Column # ---->	Plan								
		1	2	3	4	5	6	7	8	9
0	Plan Name ---->									
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	21	73	26						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff	61	47	75						
5	Phase 5 - ForceOff	82	71	95						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	7	9	17						
B	Offset B									
C	Offset C									
D	Permissive	11	13	14						
E	Hold Release	255	255	255						
F	Ped Shift	0	0	10						

**FOR OBSERVATION ONLY**

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Coordination <C Page>  
C + Plan + ROW

Row	Time	Plan	Offset	Day of Week
0	06 : 00	1	A	23456
1	10 : 00	2	A	23456
2	14 : 00	3	A	23456
3	19 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

	E	Row	F
		0	Free Lag
Plan 1	2 6	1	Plan 1 - Lag 1 4 6
Plan 2	2 6	2	Plan 2 - Lag 2 4 6
Plan 3	2 6	3	Plan 3 - Lag 1 4 6
Plan 4		4	Plan 4 - Lag
Plan 5		5	Plan 5 - Lag
Plan 6		6	Plan 6 - Lag
Plan 7		7	Plan 7 - Lag
Plan 8		8	Plan 8 - Lag
Plan 9		9	Plan 9 - Lag
Coord Ped*		A	Coord Max *
NEMA Hold		B	Coord Lag *
		C	
		D	
		E	
		F	

Sync Phases <C Page>  
C + E + FUNCTION # Lag Phases C + F + FUNCTION #

Transition Type	0
TBC Transition C + D + D	

Transition Type  
0 = Shortway  
Non-zero = Lengthen

# INTERSECTION: CLAIREMONT MESA BI @ KEARNY ME RD

223 gram

Group Assignment:  
Field Master Assignment:

N/S Street Name: KEARNY MESA Rd  
E/W Street Name: CLAIREMONT MESA BI

Last Database Change:  
System Ref. Number:

Row	Phase #	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk		7		7		7		
1	Ped FDW		19		24		26		
2	Min Green	4	10	4	4	4	10	4	4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	3.7	2.0	2.0	2.0	3.9	2.0	2.0
6	Max Gap	2.0	3.7	2.0	2.0	2.0	3.9	2.0	2.0
7	Min Gap	2.0	0.2	2.0	2.0	2.0	0.2	2.0	2.0
8	Max Limit	30	85	30	40	30	85	30	40
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.8				0.8		
E	Yellow	3.4	4.2	3.4	3.9	3.4	4.8	3.4	4.2
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

Row	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

F + E + Row

Row	F
Permit	12345678
Red Lock	
Yellow Lock	
Min Recall	2-6-
Ped Recall	
Peds (View)	2_4_6
Rest In Walk	
Red Rest	
Dbl Entry	
Max Recall	
Soft Recall	2_6/
Max 2	
Cond Serv	
Ped Lock	12345678
Yellow Start	2_6
1st Phases	4_8

Phase Functions <F Page>

F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	6	C + 0 + 0
Zone Number	6	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	5	C + 0 + 3
QuicNet Channel	DIGI:33	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	2_45_8	0

Lag Phases <C Page>

### Overlap Timing

Row	9	C	D	0
	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap A	A			
Overlap B	B			
Overlap C	C			
Overlap D	D			

<F Page>

F + COLOR +

<D Page>

D + 0 + OVERLAP

Downtime Flash 255 (minutes)

Downtime Before Auto Manual Flash

F + 0 + 8

Disable Ports 234

Disable Communication Ports

D + D + 9

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

### Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: WXW

Approved By: EFF

Drawing Number: 19789-D

Timing Implemented On: 11/15/2004

19  
21

INTERSECTION: CLAIREMONT MESA BI @ KEARNY MESA RD

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	<u>  2  </u>
6	Ped 6P	<u>    6    </u>
7	Ped 4P	<u>    4    </u>
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	<u>  1  8  </u>
B	Overlap B - Phases	<u>    67   </u>
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	<u>  1   </u>

TOD Function

7 + ROW

<D Page>

D + F + ROW

Configuration

<E Page>

E + F + ROW

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

Assign 5 Outputs

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	<u>    8    </u>
6	Overlap B - Green Omit	<u>    6    </u>
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	<u>  2  5  </u>
B	EV-B Phases	
C	EV-C Phases	<u>  1  6  </u>
D	EV-D Phases	<u>    3  8  </u>
E	Extra 1 Config. Bits	<u>  1  345  </u>
F	IC Select (Interconnect)	<u>    2    </u>

Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Pretimed Operation  
 8 = Split Ring Operation

IC Select Flags

- 1 =
- 2 = Modem
- 3 = 7-Wire Slave
- 4 = Flash / Free
- 5 =
- 6 = Simplex Master
- 7 = 7-Wire Master
- 8 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity	<u>  0  </u>	D+B+0
----------------	--------------	-------

Dial-Up Telephone Communications

(If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1-255
- w/ E + E + E bit 5 on

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Row	1 Delay	3 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2 Delay	4 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B	10.0	
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	0 Detector #
0	
1	System Det. # 1
2	System Det. # 2
3	System Det. # 3
4	System Det. # 4
5	System Det. # 5
6	System Det. # 6
7	System Det. # 7
8	System Det. # 8

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

# INTERSECTION: CLAIREMONT MESA BI @ KEARNY MESA RD

223 Program

Coordination Timing By: WXW  
Implemented On: 11/15/2004

**FOR OBSERVATION ONLY**

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Column # ---->	Plan Name ---->	Plan								
		1	2	3	4	5	6	7	8	9
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	24	71	22						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff	42 39	16	50						
4	Phase 4 - ForceOff	74	51	86						
5	Phase 5 - ForceOff	90	69	104						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff	42 39	26	57						
8	Phase 8 - ForceOff	74	51	86						
9	Ring Offset									
A	Offset A	122	0	19						
B	Offset B									
C	Offset C									
D	Permissive	11	13	14						
E	Hold Release	255	255	255						
F	Ped Shift	0	0	0						

Coordination <C Page>  
C + Plan + ROW

Row	Time	Plan	Offset	Day of Week
0	06 : 00	1	A	23456
1	10 : 00	2	A	23456
2	14 : 00	3	A	23456
3	19 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
<9 Key with C+0+9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

E	Row	F
	0	Free Lag
Plan 1	1	Plan 1 - Lag
Plan 2	2	Plan 2 - Lag
Plan 3	3	Plan 3 - Lag
Plan 4	4	Plan 4 - Lag
Plan 5	5	Plan 5 - Lag
Plan 6	6	Plan 6 - Lag
Plan 7	7	Plan 7 - Lag
Plan 8	8	Plan 8 - Lag
Plan 9	9	Plan 9 - Lag
Coord Ped*	A	Coord Max *
NEMA Hold	B	Coord Lag *
	C	
	D	
	E	
	F	

Sync Phases  
C + E + FUNCTION #

Lag Phases <C Page>  
C + F + FUNCTION #

Transition Type	0
TBC Transition	
C + D + D	

Transition Type  
0 = Shortway  
Non-zero = Lengthen

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F										
	1	2	3	4	5	6	7	8		CLK RST	E	FLAGS	1	2	3	4	5	6	7	8		
0 WALK	1	7	1	1	1	7	1	1			EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1 DONT WALK	1	12	1	1	1	12	1	1			RRI CLR	5	RED LOCK									1
2 MIN GREEN	5	20	1	5	1	17	1	1			EVA DLY	0	YEL LOCK									2
3 TYPE 3 DET	0	0	0	0	0	0	0	0			EVA CLR	5	V RECALL		2							3
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			EVB DLY	0	P RECALL									4
5 PASSAGE	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9			EVB CLR	5	PED PHASES		2							5
6 MAX GAP	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9			EVC DLY	0	RT OLA									6
7 MIN GAP	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9			EVC CLR	5	RT OLB									7
8 MAX EXT	35	45	9	45	9	45	9	9			EVD DLY	0	DBL ENTRY									8
9 MAX 2											EVD CLR	5	MAX 2 PHASES									9
A MAX 3											MAX EV	255	LAG PHASES									A
B											RR2 CLR	5	RED REST									B
C REDUCE BY EVERY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			DAY		REST-IN-WALK									C
D YELLOW	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0			DOM		MAX 3 PHASES									D
E RED	3.7	4.5	3.2	4.1	3.2	4.5	3.0	3.0			HR		YEL START UP		2							E
F PED XING FT	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0			MIN		FIRST PHASE			4						F
BIKE XING FT	70					70					SEC				1	2	3	4	5	6	7	8

NOTES :

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	0
FOE	
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FDS PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

	CONTROL PLANS									Y-COORD		LAG PHASE	FLAGS										
	1	2	3	4	5	6	7	8	9	C	D		E	F	1	2	3	4	5	6	7	8	
0 CYCLE LENGTH	110	120	135										LAG FZ FREE	1	2	3	4	5	6	7	8	0	
1 FZ1 GRN FCTR	22	40	25										GAPOUT CP1									1	
2													GAPOUT CP2									2	
3 FZ3 GRN FCTR	0	0	0										GAPOUT CP3									2	
4 FZ4 GRN FCTR	0	0	0										GAPOUT CP4									4	
5 FZ5 GRN FCTR	50	45	25										GAPOUT CP5									6	
6													GAPOUT CP6									8	
7 FZ7 GRN FCTR	0	0	0										GAPOUT CP7									7	
8 FZ8 GRN FCTR	0	0	0										GAPOUT CP8									8	
9 MULTI CYCLE	0	0	0										GAPOUT CP9									9	
A OFFSET A	25	40	100										LAG C COORD									A	
B OFFSET B													LAG D COORD									B	
C OFFSET C													COORD FAZES									C	
D FZ 3 EXT																						D	
E FZ 7 EXT																							E
F OFFSET INTRPT																							F

FEATURE

1	OFF	ON
2	OFF	ON
3	OFF	ON
4	OFF	ON
5	OFF	ON
6	OFF	ON
7	OFF	ON
8	OFF	ON

LOCATION

1	OFF	ON
2	OFF	ON
3	OFF	ON
4	OFF	ON
5	OFF	ON
6	OFF	ON
7	OFF	ON
8	OFF	ON

COO = 2

SYSTEM MASTER:  
 RTE 163SB @ KEARNY VILLA  
 SOB

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP  
 CO4 LAST CP  
 CO7 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

D	FLAGS								E	MIN RCL	FLAGS								F	PED RCL	FLAGS																					
	1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8														
MAX																																										
RCL																																										
0																																										
1																																										
2																																										
3																																										
4																																										
5																																										
6																																										
7																																										
8																																										
9																																										
A																																										
B																																										
C																																										
D																																										
E																																										
F																																										

LAST POWER FAILURE REGISTER

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

E	FUNCTION	FLAGS								F	FUNCTION	FLAGS																													
		1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8																						
0																																									
1																																									
2																																									
3																																									
4																																									
5																																									
6																																									
7																																									
8																																									
9																																									
A																																									
B																																									
C																																									
D																																									
E																																									
F																																									

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION

84 = BAD  
 85 = GOOD

TIME OF DAY ACTIVITY TABLE

7+EVENT+HR+MIN+ACT+"E"+ON/OFF+DOW LTS		ON/	S	M	T	W	T	F	S	
HR	MIN	ACT	OFF	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

ACTIVITY CODE

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN

C09 = 0 or 1

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0	06	00	1	A	2	3	4	5	6	
1	10	00	2	A	2	3	4	5	6	
2	14	00	3	A	2	3	4	5	6	
3	19	00	E		2	3	4	5	6	
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

- 8 ENERGIZE AUX OUTPUT-YELLOW

- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION

C09 = 2

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.									
		PHASES		J1	5	PHASES		5.6	TYPE
		C	D			E	F		
0	I1	1	5.6	J1	5			5.6	
1	I2U	2	5.6	J2U	6			5.6	
2	I2L	2	5.6	J2L	6			5.6	
3	I3U	2	5.6	J3U	6			5.6	
4	I3L	2	5	J3L	6			5	
5	I4	2	7.8	J4	6			7.8	
6	I5	3	5.6	J5	7			5.6	
7	I6U	4	5.6	J6U	8			5.6	
8	I6L	4	5.6	J6L	8			5.6	
9	I7U	4	5.6	J7U	8			5.6	
A	I7L	4	5	J7L	8			5	
B	I8	4	7.8	J8	8			7.8	
C	I9U	1	5.6	J9U	5			5.6	
D	I9L	3	5.6	J9L	7			5.6	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

- DETECTOR TYPE**
- 1 RED LOCK
  - 2 YELLOW LOCK
  - 5 EXTENSION
  - 6 COUNT
  - 7 CALLING
  - 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS									
	I FILE	I FILE			J FILE				
		DELAY	CARRYOVER		DELAY	CARRYOVER			
I1	D10		D30		J1	D20		D40	
I2U	D11		D31		J2U	D21		D41	
I2L	D12		D32		J2L	D22		D42	
I3U	D13		D33		J3U	D23		D43	
I3L	D14		D34		J3L	D24		D44	
I4	D15		D35		J4	D25		D45	
I5	D16		D36		J5	D26		D46	
I6U	D17		D37		J6U	D27		D47	
I6L	D18		D38		J6L	D28		D48	
I7U	D19		D39		J7U	D29		D49	
I7L	D1A		D3A		J7L	D2A		D4A	
I8	D1B		D3B		J8	D2B		D4B	
I9U	D1C		D3C		J9U	D2C		D4C	
I9L	D1D		D3D		J9L	D2D		D4D	

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F								
	1	2	3	4	5	6	7	8		CLK RST	E	FLAGS	1	2	3	4	5	6	7	8
0 WALK	1	7	1	1	1	7	1	1												
1 DONT WALK	1	32	1	1	1	12	1	1												
2 MIN GREEN	5	15	1	1	1	11	1	5												
3 TYPE 3 DET	0	0	0	0	0	0	0	0												
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0												
5 PASSAGE	2.0	2.0	0.9	0.9	0.9	2.0	0.9	2.0												
6 MAX GAP	2.0	2.0	0.9	0.9	0.9	2.0	0.9	2.0												
7 MIN GAP	2.0	2.0	0.9	0.9	0.9	2.0	0.9	2.0												
8 MAX EXT	20	40	9	9	9	40	9	40												
9 MAX 2								60												
A MAX 3																				
B																				
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0												
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0												
E YELLOW	3.7	4.8	3.0	3.0	3.0	4.8	3.0	4.0												
F RED	1.0	2.0	1.0	1.0	1.0	2.0	1.0	1.0												
PED XING FT		113				41														
BIKE XING FT		120				67														

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY



F0C LONG FAILURE	
F0D SHORT FAILURE	0
F0F	5

F0C	3
F0I	3
F02	10
F0A	0.0
F0B	0.0
F0C	0.0
F0D	0.0

F0D TB SELECT	1
F03 PED SELECT	0
F04 7 WIRE	0
F05 PERMISSIVE	0
F08 OS SEEKING	1

C05 FLASH TYPE	1
CC2 DOWNLOAD	1

	CONTROL PLANS									Y-COORD		LAG PHASE			FLAGS							
	1	2	3	4	5	6	7	8	9	C	D	E	F	1	2	3	4	5	6	7	8	
0 CYCLE LENGTH	110	120	135										LAG FZ FREE	2	4	4	6	6	8	8	0	
1 FZ1 GRN FCTR	10	15	15									GAPOUT CP1	LAG FZ CP 1	2	4	4	6	6	8	8	1	
2												GAPOUT CP2	LAG FZ CP 2	2	4	4	6	6	8	8	2	
3 FZ3 GRN FCTR	0	0	0									GAPOUT CP3	LAG FZ CP 3	2	4	4	6	6	8	8	3	
4 FZ4 GRN FCTR	0	0	0									GAPOUT CP4	LAG FZ CP 4								4	
5 FZ5 GRN FCTR	0	0	0									GAPOUT CP5	LAG FZ CP 5								5	
6												GAPOUT CP6	LAG FZ CP 6								6	
7 FZ7 GRN FCTR	0	0	0									GAPOUT CP7	LAG FZ CP 7								7	
8 FZ8 GRN FCTR	55	45	50									GAPOUT CP8	LAG FZ CP 8								8	
9 MULTI CYCLE												GAPOUT CP9	LAG FZ CP 9								9	
A OFFSET A	90	80	100									OFFSET	LAG C COORD								A	
B OFFSET B														LAG D COORD								B
C OFFSET C														COORD FAZES	2							C
D FZ 3 EXT																						D
E FZ 7 EXT																						E
F OFFSET INTRPT																						F

FEATURE

1	OFF	ON
2	OFF	ON
3	OFF	ON
4	OFF	ON
5	OFF	ON
6	OFF	ON
7	OFF	ON
8	OFF	ON

LOCATION

1	OFF	ON
2	OFF	ON
3	OFF	ON
4	OFF	ON
5	OFF	ON
6	OFF	ON
7	OFF	ON
8	OFF	ON

COO =

SYSTEM MASTER:

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP  
 CO4 LAST CP  
 CO7 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

D	FLAGS								E	FLAGS								F	FLAGS									
	MAX	1	2	3	4	5	6	7		8	MIN	1	2	3	4	5	6		7	8	PED	1	2	3	4	5	6	7
0	RCL								RCL										RCL									
1	CP 1								CP 1										CP 1									
2	CP 2								CP 2										CP 2									
3	CP 3								CP 3										CP 3									
4	CP 4								CP 4										CP 4									
5	CP 5								CP 5										CP 5									
6	CP 6								CP 6										CP 6									
7	CP 7								CP 7										CP 7									
8	CP 8								CP 8										CP 8									
9	CP 9								CP 9										CP 9									
A																		RCL 1										
B																		RCL 2										
C																												
D																												
E																												
F																												

E	FLAGS								F	FLAGS								
	FUNCTION	1	2	3	4	5	6	7		8	FUNCTION	1	2	3	4	5	6	7
0									CODE 4									
1									CODE 5									
2									C-RECALL									
3									D-RECALL									
4									EXCLUSIVE									
5									2 PED			2						
6									6 PED					4				
7									4 PED									
8									8 PED									
9																		
A	OLA NOT								OLA ON									
B	OLB NOT								OLB ON									
C	OLC NOT								OLC ON									
D	OLD NOT								OLD ON									
E																		
F																		

LAST POWER FAILURE REGISTER

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E  
 RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F  
 D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD

TIME OF DAY ACTIVITY TABLE

7+EVENT+HR+MIN+ACT+ "E"+ON/OFF+DOW LTS		ON/OFF	S	M	T	W	T	F	S
HR	MIN	ACT							
0	06	01	X		2	3	4	5	6
1	19	01			2	3	4	5	6
2									
3									
4									
5									
6									
7									
8									
9									
A									
B									
C									
D									
E									
F									

ACTIVITY CODE

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S
HR	MIN	CP	OS					
0	06	00	1	A	2	3	4	5
1	10	00	2	A	2	3	4	5
2	14	00	3	A	2	3	4	5
3	19	00	E		2	3	4	5
4								
5								
6								
7								
8								
9								
A								
B								
C								
D								
E								
F								

- 8 ENERGIZE AUX OUTPUT-YELLOW
- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S
HR	MIN	CP	OS					
0								
1								
2								
3								
4								
5								
6								
7								
8								
9								
A								
B								
C								
D								
E								
F								

F+C+F+I+2+3+E+B+ E+PHASES or TYPE+EVENT NO.		PHASES		PHASES		PHASES	
		C	D	E	F		
		TYPE	TYPE	TYPE	TYPE		
0	I1	1	5,6	J1	5	5,6	
1	I2U	2	5,6	J2U	6	5,6	
2	I2L	2	5,6	J2L	6	5,6	
3	I3U	2	5,6	J3U	6	5,6	
4	I3L	2	5,6	J3L	6	5,6	
5	I4	2	7,8	J4	6	7,8	5,6
6	I5	3	5,6	J5	7	5,6	
7	I6U	4	5,6	J6U	8	5,6	
8	I6L	4	5,6	J6L	8	5,6	
9	I7U	4	5,6	J7U	8	5,6	
A	I7L	4	5	J7L	8	5	
B	I8	4	7,8	J8	8	7,8	1
C	I9U	1	5,6	J9U	5	5,6	
D	I9L	3	5,6	J9L	7	5,6	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

- DETECTOR TYPE
- 1 RED LOCK
  - 2 YELLOW LOCK
  - 5 EXTENSION
  - 6 COUNT
  - 7 CALLING
  - 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS					
I FILE		J FILE			
DELAY	CARRYOVER	DELAY	CARRYOVER		
I1	D10	D30	J1	D20	D40
I2U	D11	D31	J2U	D21	D41
I2L	D12	D32	J2L	D22	D42
I3U	D13	D33	J3U	D23	D43
I3L	D14	D34	J3L	D24	D44
I4	D15	D35	J4	D25	D45
I5	D16	D36	J5	D26	D46
I6U	D17	D37	J6U	D27	D47
I6L	D18	D38	J6L	D28	D48
I7U	D19	D39	J7U	D29	D49
I7L	D1A	D3A	J7L	D2A	D4A
I8	D1B	D3B	J8	D2B	D4B
I9U	D1C	D3C	J9U	D2C	D4C
I9L	D1D	D3D	J9L	D2D	D4D

Min Time (seconds)  <F/1+0+8>  
**Min Green Before PE Force Off**

Max Time (minutes)  <F/1+0+9>  
**Max Preempt Time Before Failure**

Min Time (seconds)  <F/1+0+A>  
**Min Time Between Same Preempts**  
 (Does Not Apply To Railroad Preempt)

Low Pri. Channel  <E/125+C+8>  
**Disable Low Priority Channel**

- Low Priority  
 1 = Channel A  
 2 = Channel B  
 3 = Channel C  
 4 = Channel D

Row		
C	Bus Headway	
D	Bus Delay	
E	Max Early Grn	
F	Max Grn Ext.	

**Priority Parameters**  
 <F/1 +A+Row>

Row	Time	Headway	Direction	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Headway Time  
 (minutes)  
 1 thru 9 = 1 thru 9  
 A = 10  
 B = 11  
 C = 12  
 D = 13  
 E = 14  
 F = 15

**Headway Schedule** <C+0+9=2.1>

**Low Priority Preemption (Bus Priority)**

Note: Also see "Time of Day Functions", Function E, Bit 5 (Disable Low Priority)

Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 1

<C+0+E=27>

Notes:  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_

0 <E/27+5+F>  
 Limited Service Interval

Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 2

<C+0+E=28>

Notes:  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_

0 <E/28+5+F>  
 Limited Service Interval



Column Numbers ---->		0	1	2	3	1	3
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0	Ph. 2 I-2 U	39	45 7	2	123		1.8
1	Ph. 6 J-2 U	40	45 7	6	123		1.8
2	Ph. 4 I-6 U	41	45 7	4	123		1.8
3	Ph. 8 J-6 U	42	45 7	8	123		1.8
4		43	45 7	2	123		
5		44	45 7	6	123		
6		45	45 7	4	123		
7		46	45 7	8	123		
8		47	67	2	123		
9		48	67	6	123		
A		49	67	4	123		
B		50	67	8	123		
C		55	45 7	5	123		
D		56	45 7	1	123		
E		57	45 7	7	123		
F		58	45 7	3	123		

Column Numbers ---->		Ped / Phase / Overlap								Row
		1	2	3	4	5	6	7	8	
Walk										0
Don't Walk										1
Phase Green										2
Phase Yellow										3
Phase Red										4
Overlap Green										5
Overlap Yellow										6
Overlap Red										7

Redirect Phase Outputs <C+0+E=127>

Cabinet Type 0 <E/125+D+0>

Enable Redirection  
(Enable Redirection = 30)

Max OFF (minutes) 60 <D/0+0+1>

Max ON (minutes) 5 <D/0+0+2>

Chatter Fail Time 0 <D/0+0+4>

Detector Failure Monitor

	B	Row
One-Shot		8
Ext. Timer		9
DELAY-A		A
DELAY-B		B
DELAY-C		C
DELAY-D		D
DELAY-E		E
DELAY-F		F

Delay Logic Times  
<C+0+D=0> (seconds)

Column Numbers ---->		4	5	6	7	2	4
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0		59	45 7	5	123		
1		60	45 7	1	123		
2		61	45 7	7	123		
3		62	45 7	3	123		
4		63	45 7	2	123		
5		64	45 7	6	123		
6		65	45 7	4	123		
7		66	45 7	8	123		
8		67	2	2	123		
9		68	2	6	123		
A		69	2	4	123		
B		70	2	8	123		
C		76	45 7	2	123		
D		77	45 7	6	123		
E		78	45 7	4	123		
F	Ph. 8 8J-7 L	79	45 7	8	123	10.0	

Detector Attributes

- 1 = Full Time Delay
- 2 = Ped Call
- 3 = Overlap
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

Det. Assignments

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Detector Assignments <C+0+E=126>

<C+0+D=0>

		Phase							
Column Numbers →		1	2	3	4	5	6	7	8
Row	Phase Names →								
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					

Max Initial  
Alternate Walk  
Alternate FDW  
Alternate Initial  
Alternate Extension

Alternate Timing

Transition Type  
0.X = Shortway  
1.X = Lengthen  
X.1 thru X.4 =  
Number of  
cycles when  
lengthing

Transition Type 0.0 <C/5+1+9>  
**TBC Transition**

Hawk Select 0 F/1+0+4  
**Hawk Select** 200 = Mid-Block, 201 = Hawk

Address 0 <C/1+0+6>  
Select Parity 0 <C/1+0+5>  
**AB3418 Comm 2** 0 = No Parity, 1 = Even

Daylight Savings  
Date  
If set to all zeros,  
standard dates  
will be used.

Begin Month 3 <C/5+2+A>  
Begin Week 2 <C/5+2+B>  
End Month 11 <C/5+2+C>  
End Week 1 <C/5+2+D>

**Daylight Savings Time**

		Phase							
Column Numbers →		1	2	3	4	5	6	7	8
Row	Phase Names →								
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					

Max Initial  
Alternate Walk  
Alternate FDW  
Alternate Initial  
Alternate Extension

Alternate Timing

Time B4 Yellow 0.0 <F/1+C+E>  
Phase Number 0 <F/1+C+F>

**Advance Warning Beacon - Sign 1**

Time B4 Yellow 0.0  
Phase Number 0 <F/1+D+F>

**Advance Warning Beacon - Sign 2**

Offset Time 0 <C/5+2+E>  
Max Cycle Time 0 <C/5+2+F>

**Yellow Yield Coordination**

12345678  
Omit Alarm 12345678 <C/5+F+0>

**Local Alarm Disable**

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	One-Shot Timer	Latch 1 Set	NOT-3	Max 2	Pretimed	Set DOW	Dial 2 (7-Wire)	Sim Term	0
1	AND-5 (a)	Latch 1 Reset	NOT-4	Reserved	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A	71
2	AND-5 (b)	Latch 2 Set	OR-4 (a)	Reserved	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B	72
3	AND-6 (a)	Latch 2 Reset	OR-4 (b)	Reserved	Plan 3	Gate Down	Offset 2 (7-Wire)	EV-C	73
4	AND-6 (b)	NAND-3 (a)	OR-5 (a)	Reserved	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D	74
5	Reserved	NAND-3 (b)	OR-5 (b)	Reserved	Plan 5	Stop Time	82 Free (7-Wire)	RR-1	51
6	Reserved	NAND-4 (a)	OR-6 (a)	Reserved	Plan 6	Flash Sense	81 Flash (7-Wire)	RR-2	52
7	Reserved	NAND-4 (b)	OR-6 (b)	Reserved	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1	7
8	Spec. Funct. 1	OR-7 (a)	EXTMR	Reserved	Plan 8	Man. Advance	NOT-1	Spec. Event 2	8
9	Spec. Funct. 2	OR-7 (b)	Reserved	Max Inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag	9
A	Spec. Funct. 3	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)	A
B	Spec. Funct. 4	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)	B
C	Reserved	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)	C
D	Reserved	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)	D
E	Reserved	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)	E
F	Reserved	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)	F

Assignable Inputs

<C+0+E=126>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	Reserved	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)	0
1	Reserved	Phase ON - 2	Sp Evnt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)	1
2	Reserved	Phase ON - 3	Sp Evnt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)	2
3	Reserved	Phase ON - 4	Sp Evnt Out 3	EXTMR	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)	3
4	Reserved	Phase ON - 5	Sp Evnt Out 4	One-Shot Timer	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)	4
5	Reserved	Phase ON - 6	Sp Evnt Out 5	Reserved	Plan 5	AND-2	TOD Out 6	Free (7-Wire)	5
6	Reserved	Phase ON - 7	Sp Evnt Out 6	Latch 1	Plan 6	AND-3	TOD Out 7	Flash (7-Wire)	6
7	Reserved	Phase ON - 8	Sp Evnt Out 7	Latch 2	Plan 7	NOT-2	TOD Out 8	Preempt	7
8	Flh Yell Arrow 1	Ph. Check - 1	Sp Evnt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A	8
9	Green 1	Ph. Check - 2	Coord On	NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B	9
A	Flh Yell Arrow 3	Ph. Check - 3	Detector Fail	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C	A
B	Green 3	Ph. Check - 4	Spec. Funct. 1	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D	B
C	Flh Yell Arrow 5	Ph. Check - 5	Spec. Funct. 2	OR-6	NAND-3	RR-1	DELAY-C	AND-5	C
D	Green 5	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	AND-6	D
E	Flh Yell Arrow 7	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	Reserved	E
F	Green 7	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	Reserved	F

Assignable Outputs

<C+0+E=127>

Column Numbers -->		Plan								
Row	Plan Name -->	1	2	3	4	5	6	7	8	9
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	95	22	116						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff	38	42	59						
4	Phase 4 - ForceOff	80	82	101						
5	Phase 5 - ForceOff	24	112	33						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff	38	39	59						
8	Phase 8 - ForceOff	80	82	101						
9	Ring Offset	0	0	0						
A	Offset 1	14	52	17						
B	Offset 2									
C	Offset 3									
D	Perm 1 - End	11	13	14						
E	Hold Release	255	255	255						
F	Reserved	0	0	0						

Coordination - Bank 1 <C+0+C=1>

0	Ped Adjustment									
1	Perm 2 - Start									
2	Perm 2 - End									
3	Perm 3 - Start									
4	Perm 3 - End									
5	Reservice Time									
6	Reservice Phases									
7										
8	Pretimed Phases									
9	Max Recall									
A	Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
B	Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
C	Perm 2 Veh Phase									
D	Perm 2 Ped Phase									
E	Perm 3 Veh Phase									
F	Perm 3 Ped Phase									

Coordination - Bank 2 <C+0+C=2>

Coord Extra  
 1 = Programmed WALK Time for Sync Phases  
 2 = Always Terminate Sync Phase Peds

Row	E	Row
0		0
1	Plan 1 - Sync <u>2 6</u>	1
2	Plan 2 - Sync <u>2 6</u>	2
3	Plan 3 - Sync <u>2 6</u>	3
4	Plan 4 - Sync	4
5	Plan 5 - Sync	5
6	Plan 6 - Sync	6
7	Plan 7 - Sync	7
8	Plan 8 - Sync	8
9	Plan 9 - Sync	9
A	NEMA Sync	A
B	NEMA Hold	B
C		C
D		D
E		E
F		F

Sync Phases <C+0+C=1>

Row	F	Row
0	Free Lag <u>2 4 6 8</u>	0
1	Plan 1 - Lag <u>2 4 5 8</u>	1
2	Plan 2 - Lag <u>1 4 6 8</u>	2
3	Plan 3 - Lag <u>2 4 5 8</u>	3
4	Plan 4 - Lag	4
5	Plan 5 - Lag	5
6	Plan 6 - Lag	6
7	Plan 7 - Lag	7
8	Plan 8 - Lag	8
9	Plan 9 - Lag	9
A	External Lag	A
B	Lag Hold	B
C		C
D		D
E		E
F		F

Lag Phases <C+0+C=1>

Column Numbers -->		Overlap							
Row	Overlap Name -->	1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8	Overlap Recall								
9	Queue Jump Phase								
A	Queue Jump Time								
B	Minimum Green								
C	Maximum Green								
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments <C+0+E=29>

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = Solid FDW on EV  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring

- Extra 2 Flags**  
 1 = AWB During Initial  
 2 = Reserved  
 3 = Disable Min Walk  
 4 = QuicNet System  
 5 = Ignore P/P on EV  
 6 = Manual Hold in FDW  
 7 = Allow QuicNet PE  
 8 = Flash Grn B4 Yellow

	C	Row
EV-A	0	0
EV-B	0	1
EV-C	0	2
EV-D	0	3
RR-1 *	---	4
RR-2 *	---	5
SE-1	0	6
SE-2	0	7

**Preempt Priority**  
 <C+0+E=125>  
 (\* RR-1 is always Highest, and RR-2 is always Second Highest)

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

Row	Column Numbers -->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4 7
C	EV-C Phases	1 6
D	EV-D Phases	3 8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration <C+0+E=125>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	12345678
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	
Ped for 8P Output	8
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	34

Configuration <C+0+E=125>

	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	12345678
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reservice	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	12345678
Start-up Ped Calls	12345678

Specials <C+0+F=2>

- Flash to PE & PE Non-Lock**  
 1 = EV A 5 = RR 1  
 2 = EV B 6 = RR 2  
 3 = EV C 7 = SE 1  
 4 = EV D 8 = SE 2

- IC Select Flags**  
 1 =  
 2 = Modern  
 3 = 7-Wire Slave  
 4 =  
 5 =  
 6 = Simplex Master  
 7 =  
 8 = Offset Interrupter

	2	Row
Phase 1	10	0
Phase 2	10	1
Phase 3	10	2
Phase 4	10	3
Phase 5	10	4
Phase 6	10	5
Phase 7	10	6
Phase 8	10	7

**Coordination Transition Minimums**  
 <C+0+C=5>

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

Name	Type	EWStreet	NStreet	Group	Drop#	Area	AreaAddr	Channel	Sys Ref #	Last Change	FM Name
CLAIREMON 233New233		CLAIREMON KEARNY VII	NONE		7	5	4	COM33:	24	#####	NONE

Coord Minimums																Bar	
																Hour	Minute
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	0	1
Page 0 <C/5>																	

Notes are in Column A, Rows 32 to 40


**INTERSECTION: CLAIREMONT MESA @ KEARNY VILLA**

Group Assignment: **NONE**  
 Field Master Assignment: **NONE**  
 System Reference Number: **24**

N/S Street Name: **KEARNY VILLA**  
 E/W Street Name: **CLAIREMONT MESA**

Last Database Change: **11/9/2015 9:22**

Change Record					
Change	By	Date	Change	By	Date
Original TS	STC	11/9/15			

Notes:

**Manual Plan**  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

**Manual Offset**  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Drop Number	7/6	<C/0+0+0>
Zone Number	7	<C/0+0+1>
Area Number	5	<C/0+0+2>
Area Address	4	<C/0+0+3>
QuicNet Channel	COM33:	(QuicNet)

Manual Plan	0	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>
FYA Red Revert	0.0	<F/1+0+5>
OVL P CHG Red	0.0	<F/1+0+3>

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

**Exclusive Ped Phase**  
 (Outputs specified in Assignable  
 Outputs at E/127+A+E & F)

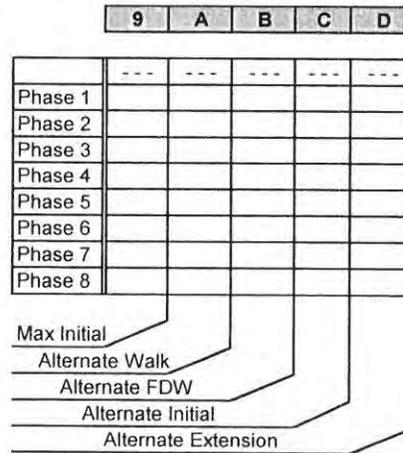
**Communication Addresses**

**Manual Selection**

**Start / Revert Times**

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk		7				7		7
1	Ped FDW		19				28		30
2	Min Green	4	10	4	7	4	10	4	7
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	3.7	2.0	4.7	2.0	4.2	2.0	4.2
6	Max Gap	2.0	3.7	2.0	4.7	2.0	4.2	2.0	4.2
7	Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
8	Max Limit	30	60	30	40	40	60	30	40
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW		1				1		1
C	Cond Serv Check								
D	Reduce Every		0.9		0.7		0.8		0.8
E	Yellow Change	3.4	4.3	3.4	4.2	3.4	4.2	3.4	4.4
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

**Phase Timing - Bank 1** <C+0+F=1>



**Alternate Timing** <C+0+F=1>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

**Preempt Timing**

	F	Row
Permit	12345678	0
Red Lock		1
Yellow Lock		2
Min Recall	2_6_	3
Ped Recall		4
View Set Peds	-----	5
Rest In Walk		6
Red Rest		7
Dual Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond. Service		C
Man Cntrl Calls		D
Yellow Start	2_6_	E
First Phases	3_7_	F

**Phase Functions** <C+0+F=1>

**INTERSECTION: CLAIREMONT MESA @ KEARNY VILLA**

Group Assignment: **NONE**  
 Field Master Assignment: **NONE**  
 System Reference Number: **24**

N/S Street Name: **KEARNY VILLA**  
 E/W Street Name: **CLAIREMONT MESA**

Last Database Change: **11/9/2015 9:22**

Change Record					
Change	By	Date	Change	By	Date
Original TS	STC	11/9/15			

Notes:

**Manual Plan**  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

**Manual Offset**  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Drop Number	7/6	<C/0+0+0>
Zone Number	7	<C/0+0+1>
Area Number	5	<C/0+0+2>
Area Address	4	<C/0+0+3>
QuicNet Channel	COM33:	(QuicNet)

Manual Plan	0	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>
FYA Red Revert	0.0	<F/1+0+5>
OVL P CHG Red	0.0	<F/1+0+3>

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

**Exclusive Ped Phase**  
 (Outputs specified in Assignable  
 Outputs at E/127+A+E & F)

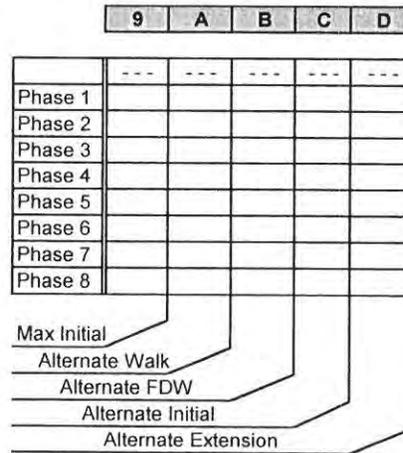
**Communication Addresses**

**Manual Selection**

**Start / Revert Times**

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk		7				7		7
1	Ped FDW		19				28		30
2	Min Green	4	10	4	7	4	10	4	7
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	3.7	2.0	4.7	2.0	4.2	2.0	4.2
6	Max Gap	2.0	3.7	2.0	4.7	2.0	4.2	2.0	4.2
7	Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
8	Max Limit	30	60	30	40	40	60	30	40
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW		1				1		1
C	Cond Serv Check								
D	Reduce Every		0.9		0.7		0.8		0.8
E	Yellow Change	3.4	4.3	3.4	4.2	3.4	4.2	3.4	4.4
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

**Phase Timing - Bank 1** <C+0+F=1>



**Alternate Timing** <C+0+F=1>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

**Preempt Timing**

	F
Permit	12345678
Red Lock	
Yellow Lock	
Min Recall	2_6_
Ped Recall	
View Set Peds	-----
Rest In Walk	
Red Rest	
Dual Entry	
Max Recall	
Soft Recall	
Max 2	
Cond. Service	
Man Cntrl Calls	
Yellow Start	2_6_
First Phases	3_7_

**Phase Functions** <C+0+F=1>

**INTERSECTION: CLAIREMONT MESA @ COMPLEX**

Group Assignment: **NONE**  
 Field Master Assignment: **NONE**  
 System Reference Number: **25**

N/S Street Name: **COMPLEX**  
 E/W Street Name: **CLAIREMONT MESA**

Page 1 (of 9)  
 Last Database Change: **11/9/2015 11:05**

Change Record					
Change	By	Date	Change	By	Date
Original TS	STC	11/10/15			

Notes:

Manual Plan  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

Manual Offset  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Drop Number	<b>8</b>	<C/0+0+0>
Zone Number	<b>8</b>	<C/0+0+1>
Area Number	<b>5</b>	<C/0+0+2>
Area Address	<b>3</b>	<C/0+0+3>
QuicNet Channel	COM33:	(QuicNet)

Manual Plan	<b>0</b>	<C/0+A+1>
Manual Offset	<b>0</b>	<C/0+B+1>

Flash Start	<b>0</b>	<F/1+0+E>
Red Revert	<b>5.0</b>	<F/1+0+F>
All Red Start	<b>0.0</b>	<F/1+C+0>
FYA Red Revert	<b>0.0</b>	<F/1+0+5>
OVLPG CHG Red	<b>0.0</b>	<F/1+0+3>

Exclusive Walk	<b>0</b>	<F/1+0+0>
Exclusive FDW	<b>0</b>	<F/1+0+1>
All Red Clear	<b>0.0</b>	<F/1+0+2>

**Communication Addresses**

**Manual Selection**

**Start / Revert Times**

**Exclusive Ped Phase**  
 (Outputs specified in Assignable  
 Outputs at E/127+A+E & F)

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW		7		7		7		
2	Min Green		14		30		13		
3	Type 3 Disconnect								
4	Added per Vehicle	4	10		4	4	10		
5	Veh Extension	2.0	3.7		2.0	2.0	2.6		
6	Max Gap	2.0	3.7		2.0	2.0	2.6		
7	Min Gap	2.0	0.2		2.0	2.0	0.2		
8	Max Limit	30	60		40	30	60		
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW		1		1		1		
C	Cond Serv Check						1		
D	Reduce Every		0.9				1.3		
E	Yellow Change	3.4	4.3		3.9	3.4	4.2		
F	Red Clear	1.0	1.0		1.0	1.0	1.0		

**Phase Timing - Bank 1** <C+0+F=1>

	9	A	B	C	D	E	F
Phase 1	---	---	---	---	---		
Phase 2							
Phase 3							
Phase 4							
Phase 5							
Phase 6							
Phase 7							
Phase 8							

Max Initial  
 Alternate Walk  
 Alternate FDW  
 Alternate Initial  
 Alternate Extension

**Alternate Timing** <C+0+F=1>

RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

**Preempt Timing**

Permit	<b>12 456</b>	0
Red Lock		1
Yellow Lock		2
Min Recall	<b>2 6</b>	3
Ped Recall		4
View Set Peds	-----	5
Rest In Walk		6
Red Rest		7
Dual Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond. Service		C
Man Cntrl Calls		D
Yellow Start	<b>2 6</b>	E
First Phases	<b>4</b>	F

**Phase Functions** <C+0+F=1>

203



Column Numbers ---->		Overlap							
Row	Overlap Name ---->	1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8	Overlap Recall								
9	Queue Jump Phase								
A	Queue Jump Time								
B	Minimum Green								
C	Maximum Green								
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments <C+0+E=29>

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = Solid FDW on EV  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring

- Extra 2 Flags**  
 1 = AWB During Initial  
 2 = Reserved  
 3 = Disable Min Walk  
 4 = QuicNet System  
 5 = Ignore P/P on EV  
 6 = Manual Hold in FDW  
 7 = Allow QuicNet PE  
 8 = Flash Grn B4 Yellow

	C	Row
EV-A	0	0
EV-B	0	1
EV-C	0	2
EV-D	0	3
RR-1 *	---	4
RR-2 *	---	5
SE-1	0	6
SE-2	0	7
<b>Preempt Priority</b>		
<C+0+E=125>		
(* RR-1 is always Highest, and RR-2 is always Second Highest)		
		8
		9
		A
		B
		C
		D
		E
		F

Row	Column Numbers ---->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4
C	EV-C Phases	1 6
D	EV-D Phases	
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration <C+0+E=125>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	12345678
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	4
Ped for 8P Output	
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	34

Configuration <C+0+E=125>

	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	12345678
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reserve	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	12345678
Start-up Ped Calls	12345678

Specials <C+0+F=2>

- Flash to PE & PE Non-Lock**  
 1 = EV A 5 = RR 1  
 2 = EV B 6 = RR 2  
 3 = EV C 7 = SE 1  
 4 = EV D 8 = SE 2

- IC Select Flags**  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 =  
 5 =  
 6 = Simplex Master  
 7 =  
 8 = Offset Interrupter

	2	Row
Phase 1	10	0
Phase 2	10	1
Phase 3	10	2
Phase 4	10	3
Phase 5	10	4
Phase 6	10	5
Phase 7	10	6
Phase 8	10	7
<b>Coordination Transition Minimums</b>		
<C+0+C=5>		
		8
		9
		A
		B
		C
		D
		E
		F

**INTERSECTION: CLAIREMONT MESA @ COMPLEX**

		Plan								
Column Numbers ---->		1	2	3	4	5	6	7	8	9
Row	Plan Name ---->									
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	65	67	108						
2	Phase 2 - ForceOff	0	0	34						
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff	42	44	85						
5	Phase 5 - ForceOff	80	87	34						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset	0	0	0						
A	Offset 1	99	69	15						
B	Offset 2									
C	Offset 3									
D	Perm 1 - End	11	13	14						
E	Hold Release	255	255	255						
F	Reserved	0	0	0						

**Coordination - Bank 1** <C+0+C=1>

Row										
0	Ped Adjustment	1								
1	Perm 2 - Start									
2	Perm 2 - End									
3	Perm 3 - Start									
4	Perm 3 - End									
5	Reservice Time									
6	Reservice Phases									
7										
8	Pretimed Phases									
9	Max Recall									
A	Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
B	Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
C	Perm 2 Veh Phase									
D	Perm 2 Ped Phase									
E	Perm 3 Veh Phase									
F	Perm 3 Ped Phase									

**Coordination - Bank 2** <C+0+C=2>

**Coord Extra**  
 1 = Programmed WALK Time for Sync Phases  
 2 = Always Terminate Sync Phase Peds

Row		E	Row
0			0
1	Plan 1 - Sync	2 6	1
2	Plan 2 - Sync	2 6	2
3	Plan 3 - Sync	6	3
4	Plan 4 - Sync		4
5	Plan 5 - Sync		5
6	Plan 6 - Sync		6
7	Plan 7 - Sync		7
8	Plan 8 - Sync		8
9	Plan 9 - Sync		9
A	NEMA Sync		A
B	NEMA Hold		B
C			C
D			D
E			E
F			F

**Sync Phases** <C+0+C=1>

Row		F	Row
0	Free Lag	2 4 6	0
1	Plan 1 - Lag	2 4 6	1
2	Plan 2 - Lag	2 4 6	2
3	Plan 3 - Lag	2 45	3
4	Plan 4 - Lag		4
5	Plan 5 - Lag		5
6	Plan 6 - Lag		6
7	Plan 7 - Lag		7
8	Plan 8 - Lag		8
9	Plan 9 - Lag		9
A	External Lag		A
B	Lag Hold		B
C			C
D			D
E			E
F			F

**Lag Phases** <C+0+C=1>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	One-Shot Timer	Latch 1 Set	NOT-3	Max 2	Pretimed	Set DOW	Dial 2 (7-Wire)	Sim Term	0
1	AND-5 (a)	Latch 1 Reset	NOT-4	Reserved	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A	71
2	AND-5 (b)	Latch 2 Set	OR-4 (a)	Reserved	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B	72
3	AND-6 (a)	Latch 2 Reset	OR-4 (b)	Reserved	Plan 3	Gate Down	Offset 2 (7-Wire)	EV-C	73
4	AND-6 (b)	NAND-3 (a)	OR-5 (a)	Reserved	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D	74
5	Reserved	NAND-3 (b)	OR-5 (b)	Reserved	Plan 5	Stop Time	Free (7-Wire)	RR-1	51
6	Reserved	NAND-4 (a)	OR-6 (a)	Reserved	Plan 6	Flash Sense	Flash (7-Wire)	RR-2	52
7	Reserved	NAND-4 (b)	OR-6 (b)	Reserved	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1	
8	Spec. Funct. 1	OR-7 (a)	EXTMR	Reserved	Plan 8	Man. Advance	NOT-1	Spec. Event 2	
9	Spec. Funct. 2	OR-7 (b)	Reserved	Max Inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag	
A	Spec. Funct. 3	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)	
B	Spec. Funct. 4	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)	
C	Reserved	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)	
D	Reserved	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)	
E	Reserved	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)	
F	Reserved	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)	

Assignable Inputs

<C+0+E=126>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	Reserved	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)	0
1	Reserved	Phase ON - 2	Sp Evnt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)	0
2	Reserved	Phase ON - 3	Sp Evnt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)	0
3	Reserved	Phase ON - 4	Sp Evnt Out 3	EXTMR	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)	0
4	Reserved	Phase ON - 5	Sp Evnt Out 4	One-Shot Timer	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)	0
5	Reserved	Phase ON - 6	Sp Evnt Out 5	Reserved	Plan 5	AND-2	TOD Out 6	Free (7-Wire)	0
6	Reserved	Phase ON - 7	Sp Evnt Out 6	Latch 1	Plan 6	AND-3	TOD Out 7	Flash (7-Wire)	0
7	Reserved	Phase ON - 8	Sp Evnt Out 7	Latch 2	Plan 7	NOT-2	TOD Out 8	Preempt	0
8	Fih Yell Arrow 1	Ph. Check - 1	Sp Evnt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A	0
9	Green 1	Ph. Check - 2	Coord On	NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B	0
A	Fih Yell Arrow 3	Ph. Check - 3	Detector Fail	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C	0
B	Green 3	Ph. Check - 4	Spec. Funct. 1	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D	0
C	Fih Yell Arrow 5	Ph. Check - 5	Spec. Funct. 2	OR-6	NAND-3	RR-1	DELAY-C	AND-5	0
D	Green 5	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	AND-6	0
E	Fih Yell Arrow 7	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	Reserved	
F	Green 7	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	Reserved	

Assignable Outputs

<C+0+E=127>

INTERSECTION: CLAIREMONT MESA @ COMPLEX

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					

Max Initial  
Alternate Walk  
Alternate FDW  
Alternate Initial  
Alternate Extension

Alternate Timing

Transition Type  
0.X = Shortway  
1.X = Lengthen  
X.1 thru X.4 =  
Number of  
cycles when  
lengthing

Transition Type 0.0 <C/5+1+9>

**TBC Transition**

Hawk Select 0 F/1+0+4>  
**Hawk Select** 200 = Mid-Block, 201 = Hawk

Address 0 <C/1+0+6>

Select Parity 0 <C/1+0+5>

**AB3418 Comm 2** 0 = No Parity, 1 = Even

Begin Month 3 <C/5+2+A>

Begin Week 2 <C/5+2+B>

End Month 11 <C/5+2+C>

End Week 1 <C/5+2+D>

**Daylight Savings Time**

Daylight Savings  
Date  
If set to all zeros,  
standard dates  
will be used.

Time B4 Yellow 0.0 <F/1+C+E>

Phase Number 0 <F/1+C+F>

**Advance Warning Beacon - Sign 1**

Time B4 Yellow 0.0

Phase Number 0 <F/1+D+F>

**Advance Warning Beacon - Sign 2**

Offset Time 0 <C/5+2+E>

Max Cycle Time 0 <C/5+2+F>

**Yellow Yield Coordination**

Omit Alarm 12345678  
12345678 <C/5+F+0>

**Local Alarm Disable**

Row	Phase Names ---->	1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					

Max Initial  
Alternate Walk  
Alternate FDW  
Alternate Initial  
Alternate Extension

Alternate Timing

Column Numbers ---->

Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0	Ph. 2 I-2 U	39	45_7	2	123		1.8
1	Ph. 6 J-2 U	40	45_7	6	123		1.8
2	Ph. 4 I-6 U	41	45_7	4	123	10.0	
3		42	45_7	8	123		
4		43	45_7	2	123		
5		44	45_7	6	123		
6		45	45_7	4	123		
7		46	45_7	8	123		
8		47	67	2	123		
9		48	67	6	123		
A		49	67	4	123		
B		50	67	8	123		
C		55	45_7	5	123		
D		56	45_7	1	123		
E		57	45_7	7	123		
F		58	45_7	3	123		

Column Numbers ---->

	Ped / Phase / Overlap								Row
	1	2	3	4	5	6	7	8	
Walk									0
Don't Walk									1
Phase Green									2
Phase Yellow									3
Phase Red									4
Overlap Green									5
Overlap Yellow									6
Overlap Red									7

Redirect Phase Outputs <C+0+E=127>

Cabinet Type  <E/125+D+0>

**Enable Redirection**  
(Enable Redirection = 30)

Max OFF (minutes)  <D/0+0+1>  
 Max ON (minutes)  <D/0+0+2>  
 Chatter Fail Time  <D/0+0+4>

**Detector Failure Monitor**

	B	Row
One-Shot		8
Ext. Timer		9
DELAY-A		A
DELAY-B		B
DELAY-C		C
DELAY-D		D
DELAY-E		E
DELAY-F		F

**Delay Logic Times**  
<C+0+D=0> (seconds)

Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0		59	45_7	5	123		
1		60	45_7	1	123		
2		61	45_7	7	123		
3		62	45_7	3	123		
4		63	45_7	2	123		
5		64	45_7	6	123		
6		65	45_7	4	123		
7		66	45_7	8	123		
8		67	2	2	123		
9		68	2	6	123		
A		69	2	4	123		
B		70	2	8	123		
C		76	45_7	2	123		
D		77	45_7	6	123		
E		78	45_7	4	123		
F		79	45_7	8	123		

**Detector Attributes**

- 1 = Full Time Delay
- 2 = Ped Call
- 3 = Overlap
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

**Det. Assignments**

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Detector Assignments <C+0+E=126>

<C+0+D=0>



Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 1

<C+0+E=27>

Notes: \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_

0 <E/27+5+F>  
**Limited Service Interval**

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 2

<C+0+E=28>

Notes: \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_

0 <E/28+5+F>  
**Limited Service Interval**

**INTERSECTION: CLAIREMONT MESA @ COMPLEX**

Min Time (seconds)  <F/1+0+8>  
**Min Green Before PE Force Off**

Max Time (minutes)  <F/1+0+9>  
**Max Preempt Time Before Failure**

Min Time (seconds)  <F/1+0+A>  
**Min Time Between Same Preempts**  
 (Does Not Apply To Railroad Preempt)

Low Pri. Channel  <E/125+C+8>  
**Disable Low Priority Channel**

- Low Priority  
 1 = Channel A  
 2 = Channel B  
 3 = Channel C  
 4 = Channel D

Row		
C	Bus Headway	
D	Bus Delay	
E	Max Early Grn	
F	Max Grn Ext.	

**Priority Parameters**  
 <F/1 +A+Row>

Row	Time	Headway	Direction	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Headway Time  
 (minutes)  
 1 thru 9 = 1 thru 9  
 A = 10  
 B = 11  
 C = 12  
 D = 13  
 E = 14  
 F = 15

**Headway Schedule** <C+0+9=2.1>

**Low Priority Preemption (Bus Priority)**

Note: Also see "Time of Day Functions", Function E, Bit 5 (Disable Low Priority)

**INTERSECTION: CLAIREMONT MESA @ OVERLAND**

Group Assignment: **NONE**

Field Master Assignment: **NONE**

System Reference Number: **26**

N/S Street Name: **OVERLAND**

E/W Street Name: **CLAIREMONT MESA**

Change Record					
Change	By	Date	Change	By	Date
Original TS	STC	11/10/15			

Notes:

Manual Plan  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

Manual Offset  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Drop Number	9	<C/0+0+0>
Zone Number	9	<C/0+0+1>
Area Number	5	<C/0+0+2>
Area Address	2	<C/0+0+3>
QuicNet Channel	COM33:	(QuicNet)

Manual Plan	0	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

**Communication Addresses**

**Manual Selection**

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>
FYA Red Revert	0.0	<F/1+0+5>
OVL P CHG Red	0.0	<F/1+0+3>

**Start / Revert Times**

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

**Exclusive Ped Phase**

(Outputs specified in Assignable Outputs at E/127+A+E & F)

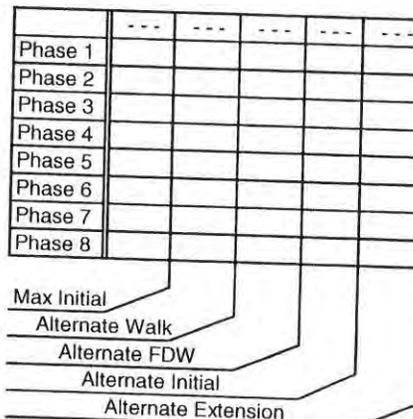
Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk		7		7		7		7
1	Ped FDW		20		29		18		29
2	Min Green	4	10	4	7	4	10	4	7
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	4.9	2.0	2.0	2.0	4.9	2.0	5.5
6	Max Gap	2.0	4.9	2.0	2.0	2.0	4.9	2.0	5.5
7	Min Gap	2.0	0.2	2.0	2.0	2.0	0.2	2.0	0.2
8	Max Limit	30	60	30	60	30	60	30	60
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW		1		1		1		1
C	Cond Serv Check								
D	Reduce Every		0.6				0.6		0.6
E	Yellow Change	3.4	4.3	3.4	3.9	3.4	4.2	3.4	3.9
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

**Phase Timing - Bank 1** <C+0+F=1>

9 A B C D

E

F



**Alternate Timing** <C+0+F=1>

RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

**Preempt Timing**

Permit	12345678	0
Red Lock		1
Yellow Lock		2
Min Recall	2 6	3
Ped Recall		4
View Set Peds	-----	5
Rest In Walk		6
Red Rest		7
Dual Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond. Service		C
Man Cntrl Calls		D
Yellow Start	2 6	E
First Phases	3 7	F

**Phase Functions** <C+0+F=1>

24



Column Numbers ---->		Overlap							
Row	Overlap Name ---->	1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8	Overlap Recall								
9	Queue Jump Phase								
A	Queue Jump Time								
B	Minimum Green								
C	Maximum Green								
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments <C+0+E=29>

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = Solid FDW on EV  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring

- Extra 2 Flags**  
 1 = AWB During Initial  
 2 = Reserved  
 3 = Disable Min Walk  
 4 = QuicNet System  
 5 = Ignore P/P on EV  
 6 = Manual Hold in FDW  
 7 = Allow QuicNet PE  
 8 = Flash Grn B4 Yellow

	C	Row
EV-A	0	0
EV-B	0	1
EV-C	0	2
EV-D	0	3
RR-1 *	---	4
RR-2 *	---	5
SE-1	0	6
SE-2	0	7
<b>Preempt Priority</b>		8
<C+0+E=125>		9
(* RR-1 is always Highest, and RR-2 is always Second Highest)		A
		B
		C
		D
		E
		F

Row	Column Numbers ---->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4 7
C	EV-C Phases	1 6
D	EV-D Phases	3 8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration <C+0+E=125>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	12345678
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	4
Ped for 8P Output	8
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	34

Configuration <C+0+E=125>

	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	12345678
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reservice	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	12345678
Start-up Ped Calls	12345678

Specials <C+0+F=2>

- Flash to PE & PE Non-Lock**  
 1 = EV A 5 = RR 1  
 2 = EV B 6 = RR 2  
 3 = EV C 7 = SE 1  
 4 = EV D 8 = SE 2

- IC Select Flags**  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 =  
 5 =  
 6 = Simplex Master  
 7 =  
 8 = Offset Interrupter

	2	Row
Phase 1	10	0
Phase 2	10	1
Phase 3	10	2
Phase 4	10	3
Phase 5	10	4
Phase 6	10	5
Phase 7	10	6
Phase 8	10	7
<b>Coordination Transition Minimums</b>		8
<C+0+C=5>		9
		A
		B
		C
		D
		E
		F

**INTERSECTION: CLAIREMONT MESA @ OVERLAND**

Coord Extra

1 = Programmed WALK Time for Sync Phases  
2 = Always Terminate Sync Phase Peds

		Plan								
Column Numbers ---->		1	2	3	4	5	6	7	8	9
Row	Plan Name ---->									
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	71	85	17						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff	19	27	37						
4	Phase 4 - ForceOff	56	59	80						
5	Phase 5 - ForceOff	71	81	96						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff	19	26	37						
8	Phase 8 - ForceOff	56	59	80						
9	Ring Offset	0	0	0						
A	Offset 1	101	55	92						
B	Offset 2									
C	Offset 3									
D	Perm 1 - End	11	13	14						
E	Hold Release	255	125	255						
F	Reserved	0	0	0						

**Coordination - Bank 1** <C+0+C=1>

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

Row	E	Row
0		0
1	Plan 1 - Sync <u>2 6</u>	1
2	Plan 2 - Sync <u>2 6</u>	2
3	Plan 3 - Sync <u>2 6</u>	3
4	Plan 4 - Sync	4
5	Plan 5 - Sync	5
6	Plan 6 - Sync	6
7	Plan 7 - Sync	7
8	Plan 8 - Sync	8
9	Plan 9 - Sync	9
A	NEMA Sync	A
B	NEMA Hold	B
C		C
D		D
E		E
F		F

**Sync Phases** <C+0+C=1>

Row		1	2	3	4	5	6	7	8	9
0	Ped Adjustment	4	6							
1	Perm 2 - Start									
2	Perm 2 - End									
3	Perm 3 - Start									
4	Perm 3 - End									
5	Reservice Time									
6	Reservice Phases									
7										
8	Pretimed Phases									
9	Max Recall									
A	Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
B	Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
C	Perm 2 Veh Phase									
D	Perm 2 Ped Phase									
E	Perm 3 Veh Phase									
F	Perm 3 Ped Phase									

**Coordination - Bank 2** <C+0+C=2>

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

Row	F	Row
0	Free Lag <u>2 4 6 8</u>	0
1	Plan 1 - Lag <u>2 4 6 8</u>	1
2	Plan 2 - Lag <u>2 4 6 8</u>	2
3	Plan 3 - Lag <u>1 4 6 8</u>	3
4	Plan 4 - Lag	4
5	Plan 5 - Lag	5
6	Plan 6 - Lag	6
7	Plan 7 - Lag	7
8	Plan 8 - Lag	8
9	Plan 9 - Lag	9
A	External Lag	A
B	Lag Hold	B
C		C
D		D
E		E
F		F

**Lag Phases** <C+0+C=1>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	One-Shot Timer	Latch 1 Set	NOT-3	Max 2	Pretimed	Set DOW	Dial 2 (7-Wire)	Sim Term	0
1	AND-5 (a)	Latch 1 Reset	NOT-4	Reserved	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A	71
2	AND-5 (b)	Latch 2 Set	OR-4 (a)	Reserved	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B	72
3	AND-6 (a)	Latch 2 Reset	OR-4 (b)	Reserved	Plan 3	Gate Down	Offset 2 (7-Wire)	EV-C	73
4	AND-6 (b)	NAND-3 (a)	OR-5 (a)	Reserved	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D	74
5	Reserved	NAND-3 (b)	OR-5 (b)	Reserved	Plan 5	Stop Time	Free (7-Wire)	RR-1	51
6	Reserved	NAND-4 (a)	OR-6 (a)	Reserved	Plan 6	Flash Sense	Flash (7-Wire)	RR-2	52
7	Reserved	NAND-4 (b)	OR-6 (b)	Reserved	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1	
8	Spec. Funct. 1	OR-7 (a)	EXTMR	Reserved	Plan 8	Man. Advance	NOT-1	Spec. Event 2	
9	Spec. Funct. 2	OR-7 (b)	Reserved	Max Inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag	
A	Spec. Funct. 3	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)	
B	Spec. Funct. 4	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)	
C	Reserved	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)	
D	Reserved	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)	
E	Reserved	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)	
F	Reserved	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)	

Assignable Inputs

<C+0+E=126>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	Reserved	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)	0
1	Reserved	Phase ON - 2	Sp Evnt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)	1
2	Reserved	Phase ON - 3	Sp Evnt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)	2
3	Reserved	Phase ON - 4	Sp Evnt Out 3	EXTMR	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)	3
4	Reserved	Phase ON - 5	Sp Evnt Out 4	One-Shot Timer	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)	4
5	Reserved	Phase ON - 6	Sp Evnt Out 5	Reserved	Plan 5	AND-2	TOD Out 6	Free (7-Wire)	5
6	Reserved	Phase ON - 7	Sp Evnt Out 6	Latch 1	Plan 6	AND-3	TOD Out 7	Flash (7-Wire)	6
7	Reserved	Phase ON - 8	Sp Evnt Out 7	Latch 2	Plan 7	NOT-2	TOD Out 8	Preempt	7
8	Flh Yell Arrow 1	Ph. Check - 1	Sp Evnt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A	8
9	Green 1	Ph. Check - 2	Coord On	NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B	9
A	Flh Yell Arrow 3	Ph. Check - 3	Detector Fail	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C	A
B	Green 3	Ph. Check - 4	Spec. Funct. 1	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D	B
C	Flh Yell Arrow 5	Ph. Check - 5	Spec. Funct. 2	OR-6	NAND-3	RR-1	DELAY-C	AND-5	C
D	Green 5	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	AND-6	D
E	Flh Yell Arrow 7	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	Reserved	E
F	Green 7	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	Reserved	F

Assignable Outputs

<C+0+E=127>

INTERSECTION: CLAIREMONT MESA @ OVERLAND

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					

Max Initial  
Alternate Walk  
Alternate FDW  
Alternate Initial  
Alternate Extension

Alternate Timing

Transition Type  
0.X = Shortway  
1.X = Lengthen  
X.1 thru X.4 =  
Number of  
cycles when  
lengthing

Transition Type 0.0 <C/5+1+9>

TBC Transition

Hawk Select 0 F/1+0+4  
Hawk Select 200 = Mid-Block, 201 = Hawk

Address 0 <C/1+0+6>  
Select Parity 0 <C/1+0+5>  
AB3418 Comm 2 0 = No Parity, 1 = Even

Begin Month 3 <C/5+2+A>  
Begin Week 2 <C/5+2+B>  
End Month 11 <C/5+2+C>  
End Week 1 <C/5+2+D>

Daylight Savings Time

Daylight Savings  
Date  
If set to all zeros,  
standard dates  
will be used.

Time B4 Yellow 0.0 <F/1+C+E>  
Phase Number 0 <F/1+C+F>

Advance Warning Beacon - Sign 1

Time B4 Yellow 0.0  
Phase Number 0 <F/1+D+F>

Advance Warning Beacon - Sign 2

Offset Time 0 <C/5+2+E>  
Max Cycle Time 0 <C/5+2+F>

Yellow Yield Coordination

12345678  
Omit Alarm 12345678 <C/5+F+0>

Local Alarm Disable

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					

Max Initial  
Alternate Walk  
Alternate FDW  
Alternate Initial  
Alternate Extension

Alternate Timing

INTERSECTION: CLAIREMONT MESA @ OVERLAND

Column Numbers ---->		0	1	2	3	1	3
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0	Ph. 2 I-2 U	39	45 7	2	123		1.8
1	Ph. 6 J-2 U	40	45 7	6	123		1.8
2		41	45 7	4	123		
3	Ph. 8 J-6 U	42	45 7	8	123		1.8
4		43	45 7	2	123		
5		44	45 7	6	123		
6		45	45 7	4	123		
7	Ph. 8 J-6 L	46	45 7	8	123		1.8
8		47	67	2	123		
9		48	67	6	123		
A		49	67	4	123		
B		50	67	8	123		
C		55	45 7	5	123		
D		56	45 7	1	123		
E		57	45 7	7	123		
F		58	45 7	3	123		

Column Numbers ---->		Ped / Phase / Overlap								Row
		1	2	3	4	5	6	7	8	
Walk										0
Don't Walk										1
Phase Green										2
Phase Yellow										3
Phase Red										4
Overlap Green										5
Overlap Yellow										6
Overlap Red										7

Redirect Phase Outputs <C+0+E=127>

Cabinet Type 0 <E/125+D+0>

Enable Redirection  
(Enable Redirection = 30)

Max OFF (minutes) 60 <D/0+0+1>

Max ON (minutes) 5 <D/0+0+2>

Chatter Fail Time 0 <D/0+0+4>

Detector Failure Monitor

	B	Row
One-Shot		8
Ext. Timer		9
DELAY-A		A
DELAY-B		B
DELAY-C		C
DELAY-D		D
DELAY-E		E
DELAY-F		F

Delay Logic Times  
<C+0+D=0> (seconds)

Column Numbers ---->		4	5	6	7	2	4
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0		59	45 7	5	123		
1		60	45 7	1	123		
2		61	45 7	7	123		
3		62	45 7	3	123		
4		63	45 7	2	123		
5		64	45 7	6	123		
6		65	45 7	4	123		
7		66	45 7	8	123		
8		67	2	2	123		
9		68	2	6	123		
A		69	2	4	123		
B		70	2	8	123		
C		76	45 7	2	123		
D		77	45 7	6	123		
E	Ph. 4 I-7 L	78	45 7	4	123		10.0
F		79	45 7	8	123		

Detector Attributes

- 1 = Full Time Delay
- 2 = Ped Call
- 3 = Overlap
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

Det. Assignments

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Detector Assignments <C+0+E=126>

<C+0+D=0>



Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

**Special Event Schedule -- Table 1** <C+0+E=27>

Notes:

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0 <E/27+5+F>  
**Limited Service Interval**

Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

**Special Event Schedule -- Table 2** <C+0+E=28>

Notes:

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0 <E/28+5+F>  
**Limited Service Interval**

**INTERSECTION: CLAIREMONT MESA @ OVERLAND**

Min Time (seconds)  <F/1+0+8>  
**Min Green Before PE Force Off**

Max Time (minutes)  <F/1+0+9>  
**Max Preempt Time Before Failure**

Min Time (seconds)  <F/1+0+A>  
**Min Time Between Same Preempts**  
 (Does Not Apply To Railroad Preempt)

Low Pri. Channel  <E/125+C+8>  
**Disable Low Priority Channel**

- Low Priority  
 1 = Channel A  
 2 = Channel B  
 3 = Channel C  
 4 = Channel D

Row		
C	Bus Headway	
D	Bus Delay	
E	Max Early Grn	
F	Max Grn Ext.	

**Priority Parameters**  
 <F/1 +A+Row>

Row	Time	Headway	Direction	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**Headway Schedule** <C+0+9=2.1>

Headway Time  
 (minutes)  
 1 thru 9 = 1 thru 9  
 A = 10  
 B = 11  
 C = 12  
 D = 13  
 E = 14  
 F = 15

**Low Priority Preemption (Bus Priority)**

Note: Also see "Time of Day Functions", Function E, Bit 5 (Disable Low Priority)



Group Assignment:  
Field Master Assignment:  
System Reference Number:

N/S Street: Ruffin Rd  
E/W Street: Farnham St

Last Database Change:

Timing sheets by: REJ  
Approved by: BC  
Timing implemented on: 1/5/2010

Row	Phase Numbers-->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green	4	10		7	4	10		7
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	5.7		2.0	2.0	5.7		2.0
6	Max Gap	2.0	5.7		2.0	2.0	5.7		2.0
7	Min Gap	2.0	0.2		2.0	2.0	0.2		2.0
8	Max Limit	30	60		40	30	60		40
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every		0.5				0.5		
E	Yellow Change	3.4	4.3		3.9	3.4	4.3		3.9
F	Red Clear	1.0	1.0		1.0	1.0	1.0		1.0

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

	F	Row
Permit	12_456_8	0
Red Lock		1
Yellow Lock		2
Min Recall		3
Ped Recall		4
View Set Peds		5
Rest In Walk		6
Red Rest		7
Double Entry	_4_8	8
Max Recall		9
Soft Recall	_2_6_	A
Max 2		B
Cond. Service		C
Man Cntrl Calls		D
Yellow Start	_2_6_	E
First Phases	_4_8	F

Phase Timing - Bank 1 <F/1+Phase+Row>

Current Calculated Cycle Length: C/0 + B + F

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					
Alternate Timing					

Free Lag | 2 4 6 8 | <C/1+F+0>

How to Set Page Access Code:  
F/1 -- C + 0 + F = 1

Preempt Timing <F/1+E+Row> Phase Functions <F/1+F+Row>

Drop Number	17	<C/0+0+0>
Zone Number	17	<C/0+0+1>
Area Number	5	<C/0+0+2>
Area Address	116	<C/0+0+3>
QuicNet Channel	COM37	(QuicNet)

Communication Addresses

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>

Start / Revert Times

Notes:

(Outputs specified in Assignable Outputs at E/127+A+E & F)

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

Exclusive Ped Phase

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Manual Plan	14	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Manual Selection

25

Row		Overlap							
		1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8									
9									
A									
B									
C									
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments <E/29+Column+Row>

	F	Row
Fast Green Flash Phase		0
Green Flash Phases		1
Flashing Walk Phases		2
Guaranteed Passage		3
Simultaneous Gap Term	12345678	4
Sequential Timing		5
Advance Walk Phases		6
Delay Walk Phases		7
External Recall		8
Start-up Overlap Green		9
Max Extension		A
Inhibit Ped Reservice		B
Semi-Actuated		C
Start-up Overlap Yellow		D
Start-up Vehicle Calls	12345678	E
Start-up Ped Calls	12345678	F

Specials <F/2+F+Row>

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4
C	EV-C Phases	1 6
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration <E/125+E+Row>

Row		F
	Ext. Permit 1 Phases	
	Ext. Permit 2 Phases	
	Exclusive Ped Assign	
	Preempt Non-Lock	12345678
	Ped for 2P Output	2
	Ped for 6P Output	6
	Ped for 4P Output	4
	Ped for 8P Output	8
	Yellow Flash Phases	
	Low Priority A Phases	
	Low Priority B Phases	
	Low Priority C Phases	
	Low Priority D Phases	
	Restricted Phases	
	Extra 2 Config. Bits	

Configuration <E/125+F+Row>

Row		C
EV-A		
EV-B		
EV-C		
EV-D		
RR-1 *	---	
RR-2 *	---	
SE-1	0	
SE-2	0	

<E/125+C+Row>

**Preemption Priority**

(\* RR-1 is always Highest, and RR-2 is always Second Highest)

Row	
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Row		2	Row
	Phase 1	0	0
	Phase 2	0	1
	Phase 3	0	2
	Phase 4	0	3
	Phase 5	0	4
	Phase 6	0	5
	Phase 7	0	6
	Phase 8	0	7
			8
			9
			A
			B
			C
			D
			E
			F

<C/5+2+Row>

**Coordination Transition Minimums**

- |                           |                        |
|---------------------------|------------------------|
| <b>Extra 1 Flags</b>      | <b>IC Select Flags</b> |
| 1 = TBC Type 1            | 1 =                    |
| 2 = NEMA Ext. Coord       | 2 = Modem              |
| 3 = Auto Daylight Savings | 3 = 7-Wire Slave       |
| 4 = EV Advance            | 4 = Flash / Free       |
| 5 = Extended Status       | 5 =                    |
| 6 = International Ped     | 6 = Simplex Master     |
| 7 = Flash - Clear Outputs | 7 = 7-Wire Master      |
| 8 = Split Ring            | 8 = Offset Interrupter |

- |                        |                                      |
|------------------------|--------------------------------------|
| <b>Extra 2 Flags</b>   | <b>Flash to PE &amp; PE Non-Lock</b> |
| 1 = AWB During Initial | 1 = EV A 5 = RR 1                    |
| 2 = LMU Installed      | 2 = EV B 6 = RR 2                    |
| 3 = Disable Min Walk   | 3 = EV C 7 = SE 1                    |
| 4 = QuicNet/4 System   | 4 = EV D 8 = SE 2                    |
| 5 = Ignore P/P on EV   |                                      |
| 6 =                    |                                      |
| 7 = Reserved           |                                      |
| 8 =                    |                                      |

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

**Time and Date**

Begin Month	0	<C/5+2+A>
Begin Week	0	<C/5+2+B>
End Month	0	<C/5+2+C>
End Week	0	<C/5+2+D>

**Daylight Savings Time**

Daylight Savings Date:  
 If set to all zeros, standard dates will be used.

Row	Detector Name	0 C1 Pin Number	1 Attributes	2 Phase(s)	3 Assign	1 Delay	3 Carry- Over
0	2I2U	39	45_7_	2	123_8		1.8
1	6J2U	40	45_7_	6	123_8		1.8
2	4I6U	41	45_7_	4	123_8	12.0	
3	8J6U	42	45_7_	8	123_8	12.0	
4	2I2L	43	45_7_	2	123_8		1.8
5	6J2L	44	45_7_	6	123_8		1.8
6		45	45_7_	4	123_8		
7		46	45_7_	8	123_8		
8		47	67_	2	123_8		
9		48	67_	6	123_8		
A		49	67_	4	123_8		
B		50	67_	8	123_8		
C		55	45_7_	5	123_8		
D		56	45_7_	1_7_	123_8		
E		57	45_7_	7_	123_8		
F		58	45_7_	3	123_8		

Program Type:

	Ped / Phase / Overlap								Row
	1	2	3	4	5	6	7	8	
Walk									0
Don't Walk									1
Phase Green									2
Phase Yellow									3
Phase Red									4
Overlap Green									5
Overlap Yellow									6
Overlap Red									7

Redirect Phase Outputs <E/127+Column+Row>

Cabinet Type	0
--------------	---

<E/125+D+0>

**Enable Redirection**

(Enable Redirection = 30)

Max OFF (minutes)	5	<D/0+0+1>
-------------------	---	-----------

Max ON (minutes)	60	<D/0+0+2>
------------------	----	-----------

**Detector Failure Monitor**

	D	Row
		0
Output Port 1		1
Output Port 2		2
Output Port 3		3
Output Port 4		4
Output Port 5		5
Output Port 6		6
Output Port 7		7

**Dimming** <E/125+D+Row>

Row	Detector Name	4 C1 Pin Number	5 Attributes	6 Phase(s)	7 Assign	2 Delay	4 Carry- Over
0		59	45_7_	5	123_8		
1		60	45_7_	1_7_	123_8		
2		61	45_7_	7	123_8		
3		62	45_7_	3	123_8		
4		63	45_7_	2	123_8		
5		64	45_7_	6	123_8		
6		65	45_7_	4	123_8		
7		66	45_7_	8	123_8		
8		67	2	2	123_8		
9		68	2	6	123_8		
A		69	2	4	123_8		
B		70	2	8	123_8		
C		76	45_7_	2	123_8		
D		77	45_7_	6	123_8		
E		78	45_7_	4	123_8		
F		79	45_7_	8	123_8		

Detector Attributes

- 1 = Full Time Delay
- 2 = Ped Call
- 3 =
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

Det. Assignments

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Detector Assignments <E/126+Column+Row> <D/0+Column+Row>

	D
Number of Digits	
1 st Digit	
2 ed Digit	
3 ed Digit	
4 th Digit	
5 th Digit	
6 th Digit	
7 th Digit	
8 th Digit	
9 th Digit	
10 th Digit	
11 th Digit	
12 th Digit	
13 th Digit	
14 th Digit	
15 th Digit	

<C/5+D+Row>

**Dial-Back Telephone Number**

Disable Alarms

- 1 = Stop Time
- 2 = Flash Sense
- 3 = Keyboard Entry
- 4 = Manual Plan
- 5 = Police Control
- 6 = External Alarm
- 7 = Detector Failure
- 8 =

	B	Row
DELAY-A	1	A
DELAY-B	1	B
DELAY-C	0	C
DELAY-D	0	D
DELAY-E	0	E
DELAY-F	0	F

<D/0+B+Row> (seconds)

**Delay Logic Times**

Omit Alarm	
------------	--

<C/5+F+0>

**Disable Alarm Reporting**

Time	0	<C/5+C+0>
------	---	-----------

**Redial Time** (minutes)

(View Redial Timer at E/2+D+6)

**INTERSECTION: CLAIREMONT MESA @ RUFFIN**

Group Assignment: **NONE**  
 Field Master Assignment: **NONE**  
 System Reference Number: **27**

N/S Street Name: **RUFFIN**  
 E/W Street Name: **CLAIREMONT MESA**

Last Database Change: **11/9/2015 12:07**

Change Record					
Change	By	Date	Change	By	Date
Original TS	STC	11/10/15			

Notes:

Manual Plan  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

Manual Offset  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Drop Number	10	<C/0+0+0>
Zone Number	10	<C/0+0+1>
Area Number	5	<C/0+0+2>
Area Address	1	<C/0+0+3>
QuicNet Channel	COM33:	(QuicNet)

Manual Plan	0	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

**Communication Addresses**

**Manual Selection**

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>
FYA Red Revert	0.0	<F/1+0+5>
OVL P CHG Red	0.0	<F/1+0+3>

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

**Start / Revert Times**

**Exclusive Ped Phase**  
 (Outputs specified in Assignable  
 Outputs at E/127+A+E & F)

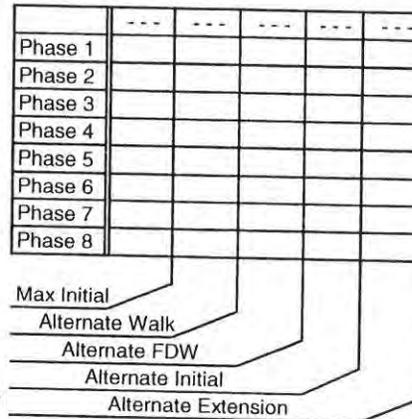
Column Numbers ---->		Phase							
Row	Phase Names ---->	1	2	3	4	5	6	7	8
0	Ped Walk		5		5		5		5
1	Ped FDW		27		29		24		28
2	Min Green	4	10	4	4	4	10	4	4
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	3.8	2.0	4.1	2.0	3.5	2.0	4.1
6	Max Gap	2.0	3.8	2.0	4.1	2.0	3.5	2.0	4.1
7	Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
8	Max Limit	30	60	40	40	30	60	30	40
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW		1		1		1		1
C	Cond Serv Check				4.3				
D	Reduce Every		0.8		0.8		0.9		0.8
E	Yellow Change	3.4	4.3	3.4	4.4	3.4	4.2	3.4	4.6
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

**Phase Timing - Bank 1** <C+0+F=1>

9 A B C D

E

F



**Alternate Timing** <C+0+F=1>

RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

**Preempt Timing**

Permit	12345678
Red Lock	
Yellow Lock	
Min Recall	2 6
Ped Recall	
View Set Peds	-----
Rest In Walk	
Red Rest	
Dual Entry	
Max Recall	
Soft Recall	
Max 2	
Cond. Service	
Man Cntrl Calls	
Yellow Start	2 6
First Phases	3 7

**Phase Functions** <C+0+F=1>

Row	0
	1
	2
	3
	4
	5
	6
	7
	8
	9
	A
	B
	C
	D
	E
	F

Name	Type	EWStreet	NSStreet	Group	Drop#	Area	AreaAddr	Channel	Sys Ref #	Last Change	FM Name
CLAIREMON 233New233		CLAIREMON RUFFIN		NONE	10	5	1	COM33:	27	#####	NONE

												Bar	
												Hour	Minute
												0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0
0	0	3	0	0	0	0	0	0	0	0	0	0	0
0	0	2	0	0	0	0	0	0	0	0	0	0	0
0	0	11	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0

Notes are in Column A, Rows 32 to 40


Column Numbers ---->		Overlap							
Row	Overlap Name ---->	1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8	Overlap Recall								
9	Queue Jump Phase								
A	Queue Jump Time								
B	Minimum Green								
C	Maximum Green								
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments <C+0+E=29>

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = Solid FDW on EV  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring

- Extra 2 Flags**  
 1 = AWB During Initial  
 2 = Reserved  
 3 = Disable Min Walk  
 4 = QuicNet System  
 5 = Ignore P/P on EV  
 6 = Manual Hold in FDW  
 7 = Allow QuicNet PE  
 8 = Flash Grn B4 Yellow

	C	Row
EV-A	0	0
EV-B	0	1
EV-C	0	2
EV-D	0	3
RR-1 *	---	4
RR-2 *	---	5
SE-1	0	6
SE-2	0	7
<b>Preempt Priority</b>		8
<C+0+E=125>		9
(* RR-1 is always Highest, and RR-2 is always Second Highest)		A
		B
		C
		D
		E
		F

Row	Column Numbers ---->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4 7
C	EV-C Phases	1 6
D	EV-D Phases	3 8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration <C+0+E=125>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	12345678
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	4
Ped for 8P Output	8
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	34

Configuration <C+0+E=125>

	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	12345678
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reserve	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	12345678
Start-up Ped Calls	12345678

Specials <C+0+F=2>

- Flash to PE & PE Non-Lock**  
 1 = EV A 5 = RR 1  
 2 = EV B 6 = RR 2  
 3 = EV C 7 = SE 1  
 4 = EV D 8 = SE 2

- IC Select Flags**  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 =  
 5 =  
 6 = Simplex Master  
 7 =  
 8 = Offset Interrupter

	2	Row
Phase 1	10	0
Phase 2	10	1
Phase 3	10	2
Phase 4	10	3
Phase 5	10	4
Phase 6	10	5
Phase 7	10	6
Phase 8	10	7
<b>Coordination Transition Minimums</b>		8
<C+0+C=5>		9
		A
		B
		C
		D
		E
		F

**INTERSECTION: CLAIREMONT MESA @ RUFFIN**

		Plan								
Column Numbers ---->		1	2	3	4	5	6	7	8	9
Row	Plan Name ---->									
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	71	86	89						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff	15	29	21						
4	Phase 4 - ForceOff	53	65	68						
5	Phase 5 - ForceOff	71	91	89						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff	15	27	30						
8	Phase 8 - ForceOff	53	65	68						
9	Ring Offset	0	0	0						
A	Offset 1	83	93	96						
B	Offset 2									
C	Offset 3									
D	Perm 1 - End	11	13	14						
E	Hold Release	255	255	255						
F	Reserved	0	0	0						

**Coordination - Bank 1** <C+0+C=1>

Row										
0	Ped Adjustment									
1	Perm 2 - Start									
2	Perm 2 - End									
3	Perm 3 - Start									
4	Perm 3 - End									
5	Reservice Time									
6	Reservice Phases									
7										
8	Pretimed Phases									
9	Max Recall									
A	Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
B	Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
C	Perm 2 Veh Phase									
D	Perm 2 Ped Phase									
E	Perm 3 Veh Phase									
F	Perm 3 Ped Phase									

**Coordination - Bank 2** <C+0+C=2>

Coord Extra  
 1 = Programmed WALK Time for Sync Phases  
 2 = Always Terminate Sync Phase Peds

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

	E	Row
		0
Plan 1 - Sync	2 6	1
Plan 2 - Sync	2 6	2
Plan 3 - Sync	2 6	3
Plan 4 - Sync		4
Plan 5 - Sync		5
Plan 6 - Sync		6
Plan 7 - Sync		7
Plan 8 - Sync		8
Plan 9 - Sync		9
NEMA Sync		A
NEMA Hold		B
		C
		D
		E
		F

**Sync Phases** <C+0+C=1>

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

	F	Row
Free Lag	2 4 6 8	0
Plan 1 - Lag	2 4 6 8	1
Plan 2 - Lag	2 4 6 8	2
Plan 3 - Lag	2 4 6 8	3
Plan 4 - Lag		4
Plan 5 - Lag		5
Plan 6 - Lag		6
Plan 7 - Lag		7
Plan 8 - Lag		8
Plan 9 - Lag		9
External Lag		A
Lag Hold		B
		C
		D
		E
		F

**Lag Phases** <C+0+C=1>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	One-Shot Timer	Latch 1 Set	NOT-3	Max 2	Pretimed	Set DOW	Dial 2 (7-Wire)	Sim Term	0
1	AND-5 (a)	Latch 1 Reset	NOT-4	Reserved	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A	1
2	AND-5 (b)	Latch 2 Set	OR-4 (a)	Reserved	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B	2
3	AND-6 (a)	Latch 2 Reset	OR-4 (b)	Reserved	Plan 3	Gate Down	Offset 2 (7-Wire)	EV-C	3
4	AND-6 (b)	NAND-3 (a)	OR-5 (a)	Reserved	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D	4
5	Reserved	NAND-3 (b)	OR-5 (b)	Reserved	Plan 5	Stop Time	Free (7-Wire)	RR-1	5
6	Reserved	NAND-4 (a)	OR-6 (a)	Reserved	Plan 6	Flash Sense	Flash (7-Wire)	RR-2	6
7	Reserved	NAND-4 (b)	OR-6 (b)	Reserved	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1	7
8	Spec. Funct. 1	OR-7 (a)	EXTMR	Reserved	Plan 8	Man. Advance	NOT-1	Spec. Event 2	8
9	Spec. Funct. 2	OR-7 (b)	Reserved	Max Inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag	9
A	Spec. Funct. 3	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)	A
B	Spec. Funct. 4	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)	B
C	Reserved	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)	C
D	Reserved	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)	D
E	Reserved	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)	E
F	Reserved	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)	F

Assignable Inputs

<C+0+E=126>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	Reserved	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)	0
1	Reserved	Phase ON - 2	Sp Evnt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)	1
2	Reserved	Phase ON - 3	Sp Evnt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)	2
3	Reserved	Phase ON - 4	Sp Evnt Out 3	EXTMR	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)	3
4	Reserved	Phase ON - 5	Sp Evnt Out 4	One-Shot Timer	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)	4
5	Reserved	Phase ON - 6	Sp Evnt Out 5	Reserved	Plan 5	AND-2	TOD Out 6	Free (7-Wire)	5
6	Reserved	Phase ON - 7	Sp Evnt Out 6	Latch 1	Plan 6	AND-3	TOD Out 7	Flash (7-Wire)	6
7	Reserved	Phase ON - 8	Sp Evnt Out 7	Latch 2	Plan 7	NOT-2	TOD Out 8	Preempt	7
8	Fih Yell Arrow 1	Ph. Check - 1	Sp Evnt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A	8
9	Green 1	Ph. Check - 2	Coord On	NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B	9
A	Fih Yell Arrow 3	Ph. Check - 3	Detector Fail	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C	A
B	Green 3	Ph. Check - 4	Spec. Funct. 1	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D	B
C	Fih Yell Arrow 5	Ph. Check - 5	Spec. Funct. 2	OR-6	NAND-3	RR-1	DELAY-C	AND-5	C
D	Green 5	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	AND-6	D
E	Fih Yell Arrow 7	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	Reserved	E
F	Green 7	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	Reserved	F

Assignable Outputs

<C+0+E=127>

INTERSECTION: CLAIREMONT MESA @ RUFFIN

		Phase							
Column Numbers ---->		1	2	3	4	5	6	7	8
Row	Phase Names ---->								
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Transition Type  
 0.X = Shortway  
 1.X = Lengthen  
 X.1 thru X.4 =  
 Number of  
 cycles when  
 lengthening

Transition Type  <C/5+1+9>  
**TBC Transition**

Hawk Select  F/1+0+4>  
**Hawk Select** 200 = Mid-Block, 201 = Hawk

Address  <C/1+0+6>  
 Select Parity  <C/1+0+5>  
**AB3418 Comm 2** 0 = No Parity, 1 = Even

Daylight Savings Date  
 If set to all zeros, standard dates will be used.

Begin Month  <C/5+2+A>  
 Begin Week  <C/5+2+B>  
 End Month  <C/5+2+C>  
 End Week  <C/5+2+D>

Daylight Savings Time

Time B4 Yellow  <F/1+C+E>  
 Phase Number  <F/1+C+F>  
**Advance Warning Beacon - Sign 1**

Time B4 Yellow   
 Phase Number  <F/1+D+F>  
**Advance Warning Beacon - Sign 2**

Offset Time  <C/5+2+E>  
 Max Cycle Time  <C/5+2+F>

Yellow Yield Coordination

Omit Alarm  12345678 <C/5+F+0>  
**Local Alarm Disable**

		Phase							
Column Numbers ---->		1	2	3	4	5	6	7	8
Row	Phase Names ---->								
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Column Numbers ---->		0	1	2	3	1	3
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0	Ph. 2 I-2 U	39	45_7	2	123		1.8
1	Ph. 6 J-2 U	40	45_7	6	123		1.8
2	Ph. 4 I-6 U	41	45_7	4	123		1.8
3	Ph. 8 J-6 U	42	45_7	8	123		1.8
4		43	45_7	2	123		
5		44	45_7	6	123		
6	Ph. 4 I-6 L	45	45_7	4	123	10.0	
7	Ph. 8 J-6 L	46	45_7	8	123	10.0	
8		47	67	2	123		
9		48	67	6	123		
A		49	67	4	123		
B		50	67	8	123		
C		55	45_7	5	123		
D		56	45_7	1	123		
E		57	45_7	7	123		
F		58	45_7	3	123		

Column Numbers ---->		4	5	6	7	2	4
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0		59	45_7	5	123		
1		60	45_7	1	123		
2		61	45_7	7	123		
3		62	45_7	3	123		
4		63	45_7	2	123		
5		64	45_7	6	123		
6		65	45_7	4	123		
7		66	45_7	8	123		
8		67	2	2	123		
9		68	2	6	123		
A		69	2	4	123		
B		70	2	8	123		
C		76	45_7	2	123		
D		77	45_7	6	123		
E		78	45_7	4	123		
F		79	45_7	8	123		

Detector Assignments <C+0+E=126>

<C+0+D=0>

Detector Attributes

- 1 = Full Time Delay
- 2 = Ped Call
- 3 = Overlap
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

Det. Assignments

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Column Numbers ---->		Ped / Phase / Overlap								Row
		1	2	3	4	5	6	7	8	
Walk										0
Don't Walk										1
Phase Green										2
Phase Yellow										3
Phase Red										4
Overlap Green										5
Overlap Yellow										6
Overlap Red										7

Redirect Phase Outputs <C+0+E=127>

Cabinet Type | 0 <E/125+D+0>

**Enable Redirection**  
(Enable Redirection = 30)

Max OFF (minutes) | 60 <D/0+0+1>

Max ON (minutes) | 5 <D/0+0+2>

Chatter Fail Time | 0 <D/0+0+4>

**Detector Failure Monitor**

	B	Row
One-Shot		8
Ext. Timer		9
DELAY-A		A
DELAY-B		B
DELAY-C		C
DELAY-D		D
DELAY-E		E
DELAY-F		F

**Delay Logic Times**  
<C+0+D=0> (seconds)



Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 1

<C+0+E=27>

Notes:  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_

0 <E/27+5+F>  
**Limited Service Interval**

Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 2

<C+0+E=28>

Notes:  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_  
 \_\_\_\_\_

0 <E/28+5+F>  
**Limited Service Interval**

**INTERSECTION: CLAIREMONT MESA @ RUFFIN**

Min Time (seconds)  <F/1+0+8>  
**Min Green Before PE Force Off**

Max Time (minutes)  <F/1+0+9>  
**Max Preempt Time Before Failure**

Min Time (seconds)  <F/1+0+A>  
**Min Time Between Same Preempts**  
 (Does Not Apply To Railroad Preempt)

Low Pri. Channel  <E/125+C+8>  
**Disable Low Priority Channel**

- Low Priority  
 1 = Channel A  
 2 = Channel B  
 3 = Channel C  
 4 = Channel D

Row		
C	Bus Headway	
D	Bus Delay	
E	Max Early Grn	
F	Max Grn Ext.	

**Priority Parameters**  
 <F/1 +A+Row>

Row	Time	Headway	Direction	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**Headway Schedule** <C+0+9=2.1>

Headway Time  
 (minutes)  
 1 thru 9 = 1 thru 9  
 A = 10  
 B = 11  
 C = 12  
 D = 13  
 E = 14  
 F = 15

**Low Priority Preemption (Bus Priority)**

Note: Also see "Time of Day Functions", Function E, Bit 5 (Disable Low Priority)

**INTERSECTION: CLAIREMONT MESA @ MURPHY CANYON**

Group Assignment: **NONE**  
 Field Master Assignment: **NONE**  
 System Reference Number: **28**

N/S Street Name: **MURPHY CANYON**  
 E/W Street Name: **CLAIREMONT MESA**

Last Database Change: **11/18/2015 12:58**

Change Record					
Change	By	Date	Change	By	Date
Original TS	STC	12/31/15			

Notes:

Manual Plan  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

Manual Offset  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

Drop Number	<b>11</b>	<C/0+0+0>
Zone Number	<b>11</b>	<C/0+0+1>
Area Number	<b>5</b>	<C/0+0+2>
Area Address	<b>34</b>	<C/0+0+3>
QuicNet Channel	COM33:	(QuicNet)

Manual Plan	<b>0</b>	<C/0+A+1>
Manual Offset	<b>0</b>	<C/0+B+1>

Flash Start	<b>0</b>	<F/1+0+E>
Red Revert	<b>5.0</b>	<F/1+0+F>
All Red Start	<b>0.0</b>	<F/1+C+0>
FYA Red Revert	<b>0.0</b>	<F/1+0+5>
OVLPG CHG Red	<b>0.0</b>	<F/1+0+3>

Exclusive Walk	<b>0</b>	<F/1+0+0>
Exclusive FDW	<b>0</b>	<F/1+0+1>
All Red Clear	<b>0.0</b>	<F/1+0+2>

**Communication Addresses**

**Manual Selection**

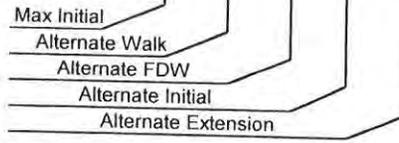
**Start / Revert Times**

**Exclusive Ped Phase**  
 (Outputs specified in Assignable  
 Outputs at E/127+A+E & F)

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk		7		4		7		
1	Ped FDW		15		30		11		
2	Min Green	4	10		4	4	10		
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	6.3		2.0	2.0	6.1		
6	Max Gap	2.0	6.3		2.0	2.0	6.1		
7	Min Gap	2.0	0.2		2.0	2.0	0.2		
8	Max Limit	30	60		40	30	60		
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW		1		1		1		
C	Cond Serv Check							1	
D	Reduce Every		0.5				0.5		
E	Yellow Change	3.4	5.1		4.2	3.4	4.2		
F	Red Clear	1.0	1.0		1.0	1.0	1.0		

**Phase Timing - Bank 1** <C+0+F=1>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					



**Alternate Timing** <C+0+F=1>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

**Preempt Timing**

	F	Row
Permit	<b>12_456</b>	<b>0</b>
Red Lock		<b>1</b>
Yellow Lock		<b>2</b>
Min Recall	<b>2_6</b>	<b>3</b>
Ped Recall		<b>4</b>
View Set Peds	-----	<b>5</b>
Rest In Walk		<b>6</b>
Red Rest		<b>7</b>
Dual Entry		<b>8</b>
Max Recall		<b>9</b>
Soft Recall		<b>A</b>
Max 2		<b>B</b>
Cond. Service		<b>C</b>
Man Cntrl Calls		<b>D</b>
Yellow Start	<b>2_6</b>	<b>E</b>
First Phases	<b>4</b>	<b>F</b>

**Phase Functions** <C+0+F=1>

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Name	Type	EWStreet	NSStreet	Group	Drop#	Area	AreaAddr	Channel	Sys Ref #	Last Change	FM Name
CLAIREMON 233	New	233	CLAIREMON	MURPHY C/	NONE	11	5	34	COM33:	28	##### NONE

																Bar		
																Hour	Minute	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	0	1	
Page 0 <C/5>																0	1	

Notes are in Column A, Rows 32 to 40


Row	Overlap Name →	Overlap							
		1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8	Overlap Recall								
9	Queue Jump Phase								
A	Queue Jump Time								
B	Minimum Green								
C	Maximum Green								
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments <C+0+E=29>

Extra 1 Flags

- 1 = TBC Type 1
- 2 = NEMA Ext. Coord
- 3 = Auto Daylight Savings
- 4 = Solid FDW on EV
- 5 = Extended Status
- 6 = International Ped
- 7 = Flash - Clear Outputs
- 8 = Split Ring

Extra 2 Flags

- 1 = AWB During Initial
- 2 = Reserved
- 3 = Disable Min Walk
- 4 = QuicNet System
- 5 = Ignore P/P on EV
- 6 = Manual Hold in FDW
- 7 = Allow QuicNet PE
- 8 = Flash Grn B4 Yellow

	C	Row
EV-A	0	0
EV-B	0	1
EV-C	0	2
EV-D	0	3
RR-1 *	---	4
RR-2 *	---	5
SE-1	0	6
SE-2	0	7

Preempt Priority

<C+0+E=125>  
 (\* RR-1 is always Highest, and RR-2 is always Second Highest)

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

Row	Column Numbers →	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	
C	EV-C Phases	1 6
D	EV-D Phases	
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration <C+0+E=125>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	12345678
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	4
Ped for 8P Output	
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	34

Configuration <C+0+E=125>

	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	12345678
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reservice	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	12345678
Start-up Ped Calls	12345678

Specials <C+0+F=2>

Flash to PE & PE Non-Lock

- 1 = EV A 5 = RR 1
- 2 = EV B 6 = RR 2
- 3 = EV C 7 = SE 1
- 4 = EV D 8 = SE 2

IC Select Flags

- 1 =
- 2 = Modem
- 3 = 7-Wire Slave
- 4 =
- 5 =
- 6 = Simplex Master
- 7 =
- 8 = Offset Interrupter

	2	Row
Phase 1	10	0
Phase 2	10	1
Phase 3	10	2
Phase 4	10	3
Phase 5	10	4
Phase 6	10	5
Phase 7	10	6
Phase 8	10	7

Coordination Transition Minimums

<C+0+C=5>

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

Column Numbers ---->		Plan								
Row	Plan Name ---->	1	2	3	4	5	6	7	8	9
0	Cycle Length	110	130	135						
1	Phase 1 - ForceOff	80	83	81						
2	Phase 2 - ForceOff	0	0	0						
3	Phase 3 - ForceOff	0	0	0						
4	Phase 4 - ForceOff	40	47	40						
5	Phase 5 - ForceOff	54	63	56						
6	Phase 6 - ForceOff	0	0	0						
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset	0	0	0						
A	Offset 1	97	127	119						
B	Offset 2									
C	Offset 3									
D	Perm 1 - End	11	13	14						
E	Hold Release	255	255	255						
F	Reserved	0	0	0						

Coordination - Bank 1 <C+0+C=1>

Row	Plan Name	1	2	3	4	5	6	7	8	9
0	Ped Adjustment									
1	Perm 2 - Start									
2	Perm 2 - End									
3	Perm 3 - Start									
4	Perm 3 - End									
5	Reservice Time									
6	Reservice Phases									
7										
8	Pretimed Phases									
9	Max Recall									
A	Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
B	Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
C	Perm 2 Veh Phase									
D	Perm 2 Ped Phase									
E	Perm 3 Veh Phase									
F	Perm 3 Ped Phase									

Coordination - Bank 2 <C+0+C=2>

Coord Extra  
1 = Programmed WALK Time for Sync Phases  
2 = Always Terminate Sync Phase Peds

Row	Plan Name	E	Row
0			0
1	Plan 1 - Sync	2 6	1
2	Plan 2 - Sync	2 6	2
3	Plan 3 - Sync	2 6	3
4	Plan 4 - Sync		4
5	Plan 5 - Sync		5
6	Plan 6 - Sync		6
7	Plan 7 - Sync		7
8	Plan 8 - Sync		8
9	Plan 9 - Sync		9
A	NEMA Sync		A
B	NEMA Hold		B
C			C
D			D
E			E
F			F

Sync Phases <C+0+C=1>

Row	Plan Name	F	Row
0	Free Lag	2 4 6	0
1	Plan 1 - Lag	2 4 6	1
2	Plan 2 - Lag	2 4 6	2
3	Plan 3 - Lag	2 4 6	3
4	Plan 4 - Lag		4
5	Plan 5 - Lag		5
6	Plan 6 - Lag		6
7	Plan 7 - Lag		7
8	Plan 8 - Lag		8
9	Plan 9 - Lag		9
A	External Lag		A
B	Lag Hold		B
C			C
D			D
E			E
F			F

Lag Phases <C+0+C=1>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	One-Shot Timer	Latch 1 Set	NOT-3	Max 2	Pretimed	Set DOW	Dial 2 (7-Wire)	Sim Term	0
1	AND-5 (a)	Latch 1 Reset	NOT-4	Reserved	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A	1
2	AND-5 (b)	Latch 2 Set	OR-4 (a)	Reserved	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B	71
3	AND-6 (a)	Latch 2 Reset	OR-4 (b)	Reserved	Plan 3	Gate Down	Offset 2 (7-Wire)	EV-C	72
4	AND-6 (b)	NAND-3 (a)	OR-5 (a)	Reserved	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D	73
5	Reserved	NAND-3 (b)	OR-5 (b)	Reserved	Plan 5	Stop Time	Free (7-Wire)	RR-1	74
6	Reserved	NAND-4 (a)	OR-6 (a)	Reserved	Plan 6	Flash Sense	Flash (7-Wire)	RR-2	82
7	Reserved	NAND-4 (b)	OR-6 (b)	Reserved	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1	81
8	Spec. Funct. 1	OR-7 (a)	EXTMR	Reserved	Plan 8	Man. Advance	NOT-1	Spec. Event 2	7
9	Spec. Funct. 2	OR-7 (b)	Reserved	Max Inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag	8
A	Spec. Funct. 3	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)	9
B	Spec. Funct. 4	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)	A
C	Reserved	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)	B
D	Reserved	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)	C
E	Reserved	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)	D
F	Reserved	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)	E

Assignable Inputs

<C+0+E=126>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	Reserved	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)	0
1	Reserved	Phase ON - 2	Sp Evt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)	1
2	Reserved	Phase ON - 3	Sp Evt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)	2
3	Reserved	Phase ON - 4	Sp Evt Out 3	EXTMR	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)	3
4	Reserved	Phase ON - 5	Sp Evt Out 4	One-Shot Timer	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)	4
5	Reserved	Phase ON - 6	Sp Evt Out 5	Reserved	Plan 5	AND-2	TOD Out 6	Free (7-Wire)	5
6	Reserved	Phase ON - 7	Sp Evt Out 6	Latch 1	Plan 6	AND-3	TOD Out 7	Flash (7-Wire)	6
7	Reserved	Phase ON - 8	Sp Evt Out 7	Latch 2	Plan 7	NOT-2	TOD Out 8	Preempt	7
8	Fih Yell Arrow 1	Ph. Check - 1	Sp Evt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A	8
9	Green 1	Ph. Check - 2	Coord On	NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B	9
A	Fih Yell Arrow 3	Ph. Check - 3	Detector Fail	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C	A
B	Green 3	Ph. Check - 4	Spec. Funct. 1	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D	B
C	Fih Yell Arrow 5	Ph. Check - 5	Spec. Funct. 2	OR-6	NAND-3	RR-1	DELAY-C	AND-5	C
D	Green 5	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	AND-6	D
E	Fih Yell Arrow 7	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	Reserved	E
F	Green 7	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	Reserved	F

Assignable Outputs

<C+0+E=127>

Column Numbers →		Phase							
Phase Names →		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Column Numbers →		Phase							
Phase Names →		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
Phase 1	---	---	---	---	---
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Transition Type  
 0.X = Shortway  
 1.X = Lengthen  
 X.1 thru X.4 =  
 Number of  
 cycles when  
 lengthening

Transition Type | 0.0 <C/5+1+9>

**TBC Transition**

Hawk Select | 0 F/1+0+4>  
**Hawk Select** 200 = Mid-Block, 201 = Hawk

Address | 0 <C/1+0+6>  
 Select Parity | 0 <C/1+0+5>  
**AB3418 Comm 2** 0 = No Parity, 1 = Even

Daylight Savings  
 Date  
 If set to all zeros,  
 standard dates  
 will be used.

Begin Month | 3 <C/5+2+A>  
 Begin Week | 2 <C/5+2+B>  
 End Month | 11 <C/5+2+C>  
 End Week | 1 <C/5+2+D>

**Daylight Savings Time**

Time B4 Yellow | 0.0 <F/1+C+E>  
 Phase Number | 0 <F/1+C+F>

**Advance Warning Beacon - Sign 1**

Time B4 Yellow | 0.0  
 Phase Number | 0 <F/1+D+F>

**Advance Warning Beacon - Sign 2**

Offset Time | 0 <C/5+2+E>  
 Max Cycle Time | 0 <C/5+2+F>

**Yellow Yield Coordination**

12345678  
 Omit Alarm | 12345678 <C/5+F+0>

**Local Alarm Disable**

INTERSECTION: CLAIREMONT MESA @ MURPHY CANYON

Column Numbers →		0	1	2	3	1	3
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0	Ph. 2 I-2 U	39	45 7	2	123		1.8
1	Ph. 6 J-2 U	40	45 7	6	123		1.8
2	Ph. 4 I-6 U	41	45 7	4	123		
3	Ph. 8 J-6 U	42	45 7	8	123	10.0	
4	Ph. 2 I-2 L	43	45 7	2	123		1.8
5	Ph. 6 J-2 L	44	45 7	6	123		1.8
6		45	45 7	4	123		
7		46	45 7	8	123		
8		47	67	2	123		
9		48	67	6	123		
A		49	67	4	123		
B		50	67	8	123		
C		55	45 7	5	123		
D		56	45 7	1	123		
E		57	45 7	7	123		
F		58	45 7	3	123		

Column Numbers →		Ped / Phase / Overlap								Row
		1	2	3	4	5	6	7	8	
Walk										0
Don't Walk										1
Phase Green										2
Phase Yellow										3
Phase Red										4
Overlap Green										5
Overlap Yellow										6
Overlap Red										7

Redirect Phase Outputs <C+0+E=127>

Cabinet Type 0 <E/125+D+0>

Enable Redirection  
(Enable Redirection = 30)

Max OFF (minutes) 60 <D/0+0+1>  
 Max ON (minutes) 5 <D/0+0+2>  
 Chatter Fail Time 0 <D/0+0+4>

Detector Failure Monitor

	B	Row
One-Shot		8
Ext. Timer		9
DELAY-A		A
DELAY-B		B
DELAY-C		C
DELAY-D		D
DELAY-E		E
DELAY-F		F

Delay Logic Times  
<C+0+D=0> (seconds)

Column Numbers →		4	5	6	7	2	4
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0		59	45 7	5	123		
1		60	45 7	1	123		
2		61	45 7	7	123		
3		62	45 7	3	123		
4	Ph. 2 I-3 U	63	45 7	2	123		1.8
5	Ph. 6 J-3 U	64	45 7	6	123		1.8
6		65	45 7	4	123		
7		66	45 7	8	123		
8		67	2	2	123		
9		68	2	6	123		
A		69	2	4	123		
B		70	2	8	123		
C		76	45 7	2	123		
D		77	45 7	6	123		
E		78	45 7	4	123		
F		79	45 7	8	123		

Detector Attributes  
 1 = Full Time Delay  
 2 = Ped Call  
 3 = Overlap  
 4 = Count  
 5 = Extension  
 6 = Type 3  
 7 = Calling  
 8 = Alternate

Det. Assignments  
 1 = Det. Set 1  
 2 = Det. Set 2  
 3 = Det. Set 3  
 4 =  
 5 =  
 6 = Failure - Min Recall  
 7 = Failure - Max Recall  
 8 = Report on Failure

Detector Assignments <C+0+E=126>

<C+0+D=0>



Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 1

<C+0+E=27>

0 <E/27+5+F>  
Limited Service Interval

Notes:

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Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omit	Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 2

<C+0+E=28>

0 <E/28+5+F>  
Limited Service Interval

Notes:

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Min Time (seconds)  <F/1+0+8>  
**Min Green Before PE Force Off**

Max Time (minutes)  <F/1+0+9>  
**Max Preempt Time Before Failure**

Min Time (seconds)  <F/1+0+A>  
**Min Time Between Same Preempts**  
 (Does Not Apply To Railroad Preempt)

Low Pri. Channel  <E/125+C+8>  
**Disable Low Priority Channel**

- Low Priority  
 1 = Channel A  
 2 = Channel B  
 3 = Channel C  
 4 = Channel D

Row		
C	Bus Headway	
D	Bus Delay	
E	Max Early Grn	
F	Max Grn Ext.	

**Priority Parameters**  
 <F/1 +A+Row>

Row	Time	Headway	Direction	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Headway Time  
 (minutes)  
 1 thru 9 = 1 thru 9  
 A = 10  
 B = 11  
 C = 12  
 D = 13  
 E = 14  
 F = 15

**Headway Schedule** <C+0+9=2.1>

**Low Priority Preemption (Bus Priority)**

Note: Also see "Time of Day Functions", Function E, Bit 5 (Disable Low Priority)



	CONTROL PLANS									Y-COORD		LAG PHASE		FLAG								
	1	2	3	4	5	6	7	8	9	C	D	E	F	1	2	3	4	5	6	7	8	
0 CYCLE LENGTH	80	60	80										LAG FZ FREE		2		4		6		8	0
1 FZ1 GRN FCTR	30	15	15									GAPOUT CP1	0	LAG FZ CP 1	2		4		6		8	1
2												GAPOUT CP2	0	LAG FZ CP 2	2		4		6		8	2
3 FZ3 GRN FCTR	0	0	0									GAPOUT CP3	0	LAG FZ CP 3	2		4		6		8	3
4 FZ4 GRN FCTR	12	12	18									GAPOUT CP4	0	LAG FZ CP 4					6		8	4
5 FZ5 GRN FCTR	0	0	0									GAPOUT CP5	1	LAG FZ CP 5								5
6												GAPOUT CP6	0	LAG FZ CP 6								6
7 FZ7 GRN FCTR	0	0	0									GAPOUT CP7		LAG FZ CP 7								7
8 FZ8 GRN FCTR	0	0	0									GAPOUT CP8		LAG FZ CP 8								8
9 MULTI CYCLE	0	0	0									GAPOUT CP9		LAG FZ CP 9								9
A OFFSET A	0	0	0											LAG C COORD								A
B OFFSET B														LAG D COORD								B
C OFFSET C														COORD FAZES		2			6			C
D FZ 3 EXT																						D
E FZ 7 EXT																						E
F OFFSET INTREPT																						F

FEATURE	OFF	ON	LOCATION	OFF	ON
C01 MANUAL CP			1		1
C02 MASTER CP			2		
C03 CURRENT CP			3		
C04 LAST CP			4		
C07 TRNSMT CP			5		
COD MANUAL OFFSET			6		
CAO LOCAL CYCLE TIMER			7		
CBO MASTER CYCLE TIMER			8		
CAA LOCAL OFFSET					
CBA MASTER OFFSET					

SYSTEM MASTER:  
 RTE 15 NB RAM

FEATURE	OFF	ON
1		1
2		
3		
4		
5		
6		
7		
8		

COO = 1

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

D PAGE

E PAGE

D	FLAGS								E	MIN	RCL	FLAGS								F	PED	RCL	FLAGS							
	1	2	3	4	5	6	7	8				1	2	3	4	5	6	7	8				1	2	3	4	5	6	7	8
MAX																														
RCL																														
0																														
1									CP 1	1							CP 1													
2									CP 2		1						CP 2													
3									CP 3								CP 3													
4									CP 4								CP 4													
5									CP 5								CP 5													
6									CP 6								CP 6													
7									CP 7								CP 7													
8									CP 8								CP 8													
9									CP 9								CP 9													
A																														
B																														
C																														
D																														
E																														
F																														

LAST POWER FAILURE REGISTER

E	FLAGS								F	FLAGS							
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
FUNCTION									FUNCTION								
0									CODE 4								
1									CODE 5								
2									C-RECALL								
3									D-RECALL								
4									EXCLUSIVE								
5									2 PED		2						
6									6 PED			6					
7									4 PED			4					
8									8 PED								
9																	
A	OLA NOT								OLA ON								
B	OLB NOT								OLB ON								
C	OLC NOT								OLC ON								
D	OLD NOT								OLD ON								
E																	
F																	

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES

(CALL ACTIVE LIGHTS)

RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES

(CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

D-E-E = C8 VERSION NUMBER

84 = BAD

85 = GOOD

MINUTE = D-B-F

DAY = D-C-F

HOUR = D-A-E

MINUTE = D-B-E

DAY = D-C-E

HOUR = D-A-E

MINUTE = D-B-E

DAY = D-C-E

HOUR = D-A-E

MINUTE = D-B-E

DAY = D-C-E

HOUR = D-A-E

MINUTE = D-B-E

DAY = D-C-E

TIME OF DAY ACTIVITY TABLE

7+EVENT+HR+MIN+ACT+"E"+ON/OFF+DOW LTS		ON/	S	M	T	W	T	F	S	
HR	MIN	ACT	OFF	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

ACTIVITY CODE

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN

C09 = 0 or 1

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S			
HR	MIN	CP	OS	1	2	3	4	5	6	7	
0	06	30	1	A	2	3	4	5	6	7	
1	09	00	2	A	1	2	3	4	5	6	7
2	15	00	3	A	2	3	4	5	6	7	
3	18	30	E	A	1	2	3	4	5	6	7
4											
5											
6											
7											
8											
9											
A											
B											
C											
D											
E											
F											

- 8 ENERGIZE AUX OUTPUT-YELLOW

- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION

C09 = 2

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		S	M	T	W	T	F	S		
HR	MIN	CP	OS	1	2	3	4	5	6	7
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

F+C+E+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.		PHASES		PHASES		PHASES	
		C	D	E	F		
			TYPE		TYPE		TYPE
0	I1	1	5,6	J1	5	5,6	
1	I2U	2	5,6	J2U	6	5,6	
2	I2L	2	5,6	J2L	6	5,6	
3	I3U	2	5,6	J3U	6	5,6	
4	I3L	2	5	J3L	6	5	
5	I4	2	7,8	J4	6	7,8	
6	I5	3	5,6	J5	7	5,6	
7	I6U	4	5,6	J6U	8	5,6	
8	I6L	4	5,6	J6L	8	5,6	
9	I7U	4	5,6	J7U	8	5,6	
A	I7L	4	5	J7L	8	5	
B	I8	4	7,8	J8	8	7,8	
C	I9U	1	5,6	J9U	5	5,6	
D	I9L	3	5,6	J9L	7	5,6	

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

DETECTOR SETTINGS			
I FILE		J FILE	
DELAY	CARRYOVER	DELAY	CARRYOVER
I1	D10	D30	D40
I2U	D11	D31	D41
I2L	D12	D32	D42
I3U	D13	D33	D43
I3L	D14	D34	D44
I4	D15	D35	D45
I5	D16	D36	D46
I6U	D17	D37	D47
I6L	D18	D38	D48
I7U	D19	D39	D49
I7L	D1A	D3A	D4A
I8	D1B	D3B	D4B
I9U	D1C	D3C	D4C
I9L	D1D	D3D	D4D

INTERVAL	PHASE TIMING									PRE-EMPTION	F										
	1	2	3	4	5	6	7	8	9		E	FLAGS	1	2	3	4	5	6	7	8	
0 WALK	1	7	1	1	1	7	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1 DONT WALK	1	17	1	1	1	17	1	1	RR1 CLR	5	RED LOCK				4	5				1	
2 MIN GREEN	1	10	1	5	5	10	1	1	EVA DLY	0	YEL LOCK									2	
3 TYPE 3 DET	0	0	0	0	0	0	0	0	EVA CLR	5	V RECALL		2				6			3	
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	EVB DLY	0	P RECALL									4	
5 PASSAGE	0.9	2.0	0.9	2.0	2.0	2.0	0.9	0.9	EVB CLR	5	PED PHASES		2				6			5	
6 MAX GAP	0.9	2.0	0.9	2.0	2.0	2.0	0.9	0.9	EVC DLY	0	RT OLA									6	
7 MIN GAP	0.9	2.0	0.9	2.0	2.0	2.0	0.9	0.9	EVC CLR	5	RT OLB									7	
8 MAX EXT	9	25	9	30	20	25	9	9	EVD DLY	0	DBL ENTRY									8	
9 MAX 2									YR	EVD CLR	5	MAX 2 PHASES								9	
A MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A
B									DAY	RR2 CLR	5	RED REST								B	
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK								C	
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES								D	
E YELLOW	3.0	5.5	3.0	4.1	3.7	5.5	3.0	3.0	MIN			YEL START UP		2				6		E	
F RED	0.0	1.0	0.0	2.0	1.0	1.0	0.0	0.0	SEC			FIRST PHASE				4				F	
PED XING FT		59				59							1	2	3	4	5	6	7	8	
BIKE XING FT		75				82															

ENTRIES IN THESE LOCATIONS ARE NOT TO BE CHANGED



ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

*AM Noon PM*

		CONTROL PLANS									Y-COORD			LAG PHASE	FLAGS										
		1	2	3	4	5	6	7	8	9		C	D	E	F	1	2	3	4	5	6	7	8		
0	CYCLE LENGTH	80	60	80											LAG FZ FREE		2		4		6		8	0	
1	FZ1 GRN FCTR	0	0	0										GAPOUT CP1	0	LAG FZ CP 1		2		4		6		8	1
2														GAPOUT CP2	0	LAG FZ CP 2		2		4	5		8	2	
3	FZ3 GRN FCTR	0	0	0										GAPOUT CP3	0	LAG FZ CP 3		2		4		6		8	3
4	FZ4 GRN FCTR	35	15	15										PERM TIME		GAPOUT CP4	1	LAG FZ CP 4							4
5	FZ5 GRN FCTR	10	12	25										LAG OFFSET		GAPOUT CP5	1	LAG FZ CP 5							5
6														FORCE OFF		GAPOUT CP6	0	LAG FZ CP 6							6
7	FZ7 GRN FCTR	0	0	0										LONG GRN		GAPOUT CP7		LAG FZ CP 7							7
8	FZ8 GRN FCTR	0	0	0										NO GREEN		GAPOUT CP8		LAG FZ CP 8							8
9	MULTI CYCLE	0	0	0												GAPOUT CP9		LAG FZ CP 9							9
A	OFFSET A	40	0	40										OFFSET		LAG C COORD									A
B	OFFSET B															LAG D COORD									B
C	OFFSET C															COORD FAZES		2				6			C
D	FZ 3 EXT																								D
E	FZ 7 EXT																								E
F	OFFSET INTRPT																								F

- CO1 MANUAL CP
- CO2 MASTER CP
- CO3 CURRENT CP
- CO4 LAST CP
- CO7 TRNSMT CP
- COD MANUAL OFFSET
- CAO LOCAL CYCLE TIMER
- CBO MASTER CYCLE TIMER
- CAA LOCAL OFFSET
- CBA MASTER OFFSET

**SYSTEM MASTER:**  
RTE 15 RAMP

FEATURE	OFF	ON
1		
2		
3		
4		
5		
6		
7		
8		

LOCATION	OFF	ON
1	0	
2		1
3	0	
4	0	
5	0	
6	0	
7	0	
8	0	

COO = 2

- CCB/CDB OFFSET TIMER
- CCC/CDC LAG GREEN TIMER
- CCD/CDD FORCE OFF TIMER
- CCE/CDE LONG GREEN TIMER
- CCF/CDF NO GREEN TIMER

	D	FLAGS								E	FLAGS								F	FLAGS							
	MAX	1	2	3	4	5	6	7	8	MIN	1	2	3	4	5	6	7	8	PED	1	2	3	4	5	6	7	8
0	RCL								RCL										RCL								
1	CP 1								CP 1				5						CP 1								
2	CP 2								CP 2				4	5					CP 2								
3	CP 3								CP 3				4	5					CP 3								
4	CP 4								CP 4										CP 4								
5	CP 5								CP 5										CP 5								
6	CP 6								CP 6										CP 6								
7	CP 7								CP 7										CP 7								
8	CP 8								CP 8										CP 8								
9	CP 9								CP 9										CP 9								
A																			RCL 1								
B																			RCL 2								
C																											
D																											
E																											
F																											

	E	FLAGS								F	FLAGS							
	FUNCTION	1	2	3	4	5	6	7	8	FUNCTION	1	2	3	4	5	6	7	8
0										CODE 4								
1										CODE 5								
2										C-RECALL								
3										D-RECALL								
4										EXCLUSIVE								
5										2 PED		2						
6										6 PED						6		
7										4 PED				4				
8										8 PED								8
9																		
A	OLA NOT									OLA ON								
B	OLB NOT									OLB ON								
C	OLC NOT									OLC ON								
D	OLD NOT									OLD ON								
E																		
F																		

**LAST POWER FAILURE REGISTER**

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

**LAST FLASH TIME REGISTER**

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.											
		PHASES		TYPE				PHASES		TYPE	
		C		D				E		F	
0	I1	1		5,6		J1	5		5,6		
1	I2U	2		5,6		J2U	6		5,6		
2	I2L	2		5,6		J2L	6		5,6		
3	I3U	2		5,6		J3U	6		5,6		
4	I3L	2		5	5,6	J3L	6		5	5,6	
5	I4	2		7,8		J4	6		7,8		
6	I5	3		5,6		J5	7		5,6		
7	I6U	4		5,6		J6U	8		5,6		
8	I6L	4		5,6		J6L	8		5,6		
9	I7U	4		5,6		J7U	8		5,6		
A	I7L	4		5		J7L	8		5		
B	I8	4		7,8		J8	8		7,8		
C	I9U	1		5,6		J9U	5		5,6		
D	I9L	3		5,6		J9L	7		5,6		

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS									
I FILE					J FILE				
DELAY		CARRYOVER			DELAY		CARRYOVER		
I1	D10		D30		J1	D20		D40	
I2U	D11		D31		J2U	D21		D41	
I2L	D12		D32		J2L	D22		D42	
I3U	D13		D33	3.0	J3U	D23		D43	3.0
I3L	D14		D34	3.0	J3L	D24		D44	3.0
I4	D15		D35		J4	D25		D45	
I5	D16		D36		J5	D26		D46	
I6U	D17		D37		J6U	D27		D47	
I6L	D18		D38		J6L	D28		D48	
I7U	D19	10.0	D39		J7U	D29		D49	
I7L	D1A		D3A		J7L	D2A		D4A	
I8	D1B		D3B		J8	D2B		D4B	
I9U	D1C		D3C		J9U	D2C		D4C	
I9L	D1D		D3D		J9L	D2D		D4D	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

# INTERSECTION: CONVOY ST & RONSON RD

Group Assignment: 4014  
Field Master Assignment: NONE

N/S -at Name: CONVOY ST  
EW Street Name: RONSON RD

223 Program

Last Database Change: 07/25/2 J-51  
System Ref. Number: 254  
Drawing Number: 27576-9  
Timing Implemented On: 12/19/01

Timing Sheet By: MMH  
Approved By: MM

Column # ->  
Phase # ->

Row	1	2	3	4	5	6	7	8
0	Ped Walk	7		7				
1	Ped FDW	13		23		14		
2	Min Green	4		4		7		
3	Type 3 Limit							
4	Add/Veh							
5	Veh Extn	2.0		2.0		3.2		
6	Max Gap	2.0		2.0		3.2		
7	Min Gap	2.0		2.0		0.2		
8	Max Limit	30		30		45		
9	Max Limit 2							
A	Bus Adv							
B	Call to Phs							
C	Reduce By	0.1				0.1		
D	Every	1.0				1.0		
E	Yellow	3.4		3.6		3.4		3.0
F	Red Clear	1.0		1.0		1.0		1.0

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0
Start / Revert Times		
Drop Number	3	C + 0 + 0
Zone Number	3	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	10	C + 0 + 3
QuickNet Channel	DIG134	(QuickNet)
Communication Addresses		
C + F + 0	F	Row
Free Lag	2 4 6	0

Lag Phases <C Page>

Overlap Timing

Row	9	C	D	0
Overlap A	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap B				
Overlap C				
Overlap D				

<F Page>

F + COLOR + D + 0 + OVERLAP

Downtime Flash 255 (minutes)

Downtime Before Auto Manual Flash

F + 0 + 8

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Permit	12	456														
Red Lock																
Yellow Lock																
Min Recall																
Ped Recall																
Peds (View)																
Rest In Walk																
Red Rest																
Dbl Entry																
Max Recall																
Soft Recall																
Max 2																
Cond Serv																
Ped Lock																12345678
Yellow Start																2 6
1st Phases																4

Phase Functions <F Page>

F + F + Row

Preempt Timing

F + E + Row

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Disable Ports	234
---------------	-----

Disable Communication Ports

D + D + 9

30

3

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:	:	:	:
1	:	:	:	:
2	:	:	:	:
3	:	:	:	:
4	:	:	:	:
5	:	:	:	:
6	:	:	:	:
7	:	:	:	:
8	:	:	:	:
9	:	:	:	:
A	:	:	:	:
B	:	:	:	:
C	:	:	:	:
D	:	:	:	:
E	:	:	:	:
F	:	:	:	:

TOD Function

7 + ROW

<D Page>

D + F + ROW

- I.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row	Exclusive Phases	IC Select Flags
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		2 5
B		
C		1 6
D		
E		1 3 4 5
F		2

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Row	Function	Day of Week	Column F Phases/Bits
0			
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		6
7	Ped 4P		4
8	Ped 8P		
9	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		

Configuration

E + F + ROW

<E Page>

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday  
 8 =

- Assign 5 Outputs**  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Time and Date

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Disable Parity

0

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program  
 C + C + F = version

Remote Download

- C + 0 + 4 = 1 - 255  
 w/ E + E + E bit 5 on

Coordination Training By: **MXM**  
 Implemented On: 8/10/98

**FOR OBSERVATION ONLY**  
 Master Plan C + A + 2  
 Current Plan C + A + 3  
 Next Plan C + A + 4  
 T.O.D. Plan C + A + 5  
 Master Cycle C + A + 0  
 Ring A Cycle C + B + 0  
 Ring B Cycle C + D + 0  
 Min Cycle C + A + E  
 Max Cycle C + B + E

Row	Plan Name	1	2	3	4	5	6	7	8	9
0	Cycle Length		100							
1	Phase 1 - ForceOff		44							
2	Phase 2 - ForceOff									
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff		30							
5	Phase 5 - ForceOff		44							
6	Phase 6 - ForceOff									
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A		67							
B	Offset B		67							
C	Offset C									
D	Permissive		10							
E	Hold Release		255							
F	Ped Shift		5							

Coordination  
 C + Plan + ROW

<C Page>

Row	Time	Plan	Offset	Day of Week
0	09 : 00	2	A	23456
1	11 : 30	2	B	23456
2	13 : 30	2	A	23456
3	18 : 00	E	A	1234567
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Coordination  
 <9 Key with C+0+9=1>

Plan Selected  
 1 thru 9 = Coordination  
 Plan 1 thru 9  
 14 or E = Free  
 15 or F = Flash

Plan	E	ROW	F
Plan 1		0	2 4 6
Plan 2	2 6	1	2 4 6
Plan 3		2	2 4 6
Plan 4		3	
Plan 5		4	
Plan 6		5	
Plan 7		6	
Plan 8		7	
Plan 9		8	
Coord Ped*		9	
NEMA Hold		A	
		B	
		C	
		D	
		E	
		F	

Sync Phases  
 C + E + FUNCTION #

Lag Phases  
 <C Page>  
 C + F + FUNCTION #

Transition Type  
 TBC Transition  
 C + D + D  
 0  
 Transition Type  
 0 = Shortway  
 Non-zero = Lengthen

Row	Delay	Carry-over	Detector Name	332 Input File	Detector Number
0				111	14
1		1.8		212U	1
2				212L	5
3				213U	21
4				213L	25
5				214	9
6				315	16
7				416U	3
8				416L	7
9				417U	23
A				417L	27
B				418	11
C				119U	18
D				319L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	Delay	Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1		1.8		6J2U	2
2				6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- --	2345

Active Detectors <D Page>

Row	System Det. #	Detector #
0	System Det. # 1	0
1	System Det. # 2	
2	System Det. # 3	
3	System Det. # 4	
4	System Det. # 5	
5	System Det. # 6	
6	System Det. # 7	
7	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor		
Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1		
Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2		
Phase Number		F+D+1
Time Before Yellow		F+D+3

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

Detector Delay & Carryover <D Page>  
D + X (across) + ROW

# INTERSECTION: KEARNY VILLA & LIGHTWAVE

Group Assignment: NONE  
Field Master Assignment: NONE

NS Street Name: KEARNY VILLA  
EW Street Name: LIGHTWAVE

Timing Sheet By: JMW  
Approved By:

Last Database Change: /2000 11:42  
System Ref. Number: 582  
Drawing Number: 29698-32-D  
Timing Implemented On: #####

Program

Row	1	2	3	4	5	6	7	8
Ped Walk	0	7	0	0	0	0	0	7
Ped FDW	0	18	0	0	0	0	0	16
Min Green	4	10	0	0	0	10	0	4
Type 3 Limit	0	0	0	0	0	0	0	0
Add/Veh	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Veh Extn	2.0	4.2	0.0	0.0	0.0	4.2	0.0	2.0
Max Gap	2.0	4.2	0.0	0.0	0.0	4.2	0.0	2.0
Min Gap	2.0	0.2	0.0	0.0	0.0	0.2	0.0	2.0
Max Limit	30	60	0	0	0	60	0	40
Max Limit 2	0	0	0	0	0	0	0	0
Bus Adv	0	0	0	0	0	0	0	0
Call to Phs	6	6	0	0	0	2	0	0
Reduce By	0.0	0.1	0.0	0.0	0.0	0.1	0.0	0.0
Every	0.0	0.8	0.0	0.0	0.0	0.8	0.0	0.0
Yellow	3.0	4.4	0.0	0.0	0.0	4.4	0.0	3.0
Red Clear	1.0	1.0	1.0	0.0	0.0	1.0	0.0	1.0

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

Max Initial	0
Red Revert	5.0
All Red Start	0.0
Start / Revert Times	
Drop Number	6
Zone Number	6
Area Number	5
Area Address	57
QuicNet Channel	DIG134:

Communication Addresses

C + F + O	Row
Free Lag	0
Lag Phases	<C Page>

Row	A	B	C	D	E	F
Overlap A	0.0	0.0	0.0	0.0	0.0	0.0
Overlap B	0.0	0.0	0.0	0.0	0.0	0.0
Overlap C	0.0	0.0	0.0	0.0	0.0	0.0
Overlap D	0.0	0.0	0.0	0.0	0.0	0.0
Overlap Timing	<F Page>					
F + COLOR +	D + 0 + OVERLAP					

Overlap Timing <F Page>

D + 0 + OVERLAP

Downtime Flash	60	(minutes)
Downtime Before Auto Manual Flash		
F + 0 + 8		

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
RR-1 Delay	0															
RR-1 Clear	0															
EV-A Delay	0															
EV-A Clear	0															
EV-B Delay	0															
EV-B Clear	0															
EV-C Delay	0															
EV-C Clear	0															
EV-D Delay	0															
EV-D Clear	0															
RR-2 Delay	0															
RR-2 Clear	0															
View EV Delay	...															
View EV Clear	...															
View RR Delay	...															
View RR Clear	...															

Preempt Timing  
F + E + Row

Phase Functions  
F + F + Row

Manual Plan	14
Manual Offset	

Manual Selection

- Manual Plan
- 0 = Automatic
- 1-9 = Plan 1-9
- 14 = Free
- 15 = Flash
- Manual Offset
- 0 =
- Automatic
- 1 = Offset A
- 2 = Offset B
- 3 = Offset C

31

Disable Ports	234
Disable Communication Ports	
D + D + 9	

# INTERSECTION: KEARNY VILLA & LIGHTWAVE

Group Assignment: NONE  
 Field Master Assignment: NONE  
 N/S Street Name: KEARNY VILLA  
 E/W Street Name: LIGHTWAVE

Training Sheet By: JMV  
 Approved By: \_\_\_\_\_  
 Last Database Change: 09/15/2000 11:42  
 System Rel. Number: 582  
 Drawing Number: 29598-32-D  
 Timing Implemented On: #####

Row	Column # →	1	2	3	4	5	6	7	8
0	Ped Walk	0	7	0	0	0	0	0	7
1	Ped FDW	0	18	0	0	0	0	0	16
2	Min Green	4	10	0	0	0	10	0	4
3	Type 3 Limit	0	0	0	0	0	0	0	0
4	Add/Veh	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
5	Veh Extn	2.0	4.2	0.0	0.0	0.0	4.2	0.0	2.0
6	Max Gap	2.0	4.2	0.0	0.0	0.0	4.2	0.0	2.0
7	Min Gap	2.0	0.2	0.0	0.0	0.0	0.2	0.0	2.0
8	Max Limit	30	60	0	0	0	60	0	40
9	Max Limit 2	0	0	0	0	0	0	0	0
A	Bus Adv	0	0	0	0	0	0	0	0
B	Call to Phs	6	6	0	0	0	2	0	0
C	Reduce By	0.0	0.1	0.0	0.0	0.0	0.1	0.0	0.0
D	Every	0.0	0.8	0.0	0.0	0.0	0.8	0.0	0.0
E	Yellow	0.0	4.4	0.0	0.0	0.0	4.4	0.0	0.0
F	Red Clear	1.0	1.0	1.0	0.0	0.0	1.0	0.0	1.0

Phase Timing - Bank 1  
 F + Phase + Row

Row	0	1	2	3	4	5	6	7	8
0	Permit	12	6	8					
1	Red Lock								
2	Yellow Lock								
3	Min Recall	2	6						
4	Ped Recall								
5	Pods (View)	2	6						
6	Rest In Wal								
7	Red Rest								
8	Dbl Entry								
9	Max Recall								
A	Soft Recall	2	6						
B	Max 2								
C	Cond Serv								
D	Ped Lock	12345678							
E	Yellow Start	2	6						
F	1st Phases								8

Preempt Timing  
 F + E + Row

Phase Functions  
 F + F + Row

Max Initial	0	F+0+E
Red Revert	5.0	F+0+F
All Red Start	0.0	F+C+O

Start / Revert Times	6	C+0+0
Drop Number	6	C+0+1
Zone Number	5	C+0+2
Area Number	57	C+0+3
Area Address		
QuickNet Channel	DIGIS4	(QuickNet)

Row	A	B	C	D	D	D
Overlap A	0.0	0.0	0.0	0.0	0.0	0
Overlap B	0.0	0.0	0.0	0.0	0.0	0
Overlap C	0.0	0.0	0.0	0.0	0.0	0
Overlap D	0.0	0.0	0.0	0.0	0.0	0

Manual Plan	14	C+A+1
Manual Offset	0	C+B+1

Manual Selection	Manual Offset
Manual Plan	0 = Automatic
1-9 = Plan 1-9	1 = Offset A
14 = Free	2 = Offset B
15 = Flash	3 = Offset C

Communication Addresses  
 C + F + O      Row  
 \_2\_4\_6\_8      0  
 Lag Phases      <C Page>

Overlap Timing  
 F + COLOR +  
 Downtime Flash      255  
 60 (minutes)  
 Downtime Before Auto Manual Flash  
 F + 0 + 8

Unsubtr Totals      234  
 Disable Communication Ports  
 D + D + 9

Row	Delay	Carry-over
0	0.0	0.0
1	0.0	1.8
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	0.0	0.0
8	0.0	0.0
9	0.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers
A	1 2 3 4 5 6 7 8
B	9 10 11 12 -- -- -- -- --
C	13 14 15 16 17 18 19 20
D	-- -- -- -- 21 22 23 24
E	-- -- -- -- -- -- -- --
F	-- 25 26 27 28 -- -- --

Row	Detector #
0	0
1	System Det. # 1
2	System Det. # 2
3	System Det. # 3
4	System Det. # 4
5	System Det. # 5
6	System Det. # 6
7	System Det. # 7
8	System Det. # 8

Active Detectors <D Page>

Row	Detector #
0	0
1	System Det. # 1
2	System Det. # 2
3	System Det. # 3
4	System Det. # 4
5	System Det. # 5
6	System Det. # 6
7	System Det. # 7
8	System Det. # 8

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)  
(These parameters are NOT downloaded)

Row	Delay	Carry-over
0	0.0	0.0
1	0.0	1.8
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	10.0	0.0
8	0.0	0.0
9	0.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

SECTION: Lightwave Av & Overland Av

Group Assignment: None  
 Field Master Assignment: None  
 N/S Street Name: Overland Av  
 EW Street Name: Lightwave Av  
 Drawing Number: 29738-42-J  
 System Ref. Number:  
 Timing implemented on:

Last Change:  
 Timing Sheet By: JMV  
 Approved By:

Row	Lightwave		Overland		Phase		Lightwave		Overland	
	1	2	3	4	5	6	7	8	9	10
0	←	←...→	↶	↷	↶	↷	↶	↷	↶	↷
1		7		7		7		7		7
2		20		19		20		19		20
3	4	7	4	7	4	7	4	7	4	7
4										
5	2.0	3.3	2.0	3.3	2.0	3.3	2.0	3.3	2.0	3.3
6	2.0	3.3	2.0	3.3	2.0	3.3	2.0	3.3	2.0	3.3
7	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
8	30	60	30	60	30	60	30	60	30	60
9										
A										
B										
C		0.1		0.1		0.1		0.1		0.1
D		1.0		1.0		1.0		1.0		1.0
E	0.3.4 3.0	0.3.4 3.6	0.3.4 3.0	0.3.4 3.6	0.3.4 3.0	0.3.4 3.6	0.3.4 3.0	0.3.4 3.6	0.3.4 3.0	0.3.4 3.6
F	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 1  
 F + Phase + Row  
 <F Page>

Row	A	B	C	D
Manual Plan				
Manual Offset				
Manual Selection				
Manual Plan	0 = Automatic	1 = Offset A	2 = Offset B	3 = Offset C
Manual Offset	14	C + A + 1	0	C + B + 1

Row	9	C	D	0
Green Clear				
Yellow Change				
Red Clear				
Load Switch #				

Row	F	Row
Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O
Start / Revert Times		
Drop Number	14	C + 0 + 0
Zone Number	14	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	111	C + 0 + 3
QuickNet Channel	D16133	(QuickNet)

Phase Functions  
 F + F + Row  
 <F Page>

Preempt Timing  
 F + E + Row  
 <F Page>

Phase Timing - Bank 1  
 F + COLOR +  
 D + 0 + OVERLAP  
 <D Page>

Communication Addresses  
 C + F + O  
 Free Lag

Overlap Timing  
 F + COLOR +  
 D + 0 + OVERLAP  
 <D Page>

Communication Addresses  
 C + F + O  
 Free Lag

Disable Ports:

Disable Communications Ports:

Downtime Flash:  (minutes)

Downtime Before Auto Manual Flash:

F + 0 + 8

Lag Phases:

Row:

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function  
7 + ROW  
<D Page>  
D + F + ROW

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Spill Monitor  
 F = Output Bits 1 thru 4

Row	Function	Day of Week	Column F Phases/Bits
0			
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Pad 2P		2
6	Pad 6P		6
7	Pad 4P		4
8	Pad 8P		8
9	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		

Configuration  
E + F + ROW  
<E Page>

- Day of Week  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

- Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Onlit		
6	Overlap B - Green Onlit		
7	Overlap C - Green Onlit		
8	Overlap D - Green Onlit		
9	Overlap Yellow Flash		
A	EVA Phases		2 5
B	EV-B Phases		4 7
C	EV-C Phases		1 6
D	EV-D Phases		3 8
E	Extra 1 Config. Bits		1 3 4 5 8
F	IC Select (Interconnect)		2

Configuration  
E + E + ROW

For access, set F + 9 + E = 1

Time and Date  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds  
 Program Information  
 C + C + 0 = program  
 7 = 7-Wire Master  
 C + C + F = version  
 Remote Download  
 C + 0 + 4 = 1 .255  
 w/ E + E + E bill 5 on  
 Disable Parity 0  
 Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)  
 (This parameter is NOT downloaded)  
 D+B+0



LIGHTHOUSE AV

**INTERSECTION: RUFFIN CT & RUFFIN RD**

Group Assignment: NONE  
Field Master Assignment: NONE  
N/S Street Name: RUFFIN RD  
E/W Street Name: RUFFIN CT

**Program**

Last Database Change: 09/07/2000 17:01  
Timing Sheet By: JMV  
System Ref. Number: 585  
Approved By:  
Drawing Number: 29739-44-D  
Timing Implemented On:

Row	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ped Walk	0	7	0	7	0	7	0	7	0	7	0	7	0	7	0
Ped FDW	0	13	0	24	0	21	0	27	0	27	0	27	0	27	0
Min Green	4	10	4	4	4	10	4	4	4	4	4	4	4	4	4
Type 3 Limit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Add/Veh	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Veh Extn	2.0	4.0	2.0	3.4	2.0	4.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Max Gap	2.0	4.0	2.0	3.4	2.0	4.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Max Limit	30	60	30	40	30	60	30	40	30	40	30	40	30	40	30
Max Limit 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Bus Adv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Call to Phs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Reduce By	0.0	0.1	0.0	0.1	0.0	0.1	0.0	0.1	0.0	0.1	0.0	0.1	0.0	0.1	0.0
Every	0.0	0.8	0.0	1.0	0.0	0.8	0.0	0.0	0.0	0.8	0.0	0.0	0.0	0.0	0.0
Yellow	3.4	3.0	4.7	4.5	3.4	3.0	4.3	4.5	3.4	3.0	3.9	3.0	3.9	3.0	3.0
Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O
Start / Revert Times		
Drop Number	13	C + 0 + 0
Zone Number	13	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	56	C + 0 + 3
QuickNet Channel	DIG133	(QuickNet)
Communication Addresses		
C + F + O	F	Row
Free Lag	2_4_6_8	0
Lag Phases		<C Page>

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1
Manual Selection		
Manual Plan		Manual Offset
0 = Automatic		0 =
1-9 = Plan 1-9		1 = Offset A
14 = Free		2 = Offset B
15 = Flash		3 = Offset C
Overlap A	0.0	0.0
Overlap B	0.0	0.0
Overlap C	0.0	0.0
Overlap D	0.0	0.0
Overlap Timing	<F Page>	<D Page>
F + COLOR +		D + 0 + OVERLAP
Downtime Flash	60	(minutes)
Downtime Before Auto Manual Flash		
F + 0 + 8		

RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0	View RR Clear	...
RR-1 Delay	0	RR-1 Clear	0
RR-2 Delay	0	RR-2 Clear	0
EV-A Delay	0	View EV Delay	...
EV-B Delay	0	View EV Clear	...
EV-C Delay	0	View RR Delay	...
EV-D Delay	0		

Row	Time	Function	Day of Week	Phases/Bits
0	00 : 00	0		
1	00 : 00	0		
2	00 : 00	0		
3	00 : 00	0		
4	00 : 00	0		
5	00 : 00	0		
6	00 : 00	0		
7	00 : 00	0		
8	00 : 00	0		
9	00 : 00	0		
A	00 : 00	0		
B	00 : 00	0		
C	00 : 00	0		
D	00 : 00	0		
E	00 : 00	0		
F	00 : 00	0		

TOD Function

7 + ROW

<D Page>  
D + F + ROW

T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Spill Monitor  
 F = Output Bits 1 thru 4

Row	Configuration	<E Page>
0		F
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	45
B	Overlap B - Phases	67
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	1

Configuration  
E + F + ROW

Row	Function	Day of Week	Phases/Bits
0	Exclusive Phases		E
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Onrl		4
6	Overlap B - Green Onrl		6
7	Overlap C - Green Onrl		
8	Overlap D - Green Onrl		
9	Overlap Yellow Flash		
A	EV/A Phases		2 5
B	EV/B Phases		4 7
C	EV/C Phases		1 6
D	EV/D Phases		3 8
E	Extra 1 Config Bits		1 345
F	IC Select (Interconnect)		2

Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Spill Ring Operation

IC Select Flags  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Fur accus, bol F + U + L - 1

Configuration  
E + L + KUW

Day of Week  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Time and Date  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Program Information  
 C + C + 0 = program  
 7 = 7-Wire Master  
 C + C + F = version

Remote Download  
 C + 0 + 4 = 1-255  
 w/ E + E + E bit 5 on

Disable Parity  0  D+B+0  
 Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)  
 (This parameter is NOT downloaded)

Row	1	3
0	Delay	Carry-over
1	0.0	0.0
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	0.0	1.8
8	0.0	0.0
9	0.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	A	B	C	D	E	F
1	2	3	4	5	6	7
9	10	11	12	--	--	--
13	14	15	16	17	18	19
--	--	--	21	22	23	24
--	--	--	--	--	--	--
--	25	26	27	28	--	--

Detector Numbers	E
12345678	12345678
1234	1234
12345678	12345678
5678	5678
1234	1234
2345	2345

Active Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8
0	1	2	3	4	5	6	7	8	0

Detector #	0
System Det. # 1	0
System Det. # 2	0
System Det. # 3	0
System Det. # 4	0
System Det. # 5	0
System Det. # 6	0
System Det. # 7	0
System Det. # 8	0

System Detectors <D Page>

Row	2	4
0	Delay	Carry-over
1	0.0	0.0
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	0.0	0.0
8	0.0	0.0
9	10.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+O+7
Short Failure	0.5	F+O+7

Power Cycle Correction (Default = 0.5)  
(These parameters are NOT downloaded)

**INTERSECTION: CONVOY ST & ENGINEER RD**

Group Assignment: 4014  
Field Master Assignment: NONE

N/S of Name: CONVOY ST  
EW Street Name: ENGINEER RD

CONVOY ST. ENGINEER RD. CONVOY ST.

223 **ogram**

Last Database Change: 07/30/20...  
System Ref. Number: 255  
Drawing Number: 19609  
Timing Implemented On: 12/17/01

Timing Sheet By: MMH  
Approved By: *MMH*

12/17/01

Row	1	2	3	4	5	6	7	8
Ped Walk	7	7						
Ped FDW	14	14						
Min Green	4	7						
Type 3 Limit								
Add/Veh								
Veh Extn	2.0	3.2		2.0	2.0	3.4		
Max Gap	2.0	3.2		2.0	2.0	3.4		
Min Gap	2.0	0.2		2.0	2.0	0.2		
Max Limit	30	50		30	30	50		
Max Limit 2								
Bus Adv		0.1				0.1		
Call to Phs		1.0				0.9		
Reduce By		4.0				3.9		
Every		1.0				1.0		
Yellow	0.3.4	3.0		0.3.9	3.6	0.3.4	3.0	
Red Clear	1.0	1.0		1.0	1.0	1.0		

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

Overlap A	Overlap B	Overlap C	Overlap D
9	Green Clear	C	Yellow Change
D	Red Clear	D	Load-Switch #
0			

Overlap Timing

<D Page>

D + 0 + OVERLAP

Start / Revert Times	Row
F + 0 + E	0
F + 0 + F	5.0
F + C + 0	0.0
C + 0 + 0	4
C + 0 + 1	4
C + 0 + 2	5
C + 0 + 3	11

Communication Addresses

C + F + 0	Row
F	0
Free Lag	2.4.6

Lag Phases <C Page>

Downtime Flash	(minutes)
255	255
Downtime Before Auto Manual Flash	

F + 0 + 8

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Permit	12_456															
Red Lock																
Yellow Lock																
Min Recall																
Ped Recall																
Peds (View)																
Rest In Walk																
Red Rest																
Dbl Entry																
Max Recall																
Soft Recall																
Max 2																
Cond Serv																
Ped Lock																
Yellow Start																
1st Phases																

Phase Functions  
F + F + Row

<F Page>

Preempt Timing  
F + E + Row

<F Page>

Manual Plan	Manual Offset
0	0
C + A + 1	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Disable Ports	Disable Communication Ports
234	D + D + 9

34 (4)



Row	1	3	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			
8			
9			
A			
B	-10.0		
C			
D			
E	---	---	---
F	---	---	---

Row	2	4	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E	---	---	---
F	---	---	---

Detector Delay & Carryover <D Page>

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

D + X (across) + ROW

Row	A	B	C	D	E	F
Detector Numbers	1 2 3 4 5 6 7 8	9 10 11 12	13 14 15 16 17 18 19 20	-- -- -- 21 22 23 24	-- -- -- -- -- -- --	-- 25 26 27 28 -- -- --

Active Detectors <D Page>

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- 21 22 23 24	5678
-- -- -- -- -- -- --	1234
-- 25 26 27 28 -- -- --	2345

Row	0	1	2	3	4	5	6	7	8
Detector #	0								
System Det. # 1									
System Det. # 2									
System Det. # 3									
System Det. # 4									
System Det. # 5									
System Det. # 6									
System Det. # 7									
System Det. # 8									

System Detectors <D Page>

Detector #	0
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7
Power Cycle Correction (Default = 0.5)		

**INTERSECTION: CONVOY ST & ENGINEER RD**

643 CIVILIAN

Coordination Timing By: **MXM**  
 Implemented On: **8/10/98**

**FOR OBSERVATION ONLY**

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Column #	1	2	3	4	5	6	7	8	9
0									
1		100							
2		47							
3									
4		31							
5		45							
6									
7									
8									
9									
A		40							
B		53							
C									
D		10							
E		255							
F		4							

Coordination C + Plan + ROW <C Page>

Row	Time	Plan	Offset	Day of Week
0	09 : 00	2	A	23456
1	11 : 30	2	B	23456
2	13 : 30	2	A	23456
3	18 : 00	E	A	1234567
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Coordination <9 Key with C+0+9=1>

- Plan Select
- 1 thru 9 = Coordination
- Plan 1 thru 9
- 14 or E = Free
- 15 or F = Flash

Row	E	F
0	Free Lag	2 4 6
1	Plan 1 - Lag	
2	Plan 2 - Lag	2 4 6
3	Plan 3 - Lag	
4	Plan 4 - Lag	
5	Plan 5 - Lag	
6	Plan 6 - Lag	
7	Plan 7 - Lag	
8	Plan 8 - Lag	
9	Plan 9 - Lag	
A	Coord Max *	
B	Coord Lag *	
C		
D		
E		
F		

Sync Phases C + E + FUNCTION #  
 Lag Phases C + F + FUNCTION # <C Page>

Transition Type	
TBC Transition	
C + D + D	
Transition Typa	
0 = Shortway	
Non-zero = Lengthen	

# INTERSECTION: KEARNY VILLA & SPECTRUM

Group Assignment: NONE  
Field Master Assignment: NONE

NIS Street Name: KEARNY VILLA  
E/W Street Name: SPECTRUM CTR

Timing Sheet By: JMV  
Approved By:

Last Database Change:  
System Ref. Number: 583  
Drawing Number: 29698-31-D  
Timing Implemented On:

Program

Row	1	2	3	4	5	6	7	8
Ped Walk	0	7	0	0	0	0	0	7
Ped FDW	0	20	0	0	0	0	0	19
Min Green	4	10	0	0	0	10.0	0	6.4
Type 3 Limit	0	0	0	0	0	0	0	0
Add/Veh	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Veh Extn	2.0	4.2	0.0	0.0	0.0	4.2	0.0	2.0
Max Gap	2.0	4.2	0.0	0.0	0.0	4.2	0.0	2.0
Min Gap	2.0	0.2	0.0	0.0	0.0	0.2	0.0	2.0
Max Limit	30	60	0	0	0	0	0	2.0
Max Limit 2	0	0	0	0	0	60	0	40
Bus Adv	0	0	0	0	0	0	0	0
Call to Phs	6	6	0	0	0	0	0	0
Reduce By	0.0	0.1	0.0	0.0	0.0	0.1	0.0	0.0
Every	0.0	0.7	0.0	0.0	0.0	0.7	0.0	0.0
Yellow	3.43.0	4.4	0.0	0.0	0.0	4.4	0.0	3.43.0
Red Clear	1.0	1.0	0.0	0.0	0.0	1.0	0.0	1.51.0

Phase Timing - Bank 1  
F + Phase + Row

Preempt Timing  
F + E + Row

Phase Functions  
F + F + Row

Row	0	1	2	3	4	5	6	7	8
Permit	12	6	8						
Red Lock									
Yellow Lock									
Min Recall	2	6							
Ped Recall									
Peds (View)	2	8							
Rest In Wal									
Red Rest									
Dbt Entry									
Max Recall									
Soft Recall									
Max 2									
Cond Serv									
Ped Lock	12345678								
Yellow Start	2	6							
1st Phases									8

Row	9	10	11	12	13	14	15
RR-1 Delay	0						
RR-1 Clear	0						
EV-A Delay	0						
EV-A Clear	0						
EV-B Delay	0						
EV-B Clear	0						
EV-C Delay	0						
EV-C Clear	0						
EV-D Delay	0						
EV-D Clear	0						
RR-2 Delay	0						
RR-2 Clear	0						
View EV Delay	...						
View EV Clear	...						
View RR Delay	...						
View RR Clear	...						

Row	16	17	18	19	20
Manual Plan	14				
Manual Offset	0				
Manual Selection					
Manual Plan	0 = Automatic				
1-9 = Plan 1-9					
14 = Free					
15 = Flash					

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Overlap Timing  
F + COLOR +  
D + 0 + OVERLAP

Communication Addresses  
C + F + O  
Free Lag  
Lag Phases

Disable Ports  
Disable Communication Ports  
D + D + 9

Downtime Flash  
Downtime Before Auto Manual Flash  
F + 0 + 8

Communication Addresses  
C + F + O  
Free Lag  
Lag Phases

35

Row	Time	Function	Day of Week	Phases/Bits
0	00 : 00	0		
1	00 : 00	0		
2	00 : 00	0		
3	00 : 00	0		
4	00 : 00	0		
5	00 : 00	0		
6	00 : 00	0		
7	00 : 00	0		
8	00 : 00	0		
9	00 : 00	0		
A	00 : 00	0		
B	00 : 00	0		
C	00 : 00	0		
D	00 : 00	0		
E	00 : 00	0		
F	00 : 00	0		

TOD Function 7 + ROW

<D Page> D + F + ROW

Row	Time	Function	Day of Week	Phases/Bits
0	00 : 00	0		
1	00 : 00	0		
2	00 : 00	0		
3	00 : 00	0		
4	00 : 00	0		
5	00 : 00	0		
6	00 : 00	0		
7	00 : 00	0		
8	00 : 00	0		
9	00 : 00	0		
A	00 : 00	0		
B	00 : 00	0		
C	00 : 00	0		
D	00 : 00	0		
E	00 : 00	0		
F	00 : 00	0		

Day of Week

Configuration E + F + ROW

<E Page>

Row	Time	Function	Day of Week	Phases/Bits
0	00 : 00	0		
1	00 : 00	0		
2	00 : 00	0		
3	00 : 00	0		
4	00 : 00	0		
5	00 : 00	0		
6	00 : 00	0		
7	00 : 00	0		
8	00 : 00	0		
9	00 : 00	0		
A	00 : 00	0		
B	00 : 00	0		
C	00 : 00	0		
D	00 : 00	0		
E	00 : 00	0		
F	00 : 00	0		

For access, set F + 9 + E = 1

Configuration E + E + ROW

- Extra 1 Flags
- 1 = TBC Type 1
- 2 = NEMA Ext. Coord
- 3 = Auto Daylight Savings
- 4 = EV Advance
- 5 = Remote Download
- 6 = Special Event
- 7 = Prelimed Operation
- 8 = Split Ring Operation

- Day of Week
- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

- Assign 5 Outputs
- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Disable Parity  0

Dial-Up Telephone Communications (if set to a non-zero value, parity will be disabled) (This parameter is NOT downloaded)

Remote Download C + 0 + 4 = 1 - 235 w/E + E + E bit 5 on

Row	Delay	Carry-over
0	0.0	0.0
1	0.0	1.8
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	0.0	0.0
8	0.0	0.0
9	0.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers
A	12345678
B	1234
C	12345678
D	5678
E	1234
F	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	0
2	0
3	0
4	0
5	0
6	0
7	0
8	0

System Detectors <D Page>

Row	Detector #
0	0
1	0
2	0
3	0
4	0
5	0
6	0
7	0
8	0

Row	Delay	Carry-over
0	0.0	0.0
1	0.0	1.8
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	10.0	0.0
8	0.0	0.0
9	0.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Delay & Carryover <D Page>

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

D + X (across) + ROW

Max ON (min)	5
Max OFF (min)	60

Detector Failure Monitor

Phase Number	0
Time Before Yellow	0.0

Advance Warning Beacon - Sign 1

Phase Number	0
Time Before Yellow	0.0

Advance Warning Beacon - Sign 2

Long Failure	0.5
Short Failure	0.5

Power Cycle Correction (Default = 0.5)  
(These parameters are NOT downloaded)

# INT ICTION: Kearny Spectrum BI & Overland Av

Group Assignment: None  
Field Master Assignment: None

N/S Street Name: Overland Av  
E/W Street Name: Kearny Spectrum BI

22 Program

Last Change: Drawing Number: 29738  
Timing Sheet By: JMV System Ref. Number:  
Approved By: Timing implemented on: 12/2000

Row	1	2	3	4	5	6	7	8
Ped Walk	7	7	7	7	7	7	7	7
Ped FDW	16	16	16	20	20	20	20	20
Min Green	4	4	4	4	4	4	4	4
Type 3 Limit								
Add/Veh								
Veh Extn	2.0	4.4	2.0	3.3	2.0	4.4	2.0	3.3
Max Gap	2.0	4.4	2.0	3.3	2.0	4.4	2.0	3.3
Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
Max Limit	30	60	30	40	30	60	30	40
Max Limit 2								
Bus Adv								
Call to Phs								
Reduce By		0.1		0.1		0.1		0.1
Every		0.7		1.0		0.7		1.0
Yellow	3.0	4.3	3.0	3.6	3.0	4.3	3.0	3.6
Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 1  
F + Phase + Row

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
RR-1 Delay																
RR-1 Clear																
EV-A Delay			0													
EV-A Clear			0													
EV-B Delay			0													
EV-B Clear			0													
EV-C Delay			0													
EV-C Clear			0													
EV-D Delay			0													
EV-D Clear			0													
RR-2 Delay																
RR-2 Clear																
View EV Delay																
View EV Clear																
View RR Delay																
View RR Clear																

Phase Functions  
F + E + Row  
F + F + Row

Max Initial	0
Red Revert	5.0
All Red Start	0.0
Start / Revert Times	
Drop Number	C + 0 + 0
Zone Number	C + 0 + 1
Area Number	C + 0 + 2
Area Address	C + 0 + 3
Quick/Not Channel	(Quick/Not)
Communication Addresses	
C + F + O	F
Free Lag	2_4_6_8

Row	A	B	C	D
Overlap A				
Overlap B				
Overlap C				
Overlap D				
Overlap Timing				
Green Clear	9			
Yellow Change		C		
Red Clear			D	
Load-Switch #				0

Manual Plan	14
Manual Offset	0
Manual Selection	
Manual Plan	0 = Automatic
Manual Offset	1 = Offset A
Manual Offset	2 = Offset B
Manual Offset	3 = Offset C

36

Disable Ports	234
Disable Communications Ports	D + D + 9

Downtime Flash	60
Downtime Before Auto Manual Flash	(minutes)
F + 0 + 8	

Row	0
Row	0

Row	Time	Function	Day of Week	Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function  
7 + ROW  
D + F + ROW

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Solt Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row	Configuration	<E Page>
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration  
E + F + ROW

Row	Function	Day of Week	Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Onlt		
6	Overlap B - Green Onlt		
7	Overlap C - Green Onlt		
8	Overlap D - Green Onlt		
9	Overlap Yellow Flash		
A	EV-A Phases		
B	EV-B Phases		
C	EV-C Phases		
D	EV-D Phases		
E	Extra 1 Config Bits		
F	IC Select (Interconnect)		

Configuration  
For access, set F + 9 + E = 1  
E + E + ROW

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

Time and Date  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Program Information  
 C + C + 0 = program  
 C + C + F = version

- Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Disable Parly 0  
 Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)  
 (This parameter is NOT downloaded)  
 D+B+0

Remote Download  
 C + 0 + 4 = 1 -255  
 w/ E + E + E bit 5 on



# SECTION: Kearny Spectrum BI & Ruffin Rd

Group Assignment: None  
Field Master Assignment: None

NS Street Name: Ruffin Rd  
EW Street Name: Kearny Spectrum BI

Last Change: JMV  
Timing Sheet By: JMV  
Approved By:

Drawing Number: 29738-43-D  
System Ref. Number:  
Timing implemented on:

22 Program

Row	1	2	3	4	5	6	7	8
Ped Walk	7	7	7	7	7	7	7	7
Ped FDW	22	22	22	22	22	22	22	22
Min Green	4	10	4	4	4	10		
Type 3 Limit								
Add/Veh								
Veh Extn	2.0	4.0	2.0	4.4	2.0	4.0		
Max Gap	2.0	4.0	2.0	4.4	2.0	4.0		
Min Gap	2.0	0.2	2.0	0.2	2.0	0.2		
Max Limit	30	60	30	40	30	60		
Max Limit 2								
Bus Adv								
Call to Phs								
Reduce By		0.1		0.1		0.1		
Every		0.8		0.7		0.8		
Yellow	3.4	3.0	4.7	4.5	3.9	3.0	4.3	3.4
Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 1

F + Phase + Row

<F Page>

Max Initial	0
Red Revert	5.0
All Red Start	0.0

Start / Revert Times

Drop Number	
Zone Number	
Area Number	
Area Address	
QuickNet Channel	

Communication Addresses

C + F + O	
Free Lag	2_4_6
Lag Phases	0

<C Page>

Row	A	B	C	D
Overlap A				
Overlap B				
Overlap C				
Overlap D				

Overlap Timing

F + COLOR +

<D Page>

D + 0 + OVERLAP

Downtime Flash	60
Downtime Before Auto Manual Flash	

F + 0 + 8

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
RR-1 Delay																
RR-1 Clear																
EV-A Delay	0															
EV-A Clear	0															
EV-B Delay	0															
EV-B Clear	0															
EV-C Delay	0															
EV-C Clear	0															
EV-D Delay	0															
EV-D Clear	0															
RR-2 Delay																
RR-2 Clear																
View EV Delay	...															
View EV Clear	...															
View RR Delay	...															
View RR Clear	...															

Preempt Timing

F + E + Row

F + F + Row

<F Page>

Manual Plan	14
Manual Offset	0

Manual Selection

Manual Offset 0  
= Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Disable Ports	234
Disable Communications Ports	

D + D + 9

37

AL3  
2/4/16

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function 7 + ROW  
 <D Page>  
 D + F + ROW

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 F = Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row	Configuration	<E Page>
0		F
1	RRR Overlap A - Phases	
2	RRR Overlap B - Phases	
3	RRR Overlap C - Phases	
4	RRR Overlap D - Phases	
5	RR Overlap 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	45
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	1

Configuration  
 E + F + ROW

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases		E
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Onlit		4
6	Overlap B - Green Onlit		
7	Overlap C - Green Onlit		
8	Overlap D - Green Onlit		
9	Overlap Yellow Flash		
A	EV-A Phases		2 5
B	EV-B Phases		4
C	EV-C Phases		1 6
D	EV-D Phases		3
E	Extra 1 Config. Bits		1 345
F	IC Select (Interconnect)		2

Configuration  
 For access, set F + 9 + E = 1  
 E + E + ROW

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Spill Ring Operation

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

- Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Time and Date  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Program Information  
 C + C + 0 = program  
 C + C + F = version

Remote Download  
 C + 0 + 4 = 1 -255  
 w/ E + E + E bit 5 on

Disable Parly  D+B+0  
 Dial-Up Telephone Communications  
 (If set to a non-zero value, parly will be disabled)  
 (This parameter is NOT downloaded)



# INTERSECTION: Engineer Road & Mercury Street

Group Assignment:  
Field Master Assignment:  
System Reference Number:

N/S Street Name: Mercury Street  
EW Street Name: Engineer Road

Last Database Change:  
Drawing Number: 19344-1-D

Change Record			
Change	By	Date	Change
Original TS <i>JD</i>	KT	7/12/2015	

Drop Number	<C/0+0+0>
Zone Number	<C/0+0+1>
Area Number	<C/0+0+2>
Area Address	<C/0+0+3>
QuickNet Channel	(QuickNet)

### Communication Addresses

Manual Plan	14
Manual Offset	0

### Manual Selection

Row	Phase Names	Mercury								Engineer							
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
0	Ped Walk																
1	Ped FDW																
2	Min Green																
3	Type 3 Disconnect																
4	Added per Vehicle																
5	Veh Extension																
6	Max Gap																
7	Min Gap																
8	Max Limit																
9	Max Limit 2																
A	Adv. / Delay Walk																
B	PE Min Ped FDW																
C	Cond Serv Check																
D	Reduce Every																
E	Yellow Change																
F	Red Clear																

### Phase Timing - Bank 1

<C+0+F=1>

Notes: Free Lag Phases 2 4 6 8

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash  
Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

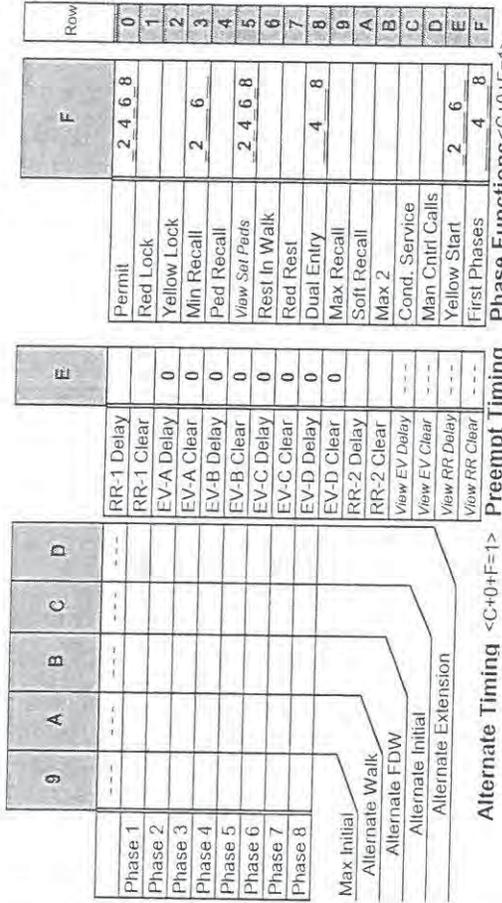
Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>
FYA Red Revert	0.0	<F/1+0+S>
OVL P CHG Red	0.0	<F/1+0+3>

### Start / Revert Times

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

### Exclusive Ped Phase

(Outputs specified in Assignable  
Outputs at E/T2/A+E & F)



### Alternate Timing <C+0+F=1>

### Preempt Timing

<C+0+F=1>

30

Row	Overlap Name ---->	1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8	Overlap Recall								
9	Queue Jump Phase								
A	Queue Jump Time								
B	Minimum Green								
C	Maximum Green								
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments

<C+0+E=29>

Row	Column Numbers ---->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2
B	EV-B Phases	4
C	EV-C Phases	6
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 34
F	IC Select (Interconnect)	2

<C+0+E=125>

Row	Column Numbers ---->	F
1	Ext. Permit 1 Phases	
	Ext. Permit 2 Phases	
	Exclusive Ped Assign	
	Preempt Non-Lock	12345678
	Ped for 2P Output	2
	Ped for 6P Output	6
	Ped for 4P Output	4
	Ped for 8P Output	8
	Yellow Flash Phases	
	Low Priority A Phases	
	Low Priority B Phases	
	Low Priority C Phases	
	Low Priority D Phases	
	Restricted Phases	
	Extra 2 Config. Bits	3

<C+0+E=125>

Row	Column Numbers ---->	F
	Fast Green Flash Phase	
	Green Flash Phases	
	Flashing Walk Phases	
	Guaranteed Passage	
	Simultaneous Gap Term	12345678
	Sequential Timing	
	Advance Walk Phases	
	Delay Walk Phases	
	External Recall	
	Start-Up Overlap Green	
	Max Extension	
	Inhibit Ped Reservice	
	Semi-Actuated	
	Start-Up Overlap Yellow	
	Start-Up Vehicle Calls	12345678
	Start-Up Ped Calls	12345678

<C+0+F=2>

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = Solid FDW on EV  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring
- Extra 2 Flags  
 1 = AWB During Initial  
 2 = Reserved  
 3 = Disable Min Walk  
 4 = QuietNet System  
 5 = Ignore P/P on EV  
 6 = Manual Hold in FDW  
 7 = Allow QuietNet PE  
 8 = Flash Gm B4 Yellow

Row	Column Numbers ---->	C
EV-A	0	
EV-B	0	
EV-C	0	
EV-D	0	
RR-1*	---	
RR-2*	---	
SE-1	0	
SE-2	0	

Preempt Priority  
 <C+0+E=125>  
 (\* RR-1 is always Highest, and RR-2 is always Second Highest)

Row	Column Numbers ---->	2
Phase 1	10	
Phase 2	10	
Phase 3	10	
Phase 4	10	
Phase 5	10	
Phase 6	10	
Phase 7	10	
Phase 8	10	

Coordination Transition Minimums  
 <C+0+C=5>

Row	Column Numbers ---->	E
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E		
F		

Coord Extra

1 = Programmed WALK Time for Sync Phases  
2 = Always Terminate Sync Phase Peds

Row	Column Numbers ---->	1	2	3	4	5	6	7	8	9
0	Plan Name ---->									
1	Cycle Length									
2	Phase 1 - ForceOff									
3	Phase 2 - ForceOff									
4	Phase 3 - ForceOff									
5	Phase 4 - ForceOff									
6	Phase 5 - ForceOff									
7	Phase 6 - ForceOff									
8	Phase 7 - ForceOff									
9	Phase 8 - ForceOff									
A	Ring Offset									
B	Offset 1									
C	Offset 2									
D	Offset 3									
E	Perm 1 - End									
F	Hold Release									
	Reserved									

Coordination - Bank 1

<C+0+C=1>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
-----	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
	Plan 1 - Sync															
	Plan 2 - Sync															
	Plan 3 - Sync															
	Plan 4 - Sync															
	Plan 5 - Sync															
	Plan 6 - Sync															
	Plan 7 - Sync															
	Plan 8 - Sync															
	Plan 9 - Sync															
	NEMA Sync															
	NEMA Hold															
	Coord Extra															

Sync Phases <C+0+C=1>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
	Ped Adjustment															
	Perm 2 - Start															
	Perm 2 - End															
	Perm 3 - Start															
	Perm 3 - End															
	Reservice Time															
	Reservice Phases															
	Pretimed Phases															
	Max Recall															
	Perm 1 Veh Phase															
	Perm 1 Ped Phase															
	Perm 2 Veh Phase															
	Perm 2 Ped Phase															
	Perm 3 Veh Phase															
	Perm 3 Ped Phase															

Coordination - Bank 2

<C+0+C=2>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
	Free Lag															
	Plan 1 - Lag															
	Plan 2 - Lag															
	Plan 3 - Lag															
	Plan 4 - Lag															
	Plan 5 - Lag															
	Plan 6 - Lag															
	Plan 7 - Lag															
	Plan 8 - Lag															
	Plan 9 - Lag															
	External Lag															
	Lag Hold															

Lag Phases <C+0+C=1>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	One-Shot Timer	Latch 1 Set	NOT-3	Max 2	Pretimed	Set Monday	Dial 2 (7-wire)	Sim Term	0
1	AND-5 (a)	Latch 1 Reset	NOT-4	Reserved	Plan 1	Ext. Perm 1	Dial 3 (7-wire)	EV-A	71
2	AND-5 (b)	Latch 2 Set	OR-4 (a)	Reserved	Plan 2	Ext. Perm 2	Offset 1 (7-wire)	EV-B	72
3	AND-6 (a)	Latch 2 Reset	OR-4 (b)	Reserved	Plan 3	Gate Down	Offset 2 (7-wire)	EV-C	73
4	AND-6 (b)		NAND-3 (a)	Reserved	Plan 4	Stop Clock	Offset 3 (7-wire)	EV-D	74
5	Reserved		OR-5 (a)	Reserved	Plan 5	Stop Time	Free (7-wire)	RR-1	51
6	Reserved		NAND-4 (a)	Reserved	Plan 6	Flash Sense	Flash (7-wire)	RR-2	52
7	Reserved		OR-6 (a)	Reserved	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1	
8	Spec. Funct 1	OR-7 (a)	OR-6 (b)	Reserved	Plan 8	Man. Advance	NOT-1	Spec. Event 2	
9	Spec. Funct 2	OR-7 (b)	EXTMR	Max Inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag	
A	Spec. Funct 3	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)	
B	Spec. Funct 4	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)	
C	Reserved	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)	
D	Reserved	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)	
E	Reserved	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)	
F	Reserved	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)	

Assignable Inputs

<C+0+E=126>

Row	Column 8	Column 9	Column A	Column B	Column C	Column D	Column E	Column F	Row
0	Reserved	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-wire)	0
1	Reserved	Phase ON - 2	Sp Evnt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-wire)	1
2	Reserved	Phase ON - 3	Sp Evnt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-wire)	2
3	Reserved	Phase ON - 4	Sp Evnt Out 3	EXTMR	Plan 3	OR-3	TOD Out 4	Offset 2 (7-wire)	3
4	Reserved	Phase ON - 5	Sp Evnt Out 4	One-Shot Timer	Plan 4	AND-1	TOD Out 5	Offset 3 (7-wire)	4
5	Reserved	Phase ON - 6	Sp Evnt Out 5	Reserved	Plan 5	AND-2	TOD Out 6	Free (7-wire)	5
6	Reserved	Phase ON - 7	Sp Evnt Out 6	Latch 1	Plan 6	AND-3	TOD Out 7	Flash (7-wire)	6
7	Reserved	Phase ON - 8	Sp Evnt Out 7	Latch 2	Plan 7	NOT-2	TOD Out 8	Adv. Warn - 1	7
8	Flh Yell Arrow 1	Ph. Check - 1	Sp Evnt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 2	Low Priority A	8
9	Green 1	Ph. Check - 2	Coord On	OR-4	Plan 9	EV-B		Low Priority B	9
A	Flh Yell Arrow 3	Ph. Check - 3	Detector Fail	OR-5	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C	A
B	Green 3	Ph. Check - 4	Spec. Funct. 1	OR-6	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D	B
C	Flh Yell Arrow 5	Ph. Check - 5	Spec. Funct. 2	AND-4	NAND-3	RR-1	DELAY-C	AND-5	C
D	Green 5	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	AND-6	D
E	Flh Yell Arrow 7	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	Reserved	E
F	Green 7	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	Reserved	F

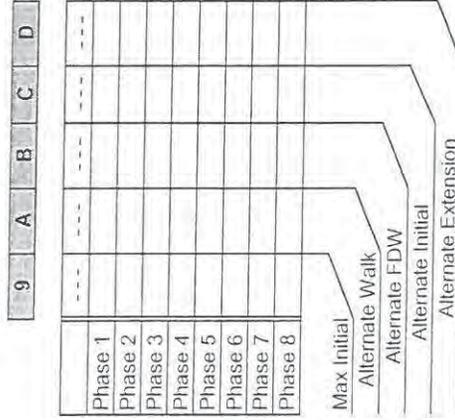
Assignable Outputs

<C+0+E=127>

INTERSECTION: Engineer Road & Mercury Street

Row	Column Numbers ---->	Phase	1	2	3	4	5	6	7	8
0	Phase Names ---->									
1	Ped Walk									
2	Ped FDW									
3	Min Green									
4	Type 3 Disconnect									
5	Added per Vehicle									
6	Veh Extension									
7	Max Gap									
8	Min Gap									
9	Max Limit									
A	Max Limit 2									
B	Adv. / Delay Walk									
C	PE Min Ped FDW									
D	Cond Serv Check									
E	Reduce Every									
F	Yellow Change									
	Red Clear									

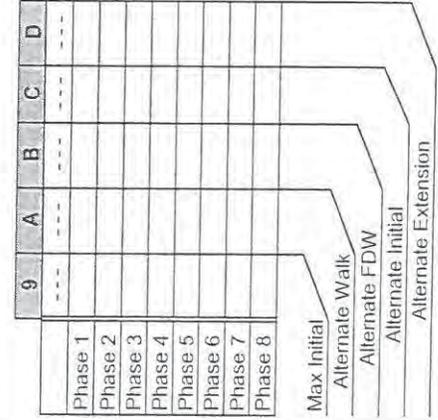
Phase Timing - Bank 2 <C+0+F=2>



Alternate Timing

Row	Column Numbers ---->	Phase	1	2	3	4	5	6	7	8
0	Phase Names ---->									
1	Ped Walk									
2	Ped FDW									
3	Min Green									
4	Type 3 Disconnect									
5	Added per Vehicle									
6	Veh Extension									
7	Max Gap									
8	Min Gap									
9	Max Limit									
A	Max Limit 2									
B	Adv. / Delay Walk									
C	PE Min Ped FDW									
D	Cond Serv Check									
E	Reduce Every									
F	Yellow Change									
	Red Clear									

Phase Timing - Bank 3 <C+0+F=3>



Alternate Timing

Transition Type  
 0X = Shortway  
 1.X = Lengthen  
 X.1 thru X.4 =  
 Number of  
 cycles when  
 lengthening

Daylight Savings  
 Date  
 If set to all zeros,  
 standard dates  
 will be used.

Transition Type **0.3** <C/5+1+9>  
**TBC Transition**

Hawk Select **0** /1+0+4>  
**Hawk Select** 200 = Mid-Block, 201 = Hawk

Address **0** <C/1+0+6>  
 Select Parity **0** <C/1+0+5>  
**AB3418 Comm 2** 0 = No Parity, 1 = Even

Begin Month **3** <C/5+2+A>  
 Begin Week **2** <C/5+2+B>  
 End Month **11** <C/5+2+C>  
 End Week **1** <C/5+2+D>  
**Daylight Savings Time**

Time B4 Yellow **0.0** <F/1+C+E>  
 Phase Number **0** <F/1+C+F>  
**Advance Warning Beacon - Sign 1**

Time B4 Yellow **0.0**  
 Phase Number **0** <F/1+D+F>  
**Advance Warning Beacon - Sign 2**

Offset Time **0** <C/5+2+E>  
 Max Cycle Time **20** <C/5+2+F>  
**Yellow Yield Coordination**

Omit Alarm **12345678**  
**Local Alarm Disable** #NAME? <C/5+F+0>

IEN Status **1** <C/5+1+B>  
 Synchron Time **0.0** <C/5+1+C>  
**Other Parameters**

Column Numbers ->

Row	Detector Name	0	1	2	3	1	3
		C1 Pin Number	Attributes	Phases(s)	Assign	Delay	Carry-over
0	212U	39	45 7	2	123		1.8
1	6J2U	40	45 7	6	123		1.8
2	416U	41	45 7	4	123		1.8
3	8J6U	42	45 7	8	123		1.8
4		43	45 7	2	123		
5		44	45 7	6	123		
6	416L	45	45 7	4	123	10.0	
7	8J6L	46	45 7	8	123	10.0	
8		47	67	2	123		
9		48	67	6	123		
A		49	67	4	123		
B		50	67	8	123		
C		55	45 7	5	123		
D		56	45 7	1	123		
E		57	45 7	7	123		
F		58	45 7	3	123		

Column Numbers ->

Row	Walk	1	2	3	4	5	6	7	8
0	Walk								
1	Don't Walk								
2	Phase Green								
3	Phase Yellow								
4	Phase Red								
5	Overlap Green								
6	Overlap Yellow								
7	Overlap Red								

**Redirect Phase Outputs**

<C+0+E=127>

Cabinet Type 0 <E/125+D+0>

**Enable Redirection**

(Enable Redirection = 30)

Max OFF (minutes)	20	<D/0+0+1>
Max ON (minutes)	7	<D/0+0+2>
Chatter Fail Time	0	<D/0+0+4>

**Detector Failure Monitor**

Row	One-Shot	B
8	0	
9	0	
A	0	
B	0	
C	0	
D	0	
E	0	
F	0	

**Delay Logic Times**

<C+0+D=0> (seconds)

Row	Detector Name	4	5	6	7	2	4
		C1 Pin Number	Attributes	Phases(s)	Assign	Delay	Carry-over
0		59	45 7	5	123		
1		60	45 7	1	123		
2		61	45 7	7	123		
3		62	45 7	3	123		
4		63	45 7	2	123		
5		64	45 7	6	123		
6		65	45 7	4	123		
7		66	45 7	8	123		
8		67	2	2	123		
9		68	2	6	123		
A		69	2	4	123		
B		70	2	8	123		
C		76	45 7	2	123		
D		77	45 7	6	123		
E		78	45 7	4	123		
F		79	45 7	8	123		

**Detector Assignments**

<C+0+E=126>

<C+0+D=0>

**Del. Assignments**

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

**Detector Attributes**

- 1 = Full Time Delay
- 2 = Red Call
- 3 = Overlap
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate



# INTERSECTION: KEARNY VILLA & TECH WY

Group Assignment: NONE  
Field Master Assignment: NONE

N/S Street Name: KEARNY VILLA  
E/W Street Name: TECH WY

# 3 Program

Timing Sheet By: JMW  
Approved By:

Last Database Change: System Ref. Number: 584  
Drawing Number: 29698-31-D  
Timing Implemented On:

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ped Walk	0	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Ped FDW	0	16	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Green	4	10	0	0	0	0	10	0	0	0	0	0	0	0	0	0
Type 3 Limit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Add/Veh	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Veh Extn	2.0	4.1	0.0	0.0	0.0	0.0	4.1	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max Gap	2.0	4.1	0.0	0.0	0.0	0.0	4.1	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Gap	2.0	0.2	0.0	0.0	0.0	0.0	0.2	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max Limit	30	60	0	0	0	0	60	0	0	0	0	0	0	0	0	0
Max Limit 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Bus Adv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Call to Phs	6	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Reduce By	0.0	0.1	0.0	0.0	0.0	0.0	0.1	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Every	0.0	0.8	0.0	0.0	0.0	0.0	0.8	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Yellow	3.4	3.0	4.4	0.0	0.0	0.0	4.4	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Red Clear	1.0	1.0	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Phase Timing - Bank 1  
F + Phase + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0

Start / Revert Times	
Drop Number	16
Zone Number	16
Area Number	5
Area Address	59
QuickNet Channel	DIGB4:

Communication Addresses	Row
C + F + 0	F
Free Lag	2.6_8
Lag Phases	0

Row	A	B	C	D	Load	Switch #
Overlap A	0.0	0.0	0.0	0.0	0.0	0
Overlap B	0.0	0.0	0.0	0.0	0.0	0
Overlap C	0.0	0.0	0.0	0.0	0.0	0
Overlap D	0.0	0.0	0.0	0.0	0.0	0

Overlap Timing  
F + COLOR +  
D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash	60	(minutes)
F + 0 + 8		

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
RR-1 Delay	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RR-1 Clear	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-A Delay	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-A Clear	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-B Delay	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-B Clear	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-C Delay	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-C Clear	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-D Delay	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EV-D Clear	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RR-2 Delay	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RR-2 Clear	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
View EV Delay	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
View EV Clear	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
View RR Delay	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
View RR Clear	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...

Preempt Timing  
F + E + Row

Phase Functions  
F + F + Row

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection  
Manual Offset  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Disable Ports	234
Disable Communication Ports	D + D + 9

39

Row	Time	Function	Day of Week	Phases/Bits
0	00 : 00	0		
1	00 : 00	0		
2	00 : 00	0		
3	00 : 00	0		
4	00 : 00	0		
5	00 : 00	0		
6	00 : 00	0		
7	00 : 00	0		
8	00 : 00	0		
9	00 : 00	0		
A	00 : 00	0		
B	00 : 00	0		
C	00 : 00	0		
D	00 : 00	0		
E	00 : 00	0		
F	00 : 00	0		

TOD Function  
7 + ROW

<D Page>  
D + F + ROW

- TOD Functions**
- 0 = Permitted Phases
  - 1 = Red Lock
  - 2 = Yellow Lock
  - 3 = Van Min Recall
  - 4 = Ped Recall
  - 5 =
  - 6 = Rest In Walk
  - 7 = Red Rest
  - 8 = Double Entry
  - 9 = Van Max Recall
  - A = Van Soft Recall
  - B = Maximum 2
  - C = Conditional Service
  - D = Free Lag Phases
  - E = Bit 1 - Local Override
  - Bit 2 - Phase Bank 2
  - Bit 3 - Phase Bank 3
  - Bit 4 - Disable Detector
  - OFF Monitor
  - Bit 7 - Detector Count Monitor
  - Bit 8 - Real Time Split Monitor
  - F = Output Bits 1 thru 4

Row	Configuration	<E Page>
0	RR Overlap A - Phases	
1	RR Overlap B - Phases	
2	RR Overlap C - Phases	
3	RR Overlap D - Phases	
4	RR Overlap A - Phases	
5	Ped 2P	2
6	Ped 6P	
7	Ped 4P	
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	2 8
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	1

Configuration  
E + F + ROW

Row	Time	Function	Day of Week	Phases/Bits
0		Exclusive Phases		
1		RR-1 Clear Phases		
2		RR-2 Clear Phases		
3		RR-2 Limited Service		
4		Prot / Perm Phases		
5		Overlap A - Green Omit		
6		Overlap B - Green Omit		2
7		Overlap C - Green Omit		
8		Overlap D - Green Omit		
9		Overlap Yellow Flash		
A		EVA Phases		2
B		EVB Phases		
C		EV-C Phases		1 6
D		EV-D Phases		2
E		Extra 1 Config. Bits		1_345
F		C Select (Interconnect)		_2

- Extra 1 Flags**
- 1 = TBC Type 1
  - 2 = NEMA Ext Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Remote Download
  - 6 = Special Event
  - 7 = Pretimed Operation
  - 8 = Split Ring Operation
- C Select Flags**
- 1 =
  - 2 = Modern
  - 3 = 7-Wire Slave
  - 4 = Flash / Free
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Configuration  
D+B+0

- Day of Week**
- 1 = Sunday
  - 2 = Monday
  - 3 = Tuesday
  - 4 = Wednesday
  - 5 = Thursday
  - 6 = Friday
  - 7 = Saturday
- Time and Date**
- 8-0 Hour, Minute, Day-of-Week
  - 8-1 Day-of-Month, Year, Month
  - 8-F Seconds
- Program Information**
- C + C + 0 = program
  - C + C + F = version
- Remote Download**
- C + 0 + 4 = 1 -255
  - w/E + E + E bit 5 on

- Assign 5 Outputs**
- 1 = Right Turn Overlap
  - 2 = TOD Outputs
  - 3 = EV Beacon - Steady
  - 4 = EV Beacon - Flashing
  - 5 = Special Event Outputs
  - 6 = Phase 3 & 7 Ped
  - 7 = Advanced Warning Sign
  - 8 =

Disable Parity  0

Dial-Up Telephone Communications  0

(If set to a non-zero value, parity will be disabled)

(This parameter is NOT downloaded)

Row	1	3
0	0.0	0.0
1	0.0	1.8
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	0.0	0.0
8	0.0	0.0
9	0.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers
A	E
B	12345678
C	1234
D	12345678
E	5678
F	1234
	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	
2	
3	
4	
5	
6	
7	
8	

System Detectors <D Page>

Row	Detector #
0	
1	
2	
3	
4	
5	
6	
7	
8	

Row	2	4
0	0.0	0.0
1	0.0	1.8
2	0.0	1.8
3	0.0	0.0
4	0.0	0.0
5	0.0	0.0
6	0.0	0.0
7	0.0	0.0
8	10.0	0.0
9	0.0	0.0
A	0.0	0.0
B	0.0	0.0
C	0.0	0.0
D	0.0	0.0
E	---	---
F	---	---

Detector Delay & Carryover <D Page>

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure		F+0+6
Short Failure		F+0+7

Power Cycle Correction (Default = 0.5)  
(These parameters are NOT downloaded)

INTERVAL	PHASE TIMING								9	PRE-EMPTION E	F										
	1	2	3	4	5	6	7	8			FLAGS	1	2	3	4	5	6	7	8		
0 WALK	1	7	1	7	1	1	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1 DONT WALK	1	30	1	29	1	1	1	1		RR1 CLR	5	RED LOCK	1			4					1
2 MIN GREEN	5	14	1	5	1	10	1	1		EVA DLY	0	YEL LOCK									2
3 TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2				6			3
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL									4
5 PASSAGE	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9		EVB CLR	5	PED PHASES		2		4					5
6 MAX GAP	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9		EVC DLY	0	RT OLA									6
7 MIN GAP	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9		EVC CLR	5	RT OLB									7
8 MAX EXT	45	45	9	45	9	45	9	9		EVD DLY	0	DBL ENTRY									8
9 MAX 2									YR	EVD CLR	5	MAX 2 PHASES									9
A MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A
B									DAY	RR2 CLR	5	RED REST									B
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK									C
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES									D
E YELLOW	3.7	4.1	3.0	4.1	3.0	4.1	3.0	3.0	MIN			YEL START UP		2				6			E
F RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	SEC			FIRST PHASE				4					F
PED XING FT		105		101									1	2	3	4	5	6	7	8	
BIKE XING FT		117				53															

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

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C PAGE

		CONTROL PLANS									Y-COORD			LAG PHASE	FLAGS										
		1	2	3	4	5	6	7	8	9		C	D	E	F		1	2	3	4	5	6	7	8	
0	CYCLE LENGTH														LAG FZ FREE			2		4		6		8	0
1	FZ1 GRN FCTR													GAPOUT CP1	LAG FZ CP 1										1
2														GAPOUT CP2	LAG FZ CP 2										2
3	FZ3 GRN FCTR													GAPOUT CP3	LAG FZ CP 3										3
4	FZ4 GRN FCTR										PERM TIME			GAPOUT CP4	LAG FZ CP 4										4
5	FZ5 GRN FCTR										LAG OFFSET			GAPOUT CP5	LAG FZ CP 5										5
6											FORCE OFF			GAPOUT CP6	LAG FZ CP 6										6
7	FZ7 GRN FCTR										LONG GRN			GAPOUT CP7	LAG FZ CP 7										7
8	FZ8 GRN FCTR										NO GREEN			GAPOUT CP8	LAG FZ CP 8										8
9	MULTI CYCLE													GAPOUT CP9	LAG FZ CP 9										9
A	OFFSET A										OFFSET				LAG C COORD										A
B	OFFSET B														LAG D COORD										B
C	OFFSET C														COORD FAZES			2				6			C
D	FZ 3 EXT																								D
E	FZ 7 EXT																								E
F	OFFSET INTRPT																								F

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP SYSTEM MASTER:  
 CO4 LAST CP RTE 163 @ KEARNY VILLA  
 CO7 TRNSMT CP (SOB)  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

FEATURE	OFF	ON
1		
2		
3		
4		
5		
6		
7		
8		

LOCATION	OFF	ON
1		1
2		1
3		0
4		1
5		
6		
7		
8		

COO = 11

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

1	2	3	4	5	6	7	8
---	---	---	---	---	---	---	---

	D	FLAGS							E	FLAGS							F	FLAGS								
	MAX	1	2	3	4	5	6	7	8	MIN	1	2	3	4	5	6	7	8	PED	1	2	3	4	5	6	7
0	RCL								RCL									RCL								
1	CP 1								CP 1									CP 1								
2	CP 2								CP 2									CP 2								
3	CP 3								CP 3									CP 3								
4	CP 4								CP 4									CP 4								
5	CP 5								CP 5									CP 5								
6	CP 6								CP 6									CP 6								
7	CP 7								CP 7									CP 7								
8	CP 8								CP 8									CP 8								
9	CP 9								CP 9									CP 9								
A																		RCL 1								
B																		RCL 2								
C																										
D																										
E																										
F																										

	E	FLAGS							F	FLAGS								
	FUNCTION	1	2	3	4	5	6	7	8	FUNCTION	1	2	3	4	5	6	7	8
0									CODE 4									0
1									CODE 5									1
2									C-RECALL									2
3									D-RECALL									3
4									EXCLUSIVE									4
5									2 PED		X							5
6									6 PED						X			6
7									4 PED				X					7
8									8 PED								X	8
9																		9
A	OLA NOT								OLA ON									A
B	OLB NOT								OLB ON									B
C	OLC NOT								OLC ON									C
D	OLD NOT								OLD ON									D
E																		E
F																		F

**LAST POWER FAILURE REGISTER**

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

**LAST FLASH TIME REGISTER**

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.											
		PHASES		TYPE				PHASES		TYPE	
		C		D				E		F	
0	I1	1		5,6		J1	5		5,6		
1	I2U	2		5,6		J2U	6		5,6		
2	I2L	2		5,6		J2L	6		5,6		
3	I3U	2		5,6		J3U	6		5,6		
4	I3L	2		5		J3L	6		5		
5	I4	2		7,8		J4	6		7,8		
6	I5	3		5,6		J5	7		5,6		
7	I6U	4		5,6		J6U	8		5,6		
8	I6L	4		5,6		J6L	8		5,6		
9	I7U	4		5,6		J7U	8		5,6		
A	I7L	4		5		J7L	8		5		
B	I8	4		7,8		J8	8		7,8		
C	I9U	1		5,6		J9U	5		5,6		
D	I9L	3		5,6		J9L	7		5,6		

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS										
I FILE					J FILE					
DELAY		CARRYOVER			DELAY		CARRYOVER			
I1	D10			D30		J1	D20		D40	
I2U	D11			D31	1.0	J2U	D21		D41	1.0
I2L	D12			D32		J2L	D22		D42	
I3U	D13			D33		J3U	D23		D43	
I3L	D14			D34		J3L	D24		D44	
I4	D15			D35		J4	D25		D45	
I5	D16			D36		J5	D26		D46	
I6U	D17			D37		J6U	D27		D47	
I6L	D18			D38		J6L	D28		D48	
I7U	D19			D39		J7U	D29		D49	
I7L	D1A			D3A		J7L	D2A		D4A	
I8	D1B			D3B		J8	D2B		D4B	
I9U	D1C			D3C		J9U	D2C		D4C	
I9L	D1D			D3D		J9L	D2D		D4D	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F									
	1	2	3	4	5	6	7	8		E	0	FLAGS	1	2	3	4	5	6	7	8	
0 WALK	1	7	7	7	1	7	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1 DONT WALK	1	23	36	36	1	32	1	1		RR1 CLR	5	RED LOCK	1				5				1
2 MIN GREEN	14	15	15	5	5	15	1	1		EVA DLY	0	YEL LOCK									2
3 TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2			6				3
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL									4
5 PASSAGE	2.0	2.0	2.0	2.0	2.0	2.0	0.9	0.9		EVB CLR	5	PED PHASES		2	3	4	6				5
6 MAX GAP	2.0	2.0	2.0	2.0	2.0	2.0	0.9	0.9		EVC DLY	0	RT OLA									6
7 MIN GAP	2.0	2.0	2.0	2.0	2.0	2.0	0.9	0.9		EVC CLR	5	RT OLB									7
8 MAX EXT	10	35	30	45	25	35	9	9		EVD DLY	0	DBL ENTRY									8
9 MAX 2									YR	EVD CLR	5	MAX 2 PHASES									9
A MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A
B									DAY	RR2 CLR	5	RED REST									B
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK									C
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES									D
E YELLOW	3.7	4.5	4.1	4.1	3.7	4.5	3.7	3.7	MIN			YEL START UP		2			6				E
F RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	SEC			FIRST PHASE			3						F
PED XING FT		81	125	125		112							1	2	3	4	5	6	7	8	
BIKE XING FT	114	129	132			121															

NOTES: 3/4 SPLIT

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

		CONTROL PLANS									Y-COORD			LAG PHASE	FLAGS									
		1	2	3	4	5	6	7	8	9		C	D	E	F	1	2	3	4	5	6	7	8	
0	CYCLE LENGTH														LAG FZ FREE		2		4		6		8	0
1	FZ1 GRN FCTR													GAPOUT CP1	LAG FZ CP 1									1
2														GAPOUT CP2	LAG FZ CP 2									2
3	FZ3 GRN FCTR													GAPOUT CP3	LAG FZ CP 3									3
4	FZ4 GRN FCTR										PERM TIME			GAPOUT CP4	LAG FZ CP 4									4
5	FZ5 GRN FCTR										LAG OFFSET			GAPOUT CP5	LAG FZ CP 5									5
6											FORCE OFF			GAPOUT CP6	LAG FZ CP 6									6
7	FZ7 GRN FCTR										LONG GRN			GAPOUT CP7	LAG FZ CP 7									7
8	FZ8 GRN FCTR										NO GREEN			GAPOUT CP8	LAG FZ CP 8									8
9	MULTI CYCLE													GAPOUT CP9	LAG FZ CP 9									9
A	OFFSET A										OFFSET				LAG C COORD									A
B	OFFSET B														LAG D COORD									B
C	OFFSET C														COORD FAZES		2				6			C
D	FZ 3 EXT																							D
E	FZ 7 EXT																							E
F	OFFSET INTRPT																							F

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP  
 CO4 LAST CP  
 CO7 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

**SYSTEM MASTER:**  
 RTE 163 KEARNY VILLA  
 SB OFF

FEATURE	OFF	ON
1		
2		
3		
4		
5		
6		
7		
8		

LOCATION	OFF	ON
1	1	
2		1
3		1
4		1
5		
6		
7		
8		

COO = 14

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

D PAGE

E PAGE

	D	FLAGS								E	FLAGS								F	FLAGS							
		MAX	1	2	3	4	5	6	7		8	MIN	1	2	3	4	5	6		7	8	PED	1	2	3	4	5
0	RCL									RCL									RCL								
1	CP 1									CP 1									CP 1								
2	CP 2									CP 2									CP 2								
3	CP 3									CP 3									CP 3								
4	CP 4									CP 4									CP 4								
5	CP 5									CP 5									CP 5								
6	CP 6									CP 6									CP 6								
7	CP 7									CP 7									CP 7								
8	CP 8									CP 8									CP 8								
9	CP 9									CP 9									CP 9								
A																		RCL 1									
B																		RCL 2									
C																											
D																											
E																											
F																											
		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8

	E	FLAGS								F	FLAGS								
		FUNCTION	1	2	3	4	5	6	7		8	FUNCTION	1	2	3	4	5	6	7
0										CODE 4									
1										CODE 5									
2										C-RECALL									
3										D-RECALL									
4										EXCLUSIVE									
5										2 PED		2							
6										6 PED				6					
7										4 PED			4						
8										8 PED			3						
9																			
A	OLA NOT									OLA ON									
B	OLB NOT									OLB ON									
C	OLC NOT									OLC ON									
D	OLD NOT									OLD ON									
E																			
F																			
		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8	

**LAST POWER FAILURE REGISTER**

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

**LAST FLASH TIME REGISTER**

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES of TYPE+EVENT NO.									
		PHASES		TYPE		PHASES		TYPE	
		C		D		E	F		
0	I1	1		5,6	J1	5		5,6	
1	I2U	2		5,6	J2U	6		5,6	
2	I2L	2		5,6	J2L	6		5,6	
3	I3U	2		5,6	J3U	6		5,6	
4	I3L	2		5	J3L	6		5	5,6
5	I4	2		7,8	J4	6		7,8	5,6
6	I5	3	2	5,6	J5	7	6	5,6	
7	I6U	4		5,6	J6U	8	3	5,6	
8	I6L	4		5,6	J6L	8	3	5,6	
9	I7U	4		5,6	J7U	8	3	5,6	
A	I7L	4		5	J7L	8	3	5	5,6
B	I8	4		7,8	J8	8	3	7,8	5,6
C	I9U	1	4	5,6	J9U	5		5,6	
D	I9L	3	4	5,6	J9L	7		5,6	

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS									
I FILE					J FILE				
DELAY		CARRYOVER			DELAY		CARRYOVER		
I1	D10		D30		J1	D20		D40	
I2U	D11		D31	1.6	J2U	D21		D41	1.6
I2L	D12		D32	1.6	J2L	D22		D42	1.6
I3U	D13		D33	1.6	J3U	D23		D43	1.6
I3L	D14		D34		J3L	D24		D44	
I4	D15		D35		J4	D25		D45	
I5	D16		D36		J5	D26		D46	
I6U	D17		D37		J6U	D27		D47	
I6L	D18		D38		J6L	D28		D48	
I7U	D19		D39		J7U	D29		D49	
I7L	D1A		D3A		J7L	D2A		D4A	
I8	D1B		D3B		J8	D2B	10.0	D4B	
I9U	D1C		D3C		J9U	D2C		D4C	
I9L	D1D		D3D		J9L	D2D		D4D	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

LOCATION.

RTE 805 SB @ 274 (BALBOA AVE)

*West*

CALTRANS C8 Version 3

DATE: 3/2/2015

PAGE 1

F PAGE

	INTERVAL	PHASE TIMING								9	PRE-EMPTION		F									
		1	2	3	4	5	6	7	8		E	0	FLAGS	1	2	3	4	5	6	7	8	
0	WALK	1	1	1	1	1	7	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1	DONT WALK	1	1	1	1	1	11	1	1		RR1 CLR	5	RED LOCK				4					1
2	MIN GREEN	1	1	1	5	1	10	1	1		EVA DLY	0	YEL LOCK									2
3	TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2				6			3
4	ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL									4
5	PASSAGE	0.9	0.9	0.9	1.0	0.9	2.0	0.9	0.9		EVB CLR	5	PED PHASES						6			5
6	MAX GAP	0.9	0.9	0.9	1.0	0.9	2.0	0.9	0.9		EVC DLY	0	RT OLA									6
7	MIN GAP	0.9	0.9	0.9	1.0	0.9	2.0	0.9	0.9		EVC CLR	5	RT OLB									7
8	MAX EXT	9	9	9	30	9	40	9	9		EVD DLY	0	DBL ENTRY									8
9	MAX 2									YR	EVD CLR	5	MAX 2 PHASES									9
A	MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A
B										DAY	RR2 CLR	5	RED REST									B
C	REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK									C
D	EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES									D
E	YELLOW	3.0	3.0	3.0	4.1	3.0	4.8	3.0	3.0	MIN			YEL START UP						6			E
F	RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	SEC			FIRST PHASE				4					F
	PED XING FT						40															
	BIKE XING FT						51															

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

*Handwritten mark*

		CONTROL PLANS									Y-COORD			LAG PHASE	FLAGS										
		1	2	3	4	5	6	7	8	9		C	D	E	F		1	2	3	4	5	6	7	8	
0	CYCLE LENGTH														LAG FZ FREE										0
1	FZ1 GRN FCTR													GAPOUT CP1	LAG FZ CP 1										1
2														GAPOUT CP2	LAG FZ CP 2										2
3	FZ3 GRN FCTR													GAPOUT CP3	LAG FZ CP 3										3
4	FZ4 GRN FCTR										PERM TIME			GAPOUT CP4	LAG FZ CP 4										4
5	FZ5 GRN FCTR										LAG OFFSET			GAPOUT CP5	LAG FZ CP 5										5
6											FORCE OFF			GAPOUT CP6	LAG FZ CP 6										6
7	FZ7 GRN FCTR										LONG GRN			GAPOUT CP7	LAG FZ CP 7										7
8	FZ8 GRN FCTR										NO GREEN			GAPOUT CP8	LAG FZ CP 8										8
9	MULTI CYCLE													GAPOUT CP9	LAG FZ CP 9										9
A	OFFSET A										OFFSET				LAG C COORD										A
B	OFFSET B														LAG D COORD										B
C	OFFSET C														COORD FAZES			2				6			C
D	FZ 3 EXT																								D
E	FZ 7 EXT																								E
F	OFFSET INTRPT																								F

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP     **SYSTEM MASTER:**  
 CO4 LAST CP         **SB RAMP**  
 CO7 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

FEATURE	OFF	ON
1		
2		
3		
4		
5		
6		
7		
8		

LOCATION	OFF	ON
1		1
2		
3		
4		
5		
6		
7		
8		

COO = 1

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

1	2	3	4	5	6	7	8
---	---	---	---	---	---	---	---

	D	FLAGS								E	FLAGS								F	FLAGS									
		MAX	1	2	3	4	5	6	7		8	MIN	1	2	3	4	5	6		7	8	PED	1	2	3	4	5	6	7
0	RCL									RCL										RCL									
1	CP 1									CP 1										CP 1									
2	CP 2									CP 2										CP 2									
3	CP 3									CP 3										CP 3									
4	CP 4									CP 4										CP 4									
5	CP 5									CP 5										CP 5									
6	CP 6									CP 6										CP 6									
7	CP 7									CP 7										CP 7									
8	CP 8									CP 8										CP 8									
9	CP 9									CP 9										CP 9									
A																				RCL 1									
B																				RCL 2									
C																													
D																													
E																													
F																													

	E	FLAGS								F	FLAGS									
		FUNCTION	1	2	3	4	5	6	7		8	FUNCTION	1	2	3	4	5	6	7	8
0										CODE 4										0
1										CODE 5										1
2										C-RECALL										2
3										D-RECALL										3
4										EXCLUSIVE										4
5										2 PED	2									5
6										6 PED						6				6
7										4 PED				4						7
8										8 PED									8	8
9																				9
A	OLA NOT									OLA ON										A
B	OLB NOT									OLB ON										B
C	OLC NOT									OLC ON										C
D	OLD NOT									OLD ON										D
E																				E
F																				F

**LAST POWER FAILURE REGISTER**

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

**LAST FLASH TIME REGISTER**

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.									
		PHASES		TYPE		PHASES		TYPE	
		C	D	E		F			
0	I1	1		5,6	J1	5		5,6	
1	I2U	2		5,6	J2U	6		5,6	
2	I2L	2		5,6	J2L	6		5,6	
3	I3U	2		5,6	J3U	6		5,6	
4	I3L	2		5	J3L	6		5, 6	
5	I4	2		7,8	J4	6		7,8	5, 6
6	I5	3		5,6	J5	7		5,6	
7	I6U	4		5,6	J6U	8		5,6	
8	I6L	4		5,6	J6L	8		5,6	
9	I7U	4		5,6	J7U	8		5,6	
A	I7L	4		5	J7L	8		5	
B	I8	4		7,8	J8	8		7,8	
C	I9U	1		5,6	J9U	5		5,6	
D	I9L	3		5,6	J9L	7		5,6	

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

DETECTOR SETTINGS									
I FILE					J FILE				
DELAY		CARRYOVER			DELAY		CARRYOVER		
I1	D10		D30		J1	D20		D40	
I2U	D11		D31		J2U	D21		D41	2.0
I2L	D12		D32		J2L	D22		D42	2.0
I3U	D13		D33		J3U	D23		D43	2.0
I3L	D14		D34		J3L	D24		D44	
I4	D15		D35		J4	D25		D45	
I5	D16		D36		J5	D26		D46	
I6U	D17	2.0	D37	2.0	J6U	D27		D47	
I6L	D18	2.0	D38	2.0	J6L	D28		D48	
I7U	D19		D39		J7U	D29		D49	
I7L	D1A		D3A		J7L	D2A		D4A	
I8	D1B		D3B		J8	D2B		D4B	
I9U	D1C		D3C		J9U	D2C		D4C	
I9L	D1D		D3D		J9L	D2D		D4D	

INTERVAL	PHASE TIMING								9	PRE-EMPTION E	F										
	1	2	3	4	5	6	7	8			FLAGS	1	2	3	4	5	6	7	8		
0 WALK	1	7	1	7	1	1	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1 DONT WALK	1	30	1	29	1	1	1	1		RR1 CLR	5	RED LOCK	1			4					1
2 MIN GREEN	5	14	1	5	1	10	1	1		EVA DLY	0	YEL LOCK									2
3 TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2			6				3
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL									4
5 PASSAGE	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9		EVB CLR	5	PED PHASES		2		4					5
6 MAX GAP	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9		EVC DLY	0	RT OLA									6
7 MIN GAP	2.0	2.0	0.9	2.0	0.9	2.0	0.9	0.9		EVC CLR	5	RT OLB									7
8 MAX EXT	45	45	9	45	9	45	9	9		EVD DLY	0	DBL ENTRY									8
9 MAX 2									YR	EVD CLR	5	MAX 2 PHASES									9
A MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A
B									DAY	RR2 CLR	5	RED REST									B
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK									C
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES									D
E YELLOW	3.7	4.1	3.0	4.1	3.0	4.1	3.0	3.0	MIN			YEL START UP		2			6				E
F RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	SEC			FIRST PHASE				4					F
PED XING FT		105		101									1	2	3	4	5	6	7	8	
BIKE XING FT		117				53															

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY

FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

LOCATION: RTE 272 (BALBOA AVE) / RTE 805 NB

CALTRANS C8 Version 3

DATE: 1/4/2016

PAGE 1

F PAGE

	INTERVAL	PHASE TIMING								9	PRE-EMPTION		F									
		1	2	3	4	5	6	7	8		E	0	FLAGS	1	2	3	4	5	6	7	8	
0	WALK	1	7	1	1	1	1	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1	DONT WALK	1	10	1	1	1	1	1	1		RR1 CLR	5	RED LOCK									1
2	MIN GREEN	1	10	1	5	1	1	1	1		EVA DLY	0	YEL LOCK									2
3	TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2				6			3
4	ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL									4
5	PASSAGE	0.9	2.0	0.9	2.0	0.9	0.9	0.9	0.9		EVB CLR	5	PED PHASES		2							5
6	MAX GAP	0.9	2.0	0.9	2.0	0.9	0.9	0.9	0.9		EVC DLY	0	RT OLA									6
7	MIN GAP	0.9	2.0	0.9	2.0	0.9	0.9	0.9	0.9		EVC CLR	5	RT OLB									7
8	MAX EXT	9	40	9	40	9	9	9	9		EVD DLY	0	DBL ENTRY									8
9	MAX 2									YR	EVD CLR	5	MAX 2 PHASES									9
A	MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A
B										DAY	RR2 CLR	5	RED REST									B
C	REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK									C
D	EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES									D
E	YELLOW	3.0	4.8	3.0	4.1	3.0	3.0	3.0	3.0	MIN			YEL START UP		2							E
F	RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	SEC			FIRST PHASE				4					F
	PED XING FT		35											1	2	3	4	5	6	7	8	
	BIKE XING FT		51																			

FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY



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C PAGE

		CONTROL PLANS									Y-COORD		LAG PHASE	FLAGS									
		1	2	3	4	5	6	7	8	9	C	D	E	F		1	2	3	4	5	6	7	8
0	CYCLE LENGTH													LAG FZ FREE		2		4		6		8	0
1	FZ1 GRN FCTR												GAPOUT CP1	LAG FZ CP 1									1
2													GAPOUT CP2	LAG FZ CP 2									2
3	FZ3 GRN FCTR												GAPOUT CP3	LAG FZ CP 3									3
4	FZ4 GRN FCTR										PERM TIME		GAPOUT CP4	LAG FZ CP 4									4
5	FZ5 GRN FCTR										LAG OFFSET		GAPOUT CP5	LAG FZ CP 5									5
6											FORCE OFF		GAPOUT CP6	LAG FZ CP 6									6
7	FZ7 GRN FCTR										LONG GRN		GAPOUT CP7	LAG FZ CP 7									7
8	FZ8 GRN FCTR										NO GREEN		GAPOUT CP8	LAG FZ CP 8									8
9	MULTI CYCLE												GAPOUT CP9	LAG FZ CP 9									9
A	OFFSET A										OFFSET			LAG C COORD									A
B	OFFSET B													LAG D COORD									B
C	OFFSET C													COORD FAZES	2				6				C
D	FZ 3 EXT																						D
E	FZ 7 EXT																						E
F	OFFSET INTRPT																						F

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP     SYSTEM MASTER:  
 CO4 LAST CP         SB OFF  
 CO7 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

FEATURE	OFF	ON
1		
2		
3		
4		
5		
6		
7		
8		

LOCATION	OFF	ON
1		1
2		1
3		1
4		
5		
6		
7		
8		

COO = 7

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

	D	FLAGS								E	FLAGS								F	FLAGS											
		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8				
	MAX									MIN											PED										
0	RCL									RCL											RCL										
1	CP 1									CP 1											CP 1										
2	CP 2									CP 2											CP 2										
3	CP 3									CP 3											CP 3										
4	CP 4									CP 4											CP 4										
5	CP 5									CP 5											CP 5										
6	CP 6									CP 6											CP 6										
7	CP 7									CP 7											CP 7										
8	CP 8									CP 8											CP 8										
9	CP 9									CP 9											CP 9										
A																					RCL 1										
B																					RCL 2										
C																															
D																															
E																															
F																															

	E	FLAGS								F	FLAGS								
		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8	
0										CODE 4									0
1										CODE 5									1
2										C-RECALL									2
3										D-RECALL									3
4										EXCLUSIVE									4
5										2 PED	2								5
6										6 PED					6				6
7										4 PED			4						7
8										8 PED								8	8
9																			9
A	OLA NOT									OLA ON									A
B	OLB NOT									OLB ON									B
C	OLC NOT									OLC ON									C
D	OLD NOT									OLD ON									D
E																			E
F																			F

LAST POWER FAILURE REGISTER

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.								
		PHASES		TYPE		PHASES		TYPE
		C	D			E	F	
0	I1	1		5,6	J1	5		5,6
1	I2U	2		5,6	J2U	6		5,6
2	I2L	2		5,6	J2L	6		5,6
3	I3U	2		5,6	J3U	6		5,6
4	I3L	2		5 5,6	J3L	6		5
5	I4	2		7,8 5,6	J4	6		7,8
6	I5	3		5,6	J5	7		5,6
7	I6U	4		5,6	J6U	8		5,6
8	I6L	4		5,6	J6L	8		5,6
9	I7U	4		5,6	J7U	8		5,6
A	I7L	4		5	J7L	8		5
B	I8	4		7,8	J8	8		7,8
C	I9U	1		5,6	J9U	5		5,6
D	I9L	3		5,6	J9L	7		5,6

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS									
I FILE					J FILE				
DELAY		CARRYOVER			DELAY		CARRYOVER		
I1	D10		D30		J1	D20		D40	
I2U	D11		D31	2.0	J2U	D21		D41	
I2L	D12		D32	2.0	J2L	D22		D42	
I3U	D13		D33	2.0	J3U	D23		D43	
I3L	D14		D34		J3L	D24		D44	
I4	D15		D35		J4	D25		D45	
I5	D16		D36		J5	D26		D46	
I6U	D17	2.0	D37	2.0	J6U	D27		D47	
I6L	D18	2.0	D38	2.0	J6L	D28		D48	
I7U	D19	7.0	D39		J7U	D29		D49	
I7L	D1A	7.0	D3A		J7L	D2A		D4A	
I8	D1B		D3B		J8	D2B		D4B	
I9U	D1C		D3C		J9U	D2C		D4C	
I9L	D1D		D3D		J9L	D2D		D4D	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

# INTERSECTION: BALBOA @ CONVOY

Group Assignment: 4118  
 Field Master Assignment: NONE  
 N/S Street Name: CONVOY  
 E/W Street Name: BALBOA

22

ogram

Last Database Change: 4/25/03 18:09  
 System Ref. Number: 761

Row	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ped Walk	7	7	2+3	7	7	7	7	7	7	7	7	7	7	7	7
Ped FDW	4	7	4	28	22	22	4	7	4	7	7	4	7	7	7
Min Green	4	7	4	7	7	7	4	7	4	7	7	4	7	7	7
Type 3 Limit															
Add/Veh		1.6		1.6											
Veh Extn	2.0	5.0	2.0	4.4	2.0	5.0	2.0	1.6	2.0	5.0	2.0	2.0	4.3	2.0	4.3
Max Gap	2.0	5.0	2.0	4.4	2.0	5.0	2.0	2.0	2.0	5.0	2.0	2.0	4.3	2.0	4.3
Min Gap	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Max Limit	30	60	30	40	30	60	30	40	30	60	30	40	30	40	40
Max Limit 2															
Bus Adv															
Call to Phs															
Reduce By		0.1		0.1											
Every		1.0		1.3											
Yellow	3.4	3.9	3.4	3.9	3.4	3.9	3.4	3.9	3.4	3.9	3.4	3.9	3.4	3.9	3.4
Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
Grade		0%		0%		-2%									0%

Phase Timing - Bank 1  
 F + Phase + Row

<F Page>

Max Initial	22	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0
Start / Revert Times		
Drop Number	2	C + 0 + 0
Zone Number	2	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	61	C + 0 + 3
QuickNet Channel	DIG137:	(QuicNet)
Communication Addresses		
C + F + 0		
Free Lag	23_5_8	Row
Lag Phases		<C Page>

Overlap Timing		9	10	11	12	13	14	15
Overlap A	Row	Green Clear	Yellow Change	Red Clear	Load-Switch #			
Overlap A	A							
Overlap B	B							
Overlap C	C							
Overlap D	D							

<F Page>  
 F + COLOR +

<D Page>  
 D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash	234	
Disable Ports	234	
Disable Communication Ports	234	
D + D + 9		

D + 0 + OVERLAP

RR-1 Delay	RR-1 Clear	EV-A Delay	EV-A Clear	EV-B Delay	EV-B Clear	EV-C Delay	EV-C Clear	EV-D Delay	EV-D Clear	RR-2 Delay	RR-2 Clear	View EV Delay	View EV Clear	View RR Delay	View RR Clear
		0	0			0	0					...	...	...	...
Permit	Red Lock	Yellow Lock	Min Recall	Ped Recall	Peds (View)	Rest In Walk	Red Rest	Dbl Entry	Max Recall	Soft Recall	Max 2	Cond Serv	Ped Lock	Yellow Start	1st Phases
					2_4_6_8					2_6				2_6	4_7

Phase Functions <F Page>  
 F + F + Row

Phase Functions <F Page>  
 F + F + Row

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1
Manual Selection		
Manual Plan		
Manual Offset		
Manual Selection		
0 = Automatic		
1 = Offset A		
2 = Offset B		
3 = Offset C		

Manual Offset  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Timing Sheet By: JD

Approved By: *mm*

Drawing Number: 12145-2-D

Timing Implemented On:

2

45

INTERSECTION: BALBOA @ CONVOY

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

T.O.D. Functions

- 0 = Permitted Phases
- 1 = Red Lock
- 2 = Yellow Lock
- 3 = Veh Min Recall
- 4 = Ped Recall
- 5 =
- 6 = Rest In Walk
- 7 = Red Rest
- 8 = Double Entry
- 9 = Veh Max Recall
- A = Veh Soft Recall
- B = Maximum 2
- C = Conditional Service
- D = Free Lag Phases
- E = Bit 1 - Local Override
- Bit 2 - Phase Bank 2
- Bit 3 - Phase Bank 3
- Bit 4 - Diaphlo Detector
- Off Monitor
- Bit 7 - Detector Count Monitor
- Bit 8 - Real Time Split Monitor
- F = Output Bits 1 thru 4

<D Page>

7 + ROW

TOD Function

Row	Exclusive Phases	RR-1 Clear Phases	RR-2 Clear Phases	RR-2 Limited Service	Prot / Perm Phases	Overlap A - Green Omit	Overlap B - Green Omit	Overlap C - Green Omit	Overlap D - Green Omit	Overlap Yellow Flash	EV-A Phases	EV-B Phases	EV-C Phases	EV-D Phases	Extra 1 Config. Bits	IC Select (Interconnect)
0																
1																
2																
3																
4																
5																
6																
7																
8																
9																
A																
B																
C																
D																
E																
F																

- Extra 1 Flags
- 1 = TBC Type 1
- 2 = NEMA Ext. Coord
- 3 = Auto Daylight Savings
- 4 = EV Advance
- 5 = Remote Download
- 6 = Special Event
- 7 = Prelimed Operation
- 8 = Split Ring Operation

IC Select Flags

- 1 =
- 2 = Modem
- 3 = 7-Wire Slave
- 4 = Flash / Free
- 5 =
- 6 = Simplex Master
- 7 = 7-Wire Master
- 8 = Offset Interrupter

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Row	RR Overlap A - Phases	RR Overlap B - Phases	RR Overlap C - Phases	RR Overlap D - Phases	Ped 2P	Ped 6P	Ped 4P	Ped 8P	Yellow Flash Phases	Overlap A - Phases	Overlap B - Phases	Overlap C - Phases	Overlap D - Phases	Restricted Phases	Assign 5 Outputs	Configuration
0																
1																
2																
3																
4																
5																
6																
7																
8																
9																
A																
B																
C																
D																
E																
F																

<E Page>

E + F + ROW

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday
- 8 =

Assign 5 Outputs

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

D+B+0

Disable Parity

Dial-Up Telephone Communications  
(If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1-255
- w/E + E + E bit 5 on

Configuration

E + E + ROW

For access, set F + 9 + E = 1



**223 Program**

**INTERSECTION: BALBOA @ CONVOY**

Coordination Timing By: EFF  
Implemented On:

**FOR OBSERVATION ONLY:**  
Master Plan C+A+2  
Current Plan C+A+3  
Next Plan C+A+4  
T.O.D. Plan C+A+5  
Master Cycle C+A+0  
Ring A Cycle C+B+0  
Ring B Cycle C+D+0  
Min Cycle C+A+E  
Max Cycle C+B+E

Column #	1	2	3	4	5	6	7	8	9
Row 0	130	130	130	130	130	130	130	130	130
Row 1	88	90	88	90	88	90	88	90	88
Row 2	0	0	0	0	0	0	0	0	0
Row 3	42	52	42	49	44	44	55	55	94
Row 4	71	81	71	78	73	73	23	23	0
Row 5	23	23	23	23	20	20	0	0	50
Row 6	0	0	0	0	0	0	17	17	94
Row 7	42	42	42	49	44	44	62	62	57
Row 8	71	81	71	78	73	73	59	59	14
Row 9	126	123	114	114	122	122	14	14	255
Row A	10	10	10	10	10	10	255	255	0
Row B	255	255	255	255	255	255	0	0	0
Row C	5	0	0	0	5	5			
Row D									
Row E									
Row F									

<C Page>

C + Plan + ROW

Coordination

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Plan 1	Free Lag	Plan 1 - Lag	Plan 2 - Lag	Plan 3 - Lag	Plan 4 - Lag	Plan 5 - Lag	Plan 6 - Lag	Plan 7 - Lag	Plan 8 - Lag	Plan 9 - Lag	Coord Max *	Coord Lag *				
Plan 2	2	6	6	6	6											
Plan 3	2	6	6	6	6											
Plan 4	2	6	6	6	6											
Plan 5																
Plan 6																
Plan 7																
Plan 8																
Plan 9																
Coord Ped*																
NEMA Hold																
Sync Phases																
Lag Phases																

Sync Phases C + E + FUNCTION #  
Lag Phases C + F + FUNCTION #

Transition Type  
TBC Transition C + D + D  
Transition Type  
0 = Shortway  
Non-zero = Lengthen

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Time	07:00	09:00	11:00	14:30	18:00											
Plan	7	7	7	7	7											
Offset	A	A	A	A	A											
Day of Week	23456	23456	23456	23456	1234567											
TOD Coordination																

<9 Key with C+0+0=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

① JMI  
7/16/10

# INTERSECTION: BALBOA AVE @ CONVOYS

223 Pr ram  
 Coordination Timing By: KH  
 Implemented On: 2/10/10

**FOR OBSERVATION ONLY**  
 Master Plan C+A+2  
 Current Plan C+A+3  
 Next Plan C+A+4  
 T.O.D. Plan C+A+5  
 Master Cycle C+A+0  
 Ring A Cycle C+B+0  
 Ring B Cycle C+D+0  
 Min Cycle C+A+E  
 Max Cycle C+B+E

Column #	1	2	3	4	5	6	7	8	9
Plan Name	Plan								
0									
1		130	130				120		140
2		98	95				77		112
3		0	0				0		0
4		52	49				23		55
5		81	78				62		94
6		23	23				80		23
7		0	0				0		0
8		42	49				17		50
9		81	78				62		94
A		123	114				59		57
B									
C									
D		10	10				14		14
E		255	255				255		255
F		10	10				0		0

Coordination C + Plan + ROW  
 <C Page>

Row	Time	Plan	Offset	Day of Week
0	07:00	7	A	23456
1	10:00	2	A	23456
2	11:00	3	A	23456
3	14:30	9	A	23456
4	19:00	E	A	1234567
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination  
 <9 Key with C+0+9=1>

Plan Select  
 1 thru 9 = Coordination  
 Plan 1 thru 9  
 14 or E = Free  
 15 or F = Flash

Row	E	F
0		
1	2 6	2 45 8
2	2 6	2 45 8
3		
4		
5		
6	2 6	2 4 6 8
7		
8	2 6	2 45 8
9		
A		
B		
C		
D		
E		
F		

Sync Phases C + E + FUNCTION #  
 Lag Phases <C Page>  
 C + F + FUNCTION #

Transition Type  
 TBC Transition C + D + D  
 Transition Type  
 0 = Shortway  
 Non-zero = Lengthen

# INTERSECTION: BALBOA Ave @ MERCURY St

Group Assignment:  
Field Master Assignment

N/S Street Name: MERCURY St  
E/W Street Name: BALBOA Ave

223 Program

Last Database Change:  
System Ref. Number:

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ped Walk	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7
Ped FDW	26	25	25	25	25	25	25	25	25	25	25	25	25	25	25	25
Min Green	6	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Type 3 Limit																
Add/Veh																
Veh Extn	2.0	4.7	2.0	4.3	2.0	4.8	2.0	4.3	2.0	4.8	2.0	4.3	2.0	4.3	2.0	4.3
Max Gap	2.0	4.7	2.0	4.3	2.0	4.8	2.0	4.3	2.0	4.8	2.0	4.3	2.0	4.3	2.0	4.3
Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
Max Limit	35	60	30	40	30	60	30	40	30	60	30	40	30	40	30	40
Max Limit 2																
Bus Adv																
Call to Phs																
Reduce By	0.1	0.7		0.1	0.1	0.1		0.1	0.1	0.1		0.1	0.1	0.1		0.1
Every																
Yellow	3.4	4.2	3.4	3.9	3.4	4.8	3.4	3.9	3.4	4.8	3.4	3.9	3.4	3.9	3.4	3.9
Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
Grade																

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

Max Initial	0
Red Revert	5.0
All Red Start	0.0
Start / Revert Times	
Drop Number	3
Zone Number	3
Area Number	5
Area Address	6.7
QuickNet Channel	DIGI: 3
Communication Addresses	
C + F + O	Row 0
Free Lag	2_4_6_8
Lag Phases	<C Page>

Overlap Timing

Row	A	B	C	D	Load-Switch #
Overlap A					
Overlap B					
Overlap C					
Overlap D					

<D Page>

D + 0 + OVERLAP

C + F + O	Row 0
Free Lag	2_4_6_8
Lag Phases	<C Page>

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash	234	(minutes)

F + 0 + 8

D + D + 9

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
RR-1 Delay																
RR-1 Clear																
EV-A Delay	0															
EV-A Clear	0															
EV-B Delay	0															
EV-B Clear	0															
EV-C Delay	0															
EV-C Clear	0															
EV-D Delay	0															
EV-D Clear	0															
RR-2 Delay																
RR-2 Clear																
View EV Delay	...															
View EV Clear	...															
View RR Delay	...															
View RR Clear	...															

Preempt Timing  
F + E + Row

<F Page>

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection  
Manual Plan  
0 = Automatic  
1 = Offset A  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: WXW  
Approved By: EFF  
Drawing Number: EFF  
Timing Implemented On:

46

INTERSECTION: BALBOA Ave @ MERCURY St

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Mfn Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest in Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

TOD Function

7 + ROW

<D Page>

D + F + ROW

Row	Exclusive Phases	IC Select	Configuration
0			
1			
2			
3			
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E			
F			

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Pretimed Operation  
 8 = Split Ring Operation

- IC Select Flags  
 1 =  
 2 = Modern  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

For access, set F + 9 + E = 1

E + E + ROW

Row	RR Overlap A - Phases	RR Overlap B - Phases	RR Overlap C - Phases	RR Overlap D - Phases	Ped 2P	Ped 6P	Ped 4P	Ped 8P	Yellow Flash Phases	Overlap A - Phases	Overlap B - Phases	Overlap C - Phases	Overlap D - Phases	Restricted Phases	Assign 5 Outputs
0															
1															
2															
3															
4															
5															
6															
7															
8															
9															
A															
B															
C															
D															
E															
F															

<E Page>

Configuration

E + F + ROW

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday  
 8 =  
 Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Time and Date

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Program Information

- C + C + 0 = program  
 C + C + F = version  
 Remote Download  
 C + 0 + 4 = 1 -255  
 w/ E + E + E bit 5 on

Disable Parity

0

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Row	1	3	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			1.8
8	10.0		
9			
A			
B			
C			
D			
E	---		---
F	---		---

Detector Name	332 Input File	Detector Number
	111	12
	212U	1
	212L	2
	213U	3
	213L	
	214	
	315	16
	416U	5
	416L	6
	417U	7
	417L	
	418	
	119U	
	319L	
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	
2	
3	
4	
5	
6	
7	
8	

System Det. #	Detector #
System Det. # 1	0
System Det. # 2	0
System Det. # 3	0
System Det. # 4	0
System Det. # 5	0
System Det. # 6	0
System Det. # 7	0
System Det. # 8	0

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3
Advance Warning Beacon - Sign 1		

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3
Advance Warning Beacon - Sign 2		

Long Failure	0.5	F+O+6
Short Failure	0.5	F+O+7
Power Cycle Correction (Default = 0.5)		

Row	2	4	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			1.8
8	10.0		
9			
A			
B			
C			
D			
E	---		---
F	---		---

Detector Name	332 Input File	Detector Number
	5J1	4
	6J2U	9
	6J2L	10
	6J3U	11
	6J3L	
	6J4	
	7J5	8
	8J6U	13
	8J6L	14
	8J7U	15
	8J7L	
	8J8	
	5J9U	
	7J9L	
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

# INTERSECTION: BALBOA @ KEARNY VILLA

Group Assignment: 4019  
Field Master Assignment: NONE

N/S Street Name: KEARNY VILLA  
E/W Street Name: BALBOA

BALBOA AV  
KEARNY VILLA Rd  
BALBOA AV  
KEARNY VILLA Rd  
BALBOA AV  
KEARNY VILLA Rd

22: **ogram**

Last Database Change: 5/29/03 15:47  
System Ref. Number: 753

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ped Walk	7				7											
Ped FDW	23				26											
Min Green	4				4											
Type 3 Limit																
Add/Veh	1.6				1.6											
Veh Extn	2.0				4.6											
Max Gap	4.1				2.0											
Min Gap	2.0				2.0											
Max Limit	30				40											
Max Limit 2																
Bus Adv																
Call to Phs																
Reduce By	0.1				0.1											
Every	1.5				1.1											
Yellow	4.7				4.4											
Red Clear	1.0				1.0											
Grade																

Phase Timing - Bank 1

F + Phase + Row

<F Page>

Preempt Timing

F + E + Row

Phase Functions <F Page>

F + F + Row

Max Initial	32	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0

Start / Revert Times

Drop Number	4	C + 0 + 0
Zone Number	4	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	63	C + 0 + 3
QuickNet Channel	DIGI37:	(QuickNet)

Communication Addresses

C + F + 0	Row
Free Lag	2_4_6_8
Lag Phases	<C Page>

Overlap Timing

Overlap A	Overlap B	Overlap C	Overlap D
Green Clear	Yellow Change	Red Clear	Load-Switch #
Row A	Row B	Row C	Row D

<F Page>

F + COLOR +

<D Page>

D + 0 + OVERLAP

Downtime Flash 255 (minutes)

Downtime Before Auto Manual Flash

F + 0 + 8

Disable Ports 234

Disable Communication Ports

D + D + 9

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1 = Offset A  
14 = Free  
15 = Flash

Timing Sheet By: JD

Approved By: *gmm*

Drawing Number:

Timing Implemented On: 6/23/07

49

Row	Function	Day of Week	Phases/Bits
0			
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		6
7	Ped 4P		4
8	Ped 8P		8
9	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		

**I.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest in Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 Off Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

<E Page>

**Configuration**  
E + F + ROW

**Day of Week**

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

- Assign 5 Outputs**  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

**Disable Parity** 0

**Dial-Up Telephone Communications**  
 (If set to a non-zero value, parity will be disabled)

**Time and Date**

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

**Program Information**

C + C + 0 = program  
 C + C + F = version

**Remote Download**

C + 0 + 4 = 1 -255  
 w/ E + E + E bit 5 on

Row	Time	Function	Day of Week	Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

<D Page>

**TOD Function**  
7 + ROW

D + F + ROW

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Spacial Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

- IC Select Flags**  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Row	Function	Day of Week	Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omnit		
6	Overlap B - Green Omnit		
7	Overlap C - Green Omnit		
8	Overlap D - Green Omnit		
9	Overlap Yellow Flash		
A	EV-A Phases		2_5
B	EV-B Phases		4_7
C	EV-C Phases		1_6
D	EV-D Phases		3_8
E	Extra 1 Config. Bits		1_345
F	IC Select (Interconnect)		2

**Configuration**  
E + E + ROW

For access, set F + 9 + E = 1

Row	1	3	Carry-over
0			
1			
2			
3			
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E	---	---	---
F	---	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- --	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	
2	
3	
4	
5	
6	
7	
8	

System Det. #	Detector #
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Row	2	4	Carry-over
0			
1			
2			
3			
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E	---	---	---
F	---	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

D + X (across) + ROW

# INTERSECTION: BALBOA @ PENNISI

Group Assignment: 4019  
Field Master Assignment: NONE

N/S Street Name: PENNISI  
EW Street Name: BALBOA

PENNISI/DY

BALBOA AV

BALBOA AV

22:ogram

Last Database Change: 4/25/03 18:01  
System Ref. Number: 754

Row	1	2	3	4	5	6	7	8
Ped Walk	→	→		→	↗	←		
Ped FDW				20	13	7		
Min Green		7		4	4	7		
Type 3 Limit								
Add/Veh								
Veh Extn		3.0		2.0	2.0	3.4		
Max Gap		3.0		2.0	2.0	3.4		
Min Gap		0.2		2.0	2.0	0.2		
Max Limit		60		40	30	60		
Max Limit 2								
Bus Adv		0.1				0.1		
Call to Phs		1.1				0.9		
Reduce By		4.4		3.9	3.4	4.2		
Every		1.0		1.0	1.0	1.0		
Yellow		0%		-1%		-1%		
Red Clear								
Grade								

Phase Timing - Bank 1

F + Phase + Row

<F Page>

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0
Start / Revert Times		
Drop Number	5	C + 0 + 0
Zone Number	5	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	64	C + 0 + 3
QuickNet Channel	DIGI37:	(QuickNet)
Communication Addresses		
C + F + O	F	Row
Free Lag	2_4_6	0

Lag Phases <C Page>

Overlap Timing

Row	A	B	C	D	0
Overlap A					
Overlap B					
Overlap C					
Overlap D					
Green Clear					
Yellow Change					
Red Clear					
Load-Switch #					

<D Page>

D + 0 + OVERLAP

Downtime Flash 255 (minutes)

Downtime Before Auto Manual Flash F + 0 + 8

F + 0 + 8

PAGE 1

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
RR-1 Delay																
RR-1 Clear																
EV-A Delay			0													
EV-A Clear			0													
EV-B Delay			0													
EV-B Clear			0													
EV-C Delay																
EV-C Clear																
EV-D Delay																
EV-D Clear																
RR-2 Delay																
RR-2 Clear																
View EV Delay																
View EV Clear																
View RR Delay																
View RR Clear																

Preempt Timing

F + E + Row

Phase Functions <F Page>

F + F + Row

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: JD

Approved By: mm

Drawing Number: 234

Timing Implemented On: D + D + 9

INTERSECTION: BALBOA @ PENNISI

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

T.O.D. Functions

- 0 = Permitted Phases
- 1 = Red Lock
- 2 = Yellow Lock
- 3 = Veh Min Recall
- 4 = Ped Recall
- 5 =
- 6 = Rest In Walk
- 7 = Red Road
- 8 = Double Entry
- 9 = Veh Mux Recall
- A = Veh Soft Recall
- B = Maximum 2
- C = Conditional Service
- D = Free Lag Phases
- E = Bit 1 - Local Override
- Bit 2 - Phase Bank 2
- Bit 3 - Phase Bank 3
- Bit 4 - Disable Detector OFF Monitor
- Bit 7 - Detector Count Monitor
- Bit 8 - Real Time Split Monitor
- F = Output Bits 1 thru 4

<D Page>

D + F + ROW

7 + ROW

TOD Function

Row	Exclusive Phases	RR-1 Clear Phases	RR-2 Clear Phases	RR-2 Limited Service	Prot / Perm Phases	Overlap A - Green Omit	Overlap B - Green Omit	Overlap C - Green Omit	Overlap D - Green Omit	Overlap Yellow Flash	EV-A Phases	EV-B Phases	EV-C Phases	EV-D Phases	Extra 1 Config. Bits	IC Select (Interconnect)
0																
1																
2																
3																
4																
5																
6																
7																
8																
9																
A																
B																
C																
D																
E																
F																

<E Page>

- Extra 1 Flags
- 1 = TBC Type 1
- 2 = NEMA Ext. Coord
- 3 = Auto Daylight Savings
- 4 = EV Advance
- 5 = Remote Download
- 6 = Special Event
- 7 = Prelimed Operation
- 8 = Split Ring Operation

IC Select/Flags

- 1 =
- 2 = Modem
- 3 = 7-Wire Slave
- 4 = Flash / Free
- 5 =
- 6 = Simplex Master
- 7 = 7-Wire Master
- 8 = Offset Interrupter

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Row	RR Overlap A - Phases	RR Overlap B - Phases	RR Overlap C - Phases	RR Overlap D - Phases	Ped 2P	Ped 6P	Ped 4P	Ped 8P	Yellow Flash Phases	Overlap A - Phases	Overlap B - Phases	Overlap C - Phases	Overlap D - Phases	Restricted Phases	Assign 5 Outputs
0															
1															
2															
3															
4															
5															
6															
7															
8															
9															
A															
B															
C															
D															
E															
F															

<E Page>

Configuration

E + F + ROW

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

Assign 5 Outputs.

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

Disable Parity 0

Dial-Up Telephone Communications

(If set to a non-zero value, parity will be disabled)

Row	Delay	Carry-over	Detector Name	332 Input File	Detector Number
0				111	14
1		1.8		212U	1
2				212L	5
3	DISCONNECT MIDS			213U	21
4	DISCONNECT MIDS			213L	25
5				214	9
6				315	16
7	10.0			416U	3
8	5.0			416L	7
9				417U	23
A				417L	27
B				418	11
C				119U	18
D				319L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	Delay	Carry-over	Detector Name	332 Input File	Detector Number
0				111	14
1		1.8		212U	1
2				212L	5
3	DISCONNECT MIDS			213U	21
4	DISCONNECT MIDS			213L	25
5				214	9
6				315	16
7	10.0			416U	3
8	5.0			416L	7
9				417U	23
A				417L	27
B				418	11
C				119U	18
D				319L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	Detector Numbers
A	12345678
B	1234
C	12345678
D	5678
E	1234
F	2345

Row	Detector Numbers
A	12345678
B	1234
C	12345678
D	5678
E	1234
F	2345

Active Detectors <D Page>

Row	Detector #
0	
1	
2	
3	
4	
5	
6	
7	
8	

Row	Detector #
0	
1	
2	
3	
4	
5	
6	
7	
8	

System Detectors <D Page>

Max ON (min)	5
Max OFF (min)	60

Detector Failure Monitor

Phase Number	#+0+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	F+0+6
Short Failure	F+0+7
Power Cycle Correction (Default = 0.5)	0.5
	0.5

Row	Delay	Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1		1.8		6J2U	2
2				6J2L	6
3	DISCONNECT MIDS			6J3U	22
4	DISCONNECT MIDS			6J3L	26
5				6J4	10
6				7J5	18
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

# INTERSECTION: BALBOA @ PONDEROSA

Group Assignment: 4019  
Field Master Assignment: NONE

N/S Street Name: PONDEROSA  
E/W Street Name: BALBOA

BALBOA AV  
PONDEROSA AV  
BALBOA AV

22: ogram

Last Database Change: 4/25/03 19:00  
System Ref. Number: 755

Row	1	2	3	4	5	6	7	8	Row
	↘	↗	↖	↗	↖	↗	↖	↗	F
Ped Walk		7							
Ped FDW		12							
Min Green	4	7							
Type 3 Limit									
Add/Veh									
Veh Extn	2.0	3.5	2.4						
Max Gap	2.0	3.5	2.4						
Min Gap	2.0	0.2							
Max Limit	30	60							
Max Limit 2									
Bus Adv									
Call to Phs									
Reduce By		0.1							
Every		1.3							
Yellow	3.4	4.4							
Red Clear	1.0	1.0							
Grade		0%							

Phase Timing - Bank 1

F + Phase + Row

<F Page>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Permit																12_56_8
Red Lock																
Yellow Lock																
Min Recall																2_6
Prod Recall																
Pods (View)																2_8
Rest In Walk																
Red Rest																
Dbl Entry																
Max Recall																
Soft Recall																
Max 2																
Cond Serv																1
Ped Lock																12345678
Yellow Start																2_6
1st Phases																8

Phase Functions <F Page>

F + F + Row

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
RR-1 Delay																
RR-1 Clear																
EV-A Delay																
EV-A Clear																
EV-B Delay																
EV-B Clear																
EV-C Delay																
EV-C Clear																
EV-D Delay																
EV-D Clear																
RR-2 Delay																
RR-2 Clear																
View EV Delay																...
View EV Clear																...
View RR Delay																...
View RR Clear																...

Preempt Timing

F + E + Row

Overlap Timing

F + COLOR +

<D Page>

Row	A	B	C	D	0
Overlap A					
Overlap B					
Overlap C					
Overlap D					
Green Clear					
Yellow Change					
Red Clear					
Load-Switch #					

<F Page>

D + 0 + OVERLAP

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0
Start / Revert Times		
Drop Number	6	C + 0 + 0
Zone Number	6	C + 0 + 1
Area Number	5	C + 0 + 2
Area Address	65	C + 0 + 3
QuickNet Channel	DIGI37:	(QuickNet)
Communication Addresses		
C + F + 0		Row
Free Lag	2_6_8	0

Lag Phases <C Page>

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		
Disable Ports	234	
Disable Communication Ports		

D + D + 9

Manual Selection

Manual Plan

0 = Automatic

1 = Offset A

2 = Offset B

3 = Offset C

Timing Sheet By: JD

Approved By: *MM*

Drawing Number: 22953-D

Timing Implemented On:

INTERSECTION: BALBOA @ PONDEROSA

Row	Function	Day of Week	Configuration
0	RR Overlap A - Phases		F
1	RR Overlap B - Phases		
2	RR Overlap C - Phases		
3	RR Overlap D - Phases		
4	Ped 2P		2
5	Ped 6P		
6	Ped 4P		
7	Ped 8P		8
8	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		

- I.O.D. Functions**
- 0 = Permitted Phases
  - 1 = Red Lock
  - 2 = Yellow Lock
  - 3 = Veh Min Recall
  - 4 = Ped Recall
  - 5 =
  - 6 = Rest in Walk
  - 7 = Red Rest
  - 8 = Double Entry
  - 9 = Veh Max Recall
  - A = Veh Soft Recall
  - B = Maximum 2
  - C = Conditional Service
  - D = Free Leg Phases
  - E = Bit 1 - Local Override
  - BIT 2 - Phase Bank 2
  - BIT 3 - Phase Bank 3
  - BIT 4 - Disable Detector OFF Monitor
  - BIT 7 - Detector Count Monitor
  - BIT 8 - Real Time Split Monitor
  - F = Output Bits 1 thru 4

<E Page>

Configuration  
E + F + ROW

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday
- 8 =

- Assign 5 Outputs.**
- 1 = Right Turn Overlap
  - 2 = TOD Outputs
  - 3 = EV Beacon - Steady
  - 4 = EV Beacon - Flashing
  - 5 = Special Event Outputs
  - 6 = Phase 3 & 7 Ped
  - 7 = Advanced Warning Sign
  - 8 =

Disable Party 0

Dial-Up Telephone Communications  
(If set to a non-zero value, party will be disabled)

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Program Information

- C + C + 0 = program
- C + C + F = version
- w/E + E + E bit 5 on

Remote Download

- C + 0 + 4 = 1-255
- w/E + E + E bit 5 on

Row	Time	Function	Day of Week	Column F
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

<D Page>

D + F + ROW

TOD Function

7 + ROW

Row	Function	Day of Week	Configuration
0	Exclusive Phases		E
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omnit		
6	Overlap B - Green Omnit		
7	Overlap C - Green Omnit		
8	Overlap D - Green Omnit		
9	Overlap Yellow Flash		2_5
A	EV-A Phases		
B	EV-B Phases		
C	EV-C Phases		1_6
D	EV-D Phases		
E	Extra 1 Config. Bits		1_345
F	IC Select (Interconnect)		2

- Extra 1 Flags**
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Remote Download
  - 6 = Special Event
  - 7 = Prelimed Operation
  - 8 = Split Ring Operation

- IC Select Flags**
- 1 =
  - 2 = Modem
  - 3 = 7-Wire Slave
  - 4 = Flash / Free
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Row	1	3
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D	---	---
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	
2	
3	
4	
5	
6	
7	
8	

System Det. #	Detector #
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Row	2	4
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A	10.0	
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7
Power Cycle Correction	(Default = 0.5)	

Column #	1	2	3	4	5	6	7	8
Phase	←	→	↶	↷	↵	↶↷	↵↶↷	↶↷↵
Ped Walk	7	7	7	7	7	7	7	7
Ped FDW	23	23	23	23	23	27	33	33
Min Green	4	4	4	4	4	7	4	7
Type 3 Limit								
Add/Veh								
Veh Extn	2.0	3.1	2.0	4.4	2.0	3.4	2.0	4.2
Max Gap	2.0	3.1	2.0	4.4	2.0	3.4	2.0	4.2
Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
Max Limit	40	30	30	50	40	30	30	50-40
Max Limit 2		25		30		90		30
Bus Adv								
Call to Phs								
Reduce By	0.1	1.0	0.1	0.1	0.1	0.1	0.1	0.1
Every	1.0	4.3	3.4	4.3	3.4	4.1	3.4	4.4
Yellow	3.4	4.3	3.4	4.3	3.4	4.1	3.4	4.4
Red Clear	1.0	2.0	1.0	1.0	1.0	2.0	1.0	1.0

Phase Timing - Bank 1  
 F + Phase + Row

Row	0	1	2	3	4	5	6	7	8	A	B	C	D	E	F	
Permit	12345678															
Red Lock																
Yellow Lock																
Min Recall																
Pad Recall																
Peds (View)																
Rest In Walk																
Red Rest																
Dbl Entry																
Max Recall																
Soft Recall																
Max 2																
Cond Serv																
Ped Lock																
Yellow Start																
1st Phases																

Manual Plan	Manual Offset
14	0

Manual Selection
0 = Automatic
1 = Offset A
1-9 = Plan 1-9
14 = Free
15 = Flash

Overlap A	Overlap B	Overlap C	Overlap D
9			

Green Clear	Yellow Change	Red Clear	Load-Switch #
0			

Drop Number	Zone Number	Area Number	Area Address	QuickNet Channel	DIGI37:
7	7	5	66		

C + F + O	Row
Free Lag	2_4_6_8

Lag Phases	Row
	0

Timing Sheet By: MS2  
 Approved By: EFF  
 Drawing Number: 30794-10-D  
 Timing Implemented On: Jan 2009

Disable Ports: 234  
 Disable Communication Ports: D + D + 8

Downtime Flash: 255 (minutes)  
 Downtime Before Auto Manual Flash: F + 0 + 8

Overlap Timing: 9, 0, 0, 0  
 <F Page>  
 F + COLOR +

Manual Offset: 14, 0  
 C + A + 1  
 C + B + 1

Sent to street  
 Div. on 01/15/09  
 to be implemented

HIS

Row	Time	Function	Day of Week	Column F Phases/Bits
0	06 : 00	B	234567	2_4_6_8
1	09 : 00	B	234567	
2	06 : 00	C	234567	1
3	09 : 00	C	234567	
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

<D Page>  
D + F + ROW

7 + ROW

TOD Function

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omnit		
6	Overlap B - Green Omnit		6
7	Overlap C - Green Omnit		
8	Overlap D - Green Omnit		
9	Overlap Yellow Flash		
A	EV-A Phases		2_5
B	EV-B Phases		4_7
C	EV-C Phases		1_6
D	EV-D Phases		3_8
E	Extra 1 Config. Bits		1_345
F	IC Select (Interconnect)		2

Configuration  
E + E + ROW

For access, set F + 9 + E = 1

Row	Function	Day of Week	Column F Phases/Bits
0			
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		6
7	Ped 4P		4
8	Ped 8P		8
9	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		1

Configuration  
E + F + ROW

<E Page>

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Day of Week  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

- IC Select Flags  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

- Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 = Bus Advance

Time and Date  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Disable Parity  
 Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Program Information  
 C + C + 0 = program  
 C + C + F = version

Remote Download  
 C + 0 + 4 = 1-255  
 w/E + E + E bit 5 on

Row	1	3
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8		1.8
9		
A	10.0	
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	
2	
3	
4	
5	
6	
7	
8	

System Det. #	Detector #
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Row	2	4
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8	10.0	
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7
Power Cycle Correction (Default = 0.5)		

Column #	Phase #	1	2	3	4	5	6	7	8
0	Ped Walk	7	7	7	7	7	7	7	7
1	Ped FDW	14	14	14	14	14	14	14	14
2	Min Green	4	7	7	7	7	7	7	7
3	Type 3 Limit								
4	Add/Veh								
5	Veh Exin	2.0	3.1	2.0	2.0	2.0	3.5		
6	Max Gap	2.0	3.1	2.0	2.0	2.0	3.5		
7	Min Gap	2.0	0.2	2.0	2.0	2.0	0.2		
8	Max Limit	30	60	40	30	60	35		
9	Max Limit 2		35						
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		1.0				0.9		
E	Yellow	3.4	5.1	3.9	3.4	4.1	4.1		
F	Red Clear	1.0	2.0	1.0	1.0	2.0	2.0		

Row	Permit	RR-1 Delay	RR-1 Clear	EV-A Delay	EV-A Clear	EV-B Delay	EV-B Clear	EV-C Delay	EV-C Clear	EV-D Delay	EV-D Clear	RR-2 Delay	RR-2 Clear	View EV Delay	View EV Clear	View RR Delay	View RR Clear
0	12_456																
1																	
2																	
3	2_6																
4																	
5	2_4_6																
6																	
7																	
8																	
9																	
A	2_8																
B																	
C																	
D	12345678																
E	2_6																
F	4																

Manual Plan	Manual Offset
14	0

Manual Selection  
 0 = Automatic  
 1 = Offset A  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

Manual Plan	Manual Offset
14	0

Manual Selection  
 0 = Automatic  
 1 = Offset A  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash

Disable Ports	Disable Communication Ports
234	D + D + 9

Timing Sheet By: MS2  
 Approved By: EFF  
 Drawing Number:  
 Timing Implemented On:

was sent to streets  
 Div. on 01/15/09  
 to be implemented  
 MS

Row	Time	Function	Day of Week	Column F Phases/Bits
0	06 : 00	C	234567	1
1	09 : 00	C	234567	
2	15 : 00	B	234567	6
3	19 : 00	B	234567	
4	06 : 00	B	234567	2
5	09 : 00	B	234567	
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function

<D Page>

D + F + ROW

7 + ROW

Row	Function	Day of Week	Configuration
0	Exclusive Phases		E
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omit		
6	Overlap B - Green Omit		
7	Overlap C - Green Omit		
8	Overlap D - Green Omit		
9	Overlap Yellow Flash		
A	EV-A Phases		
B	EV-B Phases		
C	EV-C Phases		
D	EV-D Phases		
E	Extra 1 Config. Bits	1 445	
F	IC Select (Interconnect)	2	

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Row	Function	Day of Week	Configuration
0	RR Overlap A - Phases		F
1	RR Overlap B - Phases		
2	RR Overlap C - Phases		
3	RR Overlap D - Phases		
4	Ped 2P	2	
5	Ped 6P	6	
6	Ped 4P	4	
7	Ped 8P		
8	Yellow Flash Phases		
9	Overlap A - Phases		
A	Overlap B - Phases		
B	Overlap C - Phases		
C	Overlap D - Phases		
D	Restricted Phases		
E	Assign 5 Outputs		
F			

<E Page>

Configuration

E + F + ROW

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 = Rest In Walk  
 6 = Red Rest  
 7 = Double Entry  
 8 = Veh Max Recall  
 9 = Veh Soft Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Pretimed Operation  
 8 = Split Ring Operation

- IC Select Flags  
 1 = Modem  
 2 = 7-Wire Slave  
 3 = Flash / Free  
 4 = Simplex Master  
 5 = 7-Wire Master  
 6 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Disable Parity 0 D+B+0

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program  
 C + C + F = version

Remote Download

- C + 0 + 4 = 1-255  
 w/ E + E + E bit 5 on

Row	1	3	Carry-over
0			
1			1.8
2			1.8
3			1.8
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E	---	---	---
F	---	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	
2	
3	
4	
5	
6	
7	
8	

System Det. #	Detector #
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Row	2	4	Carry-over
0			
1			1.8
2			1.8
3			1.8
4			1.8
5			
6			
7			
8			
9			
A			
B			
C			
D			
E	---	---	---
F	---	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7
Power Cycle Correction (Default = 0.5)		

INTERVAL	PHASE TIMING								9	PRE-EMPTION E	F																			
	1	2	3	4	5	6	7	8			CLK RST	EV SEL	RRI CLR	EVA DLY	EVA CLR	EVB DLY	EVB CLR	EVC DLY	EVC CLR	EVD DLY	EVD CLR	MAX EV	LAG PHASES	READ ONLY	A					
0 WALK	1	1	1	1	1	7	1	1	1	1	0	5	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
1 DONT WALK	1	1	1	1	1	19	1	1	1	1	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
2 MIN GREEN	1	11	1	5	5	11	1	1	1	1	0	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
3 TYPE 3 DET	0	0	0	0	0	0	0	0	0	0	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
5 PASSAGE	0.9	2.0	0.9	2.0	3.0	2.0	0.9	0.9	0.9	0.9	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
6 MAX GAP	0.9	2.0	0.9	2.0	3.0	2.0	0.9	0.9	0.9	0.9	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
7 MIN GAP	0.9	2.0	0.9	2.0	3.0	2.0	0.9	0.9	0.9	0.9	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
8 MAX EXT	9	35	9	30	35	35	9	9	9	9	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
9 MAX 2											5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
A MAX 3											5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
B											5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
E YELLOW	3.0	5.5	3.0	4.1	3.7	5.5	3.0	3.0	3.0	3.0	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
F RED	1.0	2.0	1.0	1.0	1.0	2.0	1.0	1.0	1.0	1.0	5	0	0	5	0	0	5	0	0	5	255	1	2	3	4	5	6	7	8	0
PED XING FT											65																			
BIKE XING FT		54																												

ENTRIES IN THESE LOCATIONS ARE NOT TO BE CHANGED

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCI FLASH ONLY

FOC LONG FAILURE	
FOD SHORT FAILURE	0
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

C05 FLASH TYPE	1
CC2 DOWNLOAD	1



D	FLAGS								E	MIN	FLAGS								F	PED	FLAGS							
	1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8
MAX	1	2	3	4	5	6	7	8	RCL	1	2	3	4	5	6	7	8	RCL	1	2	3	4	5	6	7	8		
0 RCL																												
1 CP 1									CP 1									CP 1										
2 CP 2									CP 2									CP 2										
3 CP 3									CP 3									CP 3										
4 CP 4									CP 4									CP 4										
5 CP 5									CP 5									CP 5										
6 CP 6									CP 6									CP 6										
7 CP 7									CP 7									CP 7										
8 CP 8									CP 8									CP 8										
9 CP 9									CP 9									CP 9										
A																		RCL 1										
B																		RCL 2										
C																												
D																												
E																												
F																												

E	FUNCTION	FLAGS								F	FUNCTION	FLAGS							
		1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8
0										CODE 4									
1										CODE 5									
2										C-RECALL									
3										D-RECALL									
4										EXCLUSIVE									
5										2 PED		X							
6										6 PED					X				
7										4 PED				X					
8										8 PED							X		
9																			
A	OLA NOT							X		OLA ON				X	X				
B	OLB NOT									OLB ON									
C	OLC NOT									OLC ON									
D	OLD NOT									OLD ON									
E																			
F																			

LAST POWER FAILURE REGISTER

HOUR = D-A-E RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 MINUTE = D-B-E (CALL ACTIVE LIGHTS)  
 DAY = D-C-E RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F D-E-E = C8 VERSION NUMBER  
 MINUTE = D-B-F D-E-F = LITHIUM BATTERY CONDITION  
 DAY = D-C-F 84 = BAD  
 85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.						
	PHASES		TYPE	PHASES		TYPE
	C	D		E	F	
0	I1	1	5.6	J1	5	5.6
1	I2U	2	5.6	J2U	6	5.6
2	I2L	2	5.6	J2L	6	5.6
3	I3U	2	5.6	J3U	6	5.6
4	I3L	2	5	J3L	6	5
5	I4	2	7.8	J4	6	7.8
6	I5	3	5.6	J5	7	5.6
7	I6U	4	5.6	J6U	8	5.6
8	I6L	4	5.6	J6L	8	5.6
9	I7U	4	5.6	J7U	8	5.6
A	I7L	4	5	J7L	8	5
B	I8	4	7.8	J8	8	7.8
C	I9U	1	5.6	J9U	5	5.6
D	I9L	3	5.6	J9L	7	5.6

**DETECTOR TYPE**

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

DETECTOR SETTINGS						
	I FILE			J FILE		
	DELAY	CARRYOVER		DELAY	CARRYOVER	
I1	D10	D30		J1	D20	D40
I2U	D11	D31	4.0	J2U	D21	D41
I2L	D12	D32		J2L	D22	D42
I3U	D13	D33		J3U	D23	D43
I3L	D14	D34		J3L	D24	D44
I4	D15	D35		J4	D25	D45
I5	D16	D36		J5	D26	D46
I6U	D17	D37		J6U	D27	D47
I6L	D18	D38	2.0	J6L	D28	D48
I7U	D19	D39		J7U	D29	D49
I7L	D1A	D3A		J7L	D2A	D4A
I8	D1B	D3B		J8	D2B	D4B
I9U	D1C	D3C		J9U	D2C	D4C
I9L	D1D	D3D		J9L	D2D	D4D

# INTERSECTION: CONVOY ST & ARMOUR

Group Assignment: NONE  
Field Master Assignment: NONE

N/S Name: CONVOY ST  
E/W Street Name: ARMOUR

CONVOY ST. ARMOUR ST. ARMOUR ST.

223 **ogram**

Last Database Change: 07/25/20...  
System Ref. Number: 155  
Drawing Number: 23255-2-D  
Timing Implemented On: 12/19/01

Timing Sheet By: MMH  
Approved By: mm

Timing Sheet By: MMH  
Approved By: mm

Row	1	2	3	4	5	6	7	8
0	Ped Walk	7						
1	Ped FDW	13						
2	Min Green	4						
3	Type 3 Limit							
4	Add/Veh							
5	Veh Extn	2.0	4.1		2.0	4.3	2.0	2.0
6	Max Gap	2.0	4.1		2.0	4.3	2.0	2.0
7	Min Gap	2.0	0.2		2.0	0.2	2.0	2.0
8	Max Limit	30	60		30	60	20	40
9	Max Limit 2							
A	Bus Adv							
B	Call to PHS							
C	Reduce By	0.1				0.1		
D	Every	0.8				0.7		
E	Yellow	3.4	3.0		3.4	3.0	3.0	3.0
F	Red Clear	1.0	1.0		1.0	1.0	1.0	1.0

Phase Timing - Bank 1  
F + Phase + Row

Row	9	C	D	0
9	Green Clear	Yellow Change	Red Clear	Load-Switch #
A	Overlap A			
B	Overlap B			
C	Overlap C			
D	Overlap D			

<D Page>  
D + 0 + OVERLAP

Downtime Flash	255	(minutes)
Downtime Before Auto Manual Flash		

F + 0 + 8

C + F + 0	F	Row
Free Lag	2	6

Lag Phases <C Page>

Row	E	F
0	RR-1 Delay	12
1	RR-1 Clear	56
2	EV-A Delay	78
3	EV-A Clear	
4	EV-B Delay	
5	EV-B Clear	
6	EV-C Delay	
7	EV-C Clear	
8	EV-D Delay	
9	EV-D Clear	
A	RR-2 Delay	
B	RR-2 Clear	
C	View EV Delay	
D	View EV Clear	
E	View RR Delay	
F	View RR Clear	

Preempt Timing  
F + E + Row

Phase Functions  
F + F + Row

Manual Plan	0
Manual Offset	0

Manual Selection  
Manual Plan

- 0 = Automatic
- 1 = Offset A
- 2 = Offset B
- 3 = Offset C

Disable Ports	234
---------------	-----

Disable Communication Ports  
D + D + 9

576

Row	Function	Day of Week	Column F Phases/Bits
0			F
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		6
7	Ped 4P		7
8	Ped 8P		8
9	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		1
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		1

<E Page>

Configuration  
E + F + ROW

- I.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 = Rest In Walk  
 6 = Red Rest  
 7 = Double Entry  
 8 = Veh Max Recall  
 9 = Veh Soft Recall  
 A = Maximum 2  
 B = Conditional Service  
 C = Free Lag Phases  
 D = Bit 1 - Local Override  
 E = Bit 2 - Phase Bank 2  
 F = Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

<D Page>

7 + ROW

TOD Function

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omit		
6	Overlap B - Green Omit		8
7	Overlap C - Green Omit		
8	Overlap D - Green Omit		
9	Overlap Yellow Flash		
A	EV-A Phases		2 5
B	EV-B Phases		
C	EV-C Phases		1 6
D	EV-D Phases		
E	Extra 1 Config. Bits		1 345
F	IC Select (Interconnect)		2

Configuration  
E + E + ROW

For access, set F + 9 + E = 1

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Pretimed Operation  
 8 = Split Ring Operation

- IC Select Flags**  
 1 = Modem  
 2 = 7-Wire Slave  
 3 = Flash / Free  
 4 = Simplex Master  
 5 = 7-Wire Master  
 6 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity

Dial-Up Telephone Communications  
(If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program w/ E + E + E bit 5 on
- C + 0 + 4 = 1-255

Remote Download

Row	1	3
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Row	2	4
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6	10.0	
7		
8		
9	10.0	
A		
B		
C		
D		
E	---	---
F	---	---

Detector Delay & Carryover <D Page>

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

D + X (across) + ROW

Row	A	B	C	D	E	F
Detector Numbers	12345678	1234	12345678	5678	1234	2345

Active Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8
Detector #	0								

System Det. #	Detector #
System Det. # 1	0
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: CONVOY ST & ARMOUR

Coordination Timing By: MXM  
Implemented On: 8/10/98

FOR OBSERVATION ONLY

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Row	1	2	3	4	5	6	7	8	9
0									
1		100							
2		76							
3									
4		76							
5									
6		31							
7		62							
8									
9									
A		74							
B		73							
C									
D		10							
E		255							
F									

<C Page>

Coordination  
C + Plan + ROW

Row	E	F
0	Free Lag	2 6 8
1	Plan 1 - Lag	
2	Plan 2 - Lag	2 6 8
3	Plan 3 - Lag	
4	Plan 4 - Lag	
5	Plan 5 - Lag	
6	Plan 6 - Lag	
7	Plan 7 - Lag	
8	Plan 8 - Lag	
9	Plan 9 - Lag	
A	Coord Max *	
B	Coord Lag *	
C		
D		
E		
F		

Lag Phases <C Page>  
C + F + FUNCTION #

Transition Type  
TBC Transition  
C + D + D

Transition Type  
0 = Shortway  
Non-zero = Lengthen

Row	Time	Plan	Offset	Day of Week
0	09 : 00	2	A	23456
1	11 : 30	2	B	23456
2	13 : 30	2	A	23456
3	18 : 00	E	A	1234567
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Coordination  
<9 Key with C+0+9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

INTERVAL	PHASE TIMING								9	PRE-EMPTION		F									
	1	2	3	4	5	6	7	8		E	FLAGS	1	2	3	4	5	6	7	8		
0 WALK	1	7	7	7	1	7	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0
1 DONT WALK	1	26	33	33	1	20	1	1		RR1 CLR	5	RED LOCK	1		3	4	5				1
2 MIN GREEN	5	13	15	5	13	12	1	1		EVA DLY	0	YEL LOCK									2
3 TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2				6			3
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL									4
5 PASSAGE	2.0	2.0	2.0	2.0	2.0	2.0	0.9	0.9		EVB CLR	5	PED PHASES		2	3	4		6			5
6 MAX GAP	2.0	2.0	2.0	2.0	2.0	2.0	0.9	0.9		EVC DLY	0	RT OLA									6
7 MIN GAP	2.0	2.0	2.0	2.0	2.0	2.0	0.9	0.9		EVC CLR	5	RT OLB									7
8 MAX EXT	40	45	40	35	25	45	9	9		EVD DLY	0	DBL ENTRY									8
9 MAX 2									YR	EVD CLR	5	MAX 2 PHASES									9
A MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A
B									DAY	RR2 CLR	5	RED REST									B
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK									C
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES									D
E YELLOW	3.7	4.5	4.1	4.1	3.7	4.5	3.0	3.0	MIN			YEL START UP		2				6			E
F RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	SEC			FIRST PHASE			3						F
PED XING FT		90	115	115		69							1	2	3	4	5	6	7	8	
BIKE XING FT		90	123		100	87															

NOTES:

3/4 SPLIT

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

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		CONTROL PLANS									Y-COORD			LAG PHASE	FLAG									
		1	2	3	4	5	6	7	8	9		C	D	E	F									
0	CYCLE LENGTH														LAG FZ FREE		X		X		X		X	0
1	FZ1 GRN FCTR													GAPOUT CP1	LAG FZ CP 1									1
2														GAPOUT CP2	LAG FZ CP 2									2
3	FZ3 GRN FCTR													GAPOUT CP3	LAG FZ CP 3									3
4	FZ4 GRN FCTR										PERM TIME			GAPOUT CP4	LAG FZ CP 4									4
5	FZ5 GRN FCTR										LAG OFFSET			GAPOUT CP5	LAG FZ CP 5									5
6											FORCE OFF			GAPOUT CP6	LAG FZ CP 6									6
7	FZ7 GRN FCTR										LONG GRN			GAPOUT CP7	LAG FZ CP 7									7
8	FZ8 GRN FCTR										NO GREEN			GAPOUT CP8	LAG FZ CP 8									8
9	MULTI CYCLE													GAPOUT CP9	LAG FZ CP 9									9
A	OFFSET A										OFFSET				LAG C COORD									A
B	OFFSET B														LAG D COORD									B
C	OFFSET C														COORD FAZES		X				X			C
D	FZ 3 EXT																							D
E	FZ 7 EXT																							E
F	OFFSET INTRP																							F

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP  
 CO4 LAST CP  
 CO7 TRNSMT CP  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

SYSTEM MASTER -  
 RTE 274 @ CONVOY

LOCATION	OFF	ON
1	X	
2	X	
3		X
4		X
5	X	
6	X	
7	X	
8	X	

COO = 12

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

	D	FLAGS								E	FLAGS								F	FLAGS							
	MAX	1	2	3	4	5	6	7	8	MIN	1	2	3	4	5	6	7	8	PED	1	2	3	4	5	6	7	8
0	RCL									RCL									RCL								
1	CP 1									CP 1									CP 1								
2	CP 2									CP 2									CP 2								
3	CP 3									CP 3									CP 3								
4	CP 4									CP 4									CP 4								
5	CP 5									CP 5									CP 5								
6	CP 6									CP 6									CP 6								
7	CP 7									CP 7									CP 7								
8	CP 8									CP 8									CP 8								
9	CP 9									CP 9									CP 9								
A																			RCL 1								
B																			RCL 2								
C																											
D																											
E																											
F																											

	E	FLAGS								F	FLAGS								
	FUNCTION	1	2	3	4	5	6	7	8	FUNCTION	1	2	3	4	5	6	7	8	
0										CODE 4									0
1										CODE 5									1
2										C-RECALL									2
3										D-RECALL									3
4										EXCLUSIVE									4
5										2 PED		X							5
6										6 PED						X			6
7										4 PED			X						7
8										8 PED		X							8
9																			9
A	OLA NOT									OLA ON									A
B	OLB NOT									OLB ON									B
C	OLC NOT									OLC ON									C
D	OLD NOT									OLD ON									D
E																			E
F																			F

LAST POWER FAILURE REGISTER

HOUR = D-A-E

MINUTE = D-B-E

DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
(CALL ACTIVE LIGHTS)

RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
(CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F

MINUTE = D-B-F

DAY = D-C-F

D-E-E = C8 VERSION NUMBER

D-E-F = LITHIUM BATTERY CONDITION

84 = BAD

85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.									
PHASES			TYPE		PHASES			TYPE	
C			D		E			F	
0	I1	1		5,6	J1	5		5,6	
1	I2U	2		5,6	J2U	6		5,6	
2	I2L	2		5,6	J2L	6		5,6	
3	I3U	2		5,6	J3U	6		5,6	
4	I3L	2		5	J3L	6		5	
5	I4	2		7,8	J4	6		7,8	
6	I5	3		5,6	J5	7		5,6	
7	I6U	4		5,6	J6U	8		5,6	
8	I6L	4		5,6	J6L	8		5,6	
9	I7U	4		5,6	J7U	8		5,6	
A	I7L	4		5	J7L	8		5	
B	I8	4		7,8	J8	8		7,8	
C	I9U	1		5,6	J9U	5		5,6	
D	I9L	3		5,6	J9L	7		5,6	

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS									
I FILE					J FILE				
DELAY		CARRYOVER			DELAY		CARRYOVER		
I1	D10			D30		J1	D20		D40
I2U	D11			D31	1.0	J2U	D21		D41 1.0
I2L	D12			D32		J2L	D22		D42
I3U	D13			D33		J3U	D23		D43
I3L	D14			D34		J3L	D24		D44
I4	D15			D35		J4	D25		D45
I5	D16	7.0		D36		J5	D26		D46
I6U	D17			D37		J6U	D27		D47
I6L	D18			D38		J6L	D28		D48
I7U	D19			D39		J7U	D29		D49
I7L	D1A			D3A		J7L	D2A		D4A
I8	D1B			D3B		J8	D2B		D4B
I9U	D1C			D3C		J9U	D2C		D4C
I9L	D1D			D3D		J9L	D2D		D4D

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

	INTERVAL	PHASE TIMING								9	PRE-EMPTION		F										
		1	2	3	4	5	6	7	8		E	0	FLAGS	1	2	3	4	5	6	7	8		
0	WALK	1	1	1	7	1	7	1	1	CLK RST	EV SEL	0	PERMIT	1	2	3	4	5	6	7	8	0	
1	DONT WALK	1	1	1	27	1	17	1	1		RR1 CLR	5	RED LOCK				4	5				1	
2	MIN GREEN	1	10	1	5	5	14	1	1		EVA DLY	0	YEL LOCK									2	
3	TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL		2				6			3	
4	ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL									4	
5	PASSAGE	0.9	2.0	0.9	2.0	2.0	2.0	0.9	0.9		EVB CLR	5	PED PHASES				4		6			5	
6	MAX GAP	0.9	2.0	0.9	2.0	2.0	2.0	0.9	0.9		EVC DLY	0	RT OLA									6	
7	MIN GAP	0.9	2.0	0.9	2.0	2.0	2.0	0.9	0.9		EVC CLR	5	RT OLB									7	
8	MAX EXT	9	45	9	45	25	45	9	9		EVD DLY	0	DBL ENTRY									8	
9	MAX 2									YR	EVD CLR	5	MAX 2 PHASES									9	
A	MAX 3									MO	MAX EV	255	LAG PHASES	READ ONLY								A	
B										DAY	RR2 CLR	5	RED REST									B	
C	REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK									C	
D	EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES									D	
E	YELLOW	3.0	4.8	3.0	4.1	3.7	4.8	3.0	3.0	MIN			YEL START UP		2				6			E	
F	RED	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	SEC			FIRST PHASE				4					F	
	PED XING FT				94		60								1	2	3	4	5	6	7	8	
	BIKE XING FT		45				112																

NOTES:

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CC1 FLASH ONLY



FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1

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		CONTROL PLANS									Y-COORD			LAG PHASE		FLAGS										
		1	2	3	4	5	6	7	8	9		C	D	E		F		1	2	3	4	5	6	7	8	
0	CYCLE LENGTH															LAG FZ FREE			2		4		6		8	0
1	FZ1 GRN FCTR													GAPOUT CP1		LAG FZ CP 1										1
2														GAPOUT CP2		LAG FZ CP 2										2
3	FZ3 GRN FCTR													GAPOUT CP3		LAG FZ CP 3										3
4	FZ4 GRN FCTR										PERM TIME			GAPOUT CP4		LAG FZ CP 4										4
5	FZ5 GRN FCTR										LAG OFFSET			GAPOUT CP5		LAG FZ CP 5										5
6											FORCE OFF			GAPOUT CP6		LAG FZ CP 6										6
7	FZ7 GRN FCTR										LONG GRN			GAPOUT CP7		LAG FZ CP 7										7
8	FZ8 GRN FCTR										NO GREEN			GAPOUT CP8		LAG FZ CP 8										8
9	MULTI CYCLE													GAPOUT CP9		LAG FZ CP 9										9
A	OFFSET A										OFFSET					LAG C COORD										A
B	OFFSET B															LAG D COORD										B
C	OFFSET C															COORD FAZES			2				6			C
D	FZ 3 EXT																									D
E	FZ 7 EXT																									E
F	OFFSET INTRPT																									F

CO1 MANUAL CP  
 CO2 MASTER CP  
 CO3 CURRENT CP SYSTEM MASTER:  
 CO4 LAST CP RTE 163 @ KEARNY VILL  
 CO7 TRNSMT CP (SOB)  
 COD MANUAL OFFSET  
 CAO LOCAL CYCLE TIMER  
 CBO MASTER CYCLE TIMER  
 CAA LOCAL OFFSET  
 CBA MASTER OFFSET

FEATURE	OFF	ON
1		
2		
3		
4		
5		
6		
7		
8		

LOCATION	OFF	ON
1		1
2		1
3		1
4		1
5		
6		
7		
8		

COO = 15

CCB/CDB OFFSET TIMER  
 CCC/CDC LAG GREEN TIMER  
 CCD/CDD FORCE OFF TIMER  
 CCE/CDE LONG GREEN TIMER  
 CCF/CDF NO GREEN TIMER

	D	FLAGS								E	FLAGS								F	FLAGS							
	MAX	1	2	3	4	5	6	7	8	MIN	1	2	3	4	5	6	7	8	PED	1	2	3	4	5	6	7	8
0	RCL									RCL									RCL								
1	CP 1									CP 1									CP 1								
2	CP 2									CP 2									CP 2								
3	CP 3									CP 3									CP 3								
4	CP 4									CP 4									CP 4								
5	CP 5									CP 5									CP 5								
6	CP 6									CP 6									CP 6								
7	CP 7									CP 7									CP 7								
8	CP 8									CP 8									CP 8								
9	CP 9									CP 9									CP 9								
A																			RCL 1								
B																			RCL 2								
C																											
D																											
E																											
F																											

	E	FLAGS								F	FLAGS								
	FUNCTION	1	2	3	4	5	6	7	8	FUNCTION	1	2	3	4	5	6	7	8	
0										CODE 4									0
1										CODE 5									1
2										C-RECALL									2
3										D-RECALL									3
4										EXCLUSIVE									4
5										2 PED		X							5
6										6 PED						X			6
7										4 PED				X					7
8										8 PED								X	8
9																			9
A	OLA NOT									OLA ON									A
B	OLB NOT									OLB ON									B
C	OLC NOT									OLC ON									C
D	OLD NOT									OLD ON									D
E																			E
F																			F

**LAST POWER FAILURE REGISTER**

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

**LAST FLASH TIME REGISTER**

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD



F+C+F+1+2+3+E+B+ E+PHASES or TYPE+EVENT NO.									
		PHASES		TYPE		PHASES		TYPE	
		C	D	E	F				
0	I1	1		5,6		J1	5		5,6
1	I2U	2		5,6		J2U	6		5,6
2	I2L	2		5,6		J2L	6		5,6
3	I3U	2		5,6		J3U	6		5,6
4	I3L	2		5		J3L	6		5
5	I4	2		7,8		J4	6		7,8
6	I5	3		5,6		J5	7		5,6
7	I6U	4		5,6		J6U	8		5,6
8	I6L	4		5,6		J6L	8		5,6
9	I7U	4		5,6		J7U	8		5,6
A	I7L	4		5		J7L	8		5
B	I8	4		7,8		J8	8		7,8
C	I9U	1		5,6		J9U	5		5,6
D	I9L	3		5,6		J9L	7		5,6

DETECTOR TYPE

- 1 RED LOCK
- 2 YELLOW LOCK
- 5 EXTENSION
- 6 COUNT
- 7 CALLING
- 8 TYPE 3 DISCONNECT

DETECTOR SETTINGS									
I FILE					J FILE				
DELAY		CARRYOVER			DELAY		CARRYOVER		
I1	D10		D30		J1	D20		D40	
I2U	D11		D31	1.0	J2U	D21		D41	1.0
I2L	D12		D32		J2L	D22		D42	
I3U	D13		D33		J3U	D23		D43	
I3L	D14		D34		J3L	D24		D44	
I4	D15		D35		J4	D25		D45	
I5	D16		D36		J5	D26		D46	
I6U	D17	2.0	D37		J6U	D27		D47	
I6L	D18	2.0	D38		J6L	D28		D48	
I7U	D19		D39		J7U	D29		D49	
I7L	D1A		D3A		J7L	D2A		D4A	
I8	D1B		D3B		J8	D2B		D4B	
I9U	D1C		D3C		J9U	D2C		D4C	
I9L	D1D		D3D		J9L	D2D		D4D	

REASSIGNS DETECTORS TO VARIOUS PHASES / FUNCTIONS

F-C-F MUST EQUAL ZERO WHEN FINISHED

LOWER CASE NUMBERS ARE DEFAULT VALUES

BLANK SPACES CONTAIN DEFAULTS (DO NOT ZERO OUT)

# INTERSECTION: Ruffin Road & Ridgehaven Court

22 program

Group Assignment:  
Field Master Assignment: None

N/S Street Name: Ruffin Road  
E/W Street Name: Ridgehaven Court

Last Database Change  
System Ref. Number:

Turned on 6/3/04 SP

Row	Phase # →	Ruffin Road		Ridgehaven Court		Ruffin Road		Ridgehaven Court	
		1	2	3	4	5	6	7	8
0	Ped Walk		7		7		7		7
1	Ped FDW		13		22		13		22
2	Min Green		10		4		10		4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn		4.2		2.0		4.2		2.0
6	Max Gap		4.2		2.0		4.2		2.0
7	Min Gap		0.2		2.0		0.2		2.0
8	Max Limit		60		30		60		30
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.7				0.7		
E	Yellow		4.7 4.4		3.9 3.4		4.7 4.4		3.9 3.4
F	Red Clear		1.0		1.0		1.0		1.0
	Grade								

Phase Timing - Bank 1  
F + Phase + Row

<F Page>

	E	F	Row
RR-1 Delay		Permit	0
RR-1 Clear		Red Lock	1
EV-A Delay	0	Yellow Lock	2
EV-A Clear	0	Min Recall	3
EV-B Delay	0	Ped Recall	4
EV-B Clear	0	Peds (View)	5
EV-C Delay	0	Rest In Walk	6
EV-C Clear	0	Red Rest	7
EV-D Delay	0	DbI Entry	8
EV-D Clear	0	Max Recall	9
RR-2 Delay		Soft Recall	A
RR-2 Clear		Max 2	B
View EV Delay	---	Cond Serv	C
View EV Clear	---	Ped Lock	D
View RR Delay	---	Yellow Start	E
View RR Clear	---	1st Phases	F

Preempt Timing

F + E + Row

Phase Functions <F Page>

F + F + Row

AL3  
214116

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + 0

Start / Revert Times		
Drop Number		C + 0 + 0
Zone Number		C + 0 + 1
Area Number		C + 0 + 2
Area Address		C + 0 + 3
QuicNet Channel		(QuicNet)

Communication Addresses

C + F + 0	F	Row
Free Lag	2_4_6_8	0

Lag Phases <C Page>

Overlap Timing

Row	9	C	D	0
Overlap A	Green Clear	Yellow Change	Red Clear	Load-Switch #
Overlap B				
Overlap C				
Overlap D				

<F Page>  
F + COLOR +

<D Page>  
D + 0 + OVERLAP

Downtime Flash 255 (minutes)  
Downtime Before Auto Manual Flash  
F + 0 + 8

Disable Ports 234  
Disable Communication Ports  
D + D + 9

Manual Plan	14	C + A + 1
Manual Offset		C + B + 1

Manual Selection

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Timing Sheet By: Pence  
Approved By: Pence  
Drawing Number: 32288D  
Timing Implemented On: 1/21/2004

09

Row	Time	Function	Day of Week	Column F
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**T.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
     Bit 2 - Phase Bank 2  
     Bit 3 - Phase Bank 3  
     Bit 4 - Disable Detector  
         OFF Monitor  
     Bit 7 - Detector Count Monitor  
     Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

TOD Function

7 + ROW

<D Page>

D + F + ROW

Configuration

E + F + ROW

<E Page>

**Day of Week**

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

**Assign 5 Outputs**

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omif	
6	Overlap B - Green Omif	
7	Overlap C - Green Omif	
8	Overlap D - Green Omif	
9	Overlap Yellow Flash	
A	EV-A Phases	2
B	EV-B Phases	4
C	EV-C Phases	6
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

**Extra 1 Flags**

- 1 = TBC Type 1
- 2 = NEMA Ext. Coord
- 3 = Auto Daylight Savings
- 4 = EV Advance
- 5 = Remote Download
- 6 = Special Event
- 7 = Pretimed Operation
- 8 = Split Ring Operation

**IC Select Flags**

- 1 =
- 2 = Modem
- 3 = 7-Wire Slave
- 4 = Flash / Free
- 5 =
- 6 = Simplex Master
- 7 = 7-Wire Master
- 8 = Offset Interrupter

Configuration

E + E + ROW

For access, set F + 9 + E = 1

**Time and Date**

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

**Program Information**

- C + C + 0 = program
- C + C + F = version

**Remote Download**

- C + 0 + 4 = 1-255
- w/ E + E + E bit 5 on

Disable Parity	0	D+B+0
----------------	---	-------

**Dial-Up Telephone Communications**

(If set to a non-zero value, parity will be disabled)

Row	1 Delay	3 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7	10.0	
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row
A
B
C
D
E
F

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12 -- -- --	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- 21 22 23 24	5678
-- -- -- -- -- -- --	1234
-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row
0
1
2
3
4
5
6
7
8

0	Detector #
	System Det. # 1
	System Det. # 2
	System Det. # 3
	System Det. # 4
	System Det. # 5
	System Det. # 6
	System Det. # 7
	System Det. # 8

System Detectors <D Page>

Row	2 Delay	4 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7	10.0	
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

**Detector Failure Monitor**

Phase Number		F+C+1
Time Before Yellow		F+C+3

**Advance Warning Beacon - Sign 1**

Phase Number		F+D+1
Time Before Yellow		F+D+3

**Advance Warning Beacon - Sign 2**

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

# INTERSECTION: CONVOY ST & OTHELLO

Group Assignment: NONE  
Field Master Assignment: NONE

NIS Control Name: CONVOY ST  
EW Street Name: OTHELLO

223 **gram**

Last Database Change: 08/07/2006, ...:47  
System Ref. Number: 120  
Drawing Number: 273006-5  
Timing Implemented On: 12/17/01

Timing Sheet By: MMH  
Approved By: *mm*

Row	1	2	3	4	5	6	7	8
Ped Walk	→	↑		←	←	↑	→	
Ped FDW		7		7		7		
Min Green		13		13		13		
Type 3 Limit		4		4		4		
Add/Veh		2.0		2.0		2.0		
Veh Exln		2.0	5.0	4.7	2.0	2.0	2.0	
Max Gap		2.0	5.0	4.7	2.0	2.0	4.6	
Min Gap		2.0	2.0	0.2	2.0	2.0	0.2	
Max Limit		30	30	20	40	30	20	
Max Limit 2		30	50	10	40	30	10	
Bus Adv								
Call to Phs								
Reduce By		0.1					0.1	
Every		0.7					0.7	
Yellow		3.4	3.0	4.1	3.4	3.0	4.0	
Red Clear		1.0		1.0		1.0	1.0	

Phase Timing - Bank 1  
F + Phase + Row

Row	9	C	D	0
Green Clear				
Yellow Change				
Red Clear				
Load-Switch #				

Overlap Timing

Row	A	B	C	D
Overlap A				
Overlap B				
Overlap C				
Overlap D				

Communication Addresses

C + F + O	F	Row
Free Lag	2_4_6	0

Lag Phases <C Page>

Row	0
Free Lag	0

Disable Ports

Disable Ports	234
Disable Communication Ports	234

Phase Functions

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Permit	12_456															
Red Lock																
Yellow Lock																
Min Recall	2_6															
Ped Recall																
Peds (View)	2_4_6															
Rest In Walk																
Red Rest																
Dbl Entry																
Max Recall																
Soft Recall	2_6															
Max 2																
Cond Serv																
Ped Lock	12345678															
Yellow Start	2_6															
1st Phases	4															

Manual Plan

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection  
Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Row	Function	Day of Week	Phases/Bits
0	19 : 00 B 11	1234567	2 6
1	11 : 00 B 11	1234567	
2	13 : 00 B 11	1234567	2 6
3	16 : 00 B 11	1234567	
4	:		
5	:		
6	:		
7	:		
8	:		
9	:		
A	:		
B	:		
C	:		
D	:		
E	:		
F	:		

**I.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 = Rest In Walk  
 6 = Red Rest  
 7 = Double Entry  
 8 = Veh Max Recall  
 9 = Veh Soft Recall  
 A = Maximum 2  
 B = Conditional Service  
 C = Free Lag Phases  
 D = Bit 1 - Local Override  
 E = Bit 2 - Phase Bank 2  
 F = Bit 3 - Phase Bank 3  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row	Function	Day of Week	Phases/Bits
0	19 : 00 B 11	1234567	2 6
1	11 : 00 B 11	1234567	
2	13 : 00 B 11	1234567	2 6
3	16 : 00 B 11	1234567	
4	:		
5	:		
6	:		
7	:		
8	:		
9	:		
A	:		
B	:		
C	:		
D	:		
E	:		
F	:		

<D Page>

D + F + ROW

TOD Function

7 + ROW

Configuration

E + E + ROW

<E Page>

Row	Function	Day of Week	Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omit		
6	Overlap B - Green Omit		
7	Overlap C - Green Omit		
8	Overlap D - Green Omit		
9	Overlap Yellow Flash		
A	EV-A Phases		
B	EV-B Phases		
C	EV-C Phases		
D	EV-D Phases		
E	Extra 1 Config. Bits	1 345	
F	IC Select (Interconnect)	2	

**Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

**IC Select Flags**  
 1 = Modem  
 2 = 7-Wire Slave  
 3 = Flash / Free  
 4 = Simplex Master  
 5 = 7-Wire Master  
 6 = Offset Interrupter

**Day of Week**  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

**Time and Date**  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

**Program Information**  
 C + C + 0 = program  
 C + C + F = version

**Remote Download**  
 C + 0 + 4 = 1 - 255  
 w/ E + E + E bit 5 on

**Disable Parity**  
 0  
**Dial-Up Telephone Communications**  
 (If set to a non-zero value, parity will be disabled)

For access, set F + 9 + E = 1  
 Configuration  
 E + E + ROW

Row	1	3	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7	12.0		
8	12.0		
9			
A			
B			
C			
D			
E	---		---
F	---		---

*08/11/2001*

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	2	4	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E	---		---
F	---		---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	A	B	C	D	E	F
Detector Numbers	12345678	1234	12345678	5678	1234	2345

Active Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8
Detector #	0								

System Det. #	Detector #
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7
Power Cycle Correction (Default = 0.5)		

Coordination Timing By: MXM  
Implemented On: 8/10/98

FOR OBSERVATION ONLY

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Column #	1	2	3	4	5	6	7	8	9
0									
1		100							
2		60.54							
3									
4		34							
5		48							
6		15							
7		40							
8									
9									
A		65							
B		68							
C									
D		12	10						
E		255							
F									

<C Page>

Coordination C + Plan + ROW

*note: controller is set for free operation  
end 1-29-13*

Row	Time	Plan	Offset	Day of Week
0	09 : 00	2	A	1234567
1	11 : 30	2	B	23456
2	13 : 30	2	A	23456
3	2000 - 18 : 00	E	A	1234567
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Coordination

<9 Key with C+0+9=1>

- Plan Select
- 1 thru 9 = Coordination
- Plan 1 thru 9
- 14 or E = Free
- 15 or F = Flash

Row	E	F
0		Free Lag
1		Plan 1 - Lag
2	2 6	Plan 2 - Lag
3		Plan 3 - Lag
4		Plan 4 - Lag
5		Plan 5 - Lag
6		Plan 6 - Lag
7		Plan 7 - Lag
8		Plan 8 - Lag
9		Plan 9 - Lag
A		Coord Max *
B		Coord Lag *
C		
D		
E		
F		

Lag Phases <C Page>

C + F + FUNCTION #

Transition Type	
TBC Transition	
C + D + D	
Transition Type	
0 = Shortway	
Non-zero = Lengthen	

Sync Phases

C + E + FUNCTION #

**INTERSECTION: RUFFIN RD & SKY PARK CT**

**233 Prc am**

Group Assignment:  
Field Master Assignment:  
System Reference Number:

N/S Street: RUFFIN RD  
E/W Street: SKY PARK CT

Last Database Change:

Timing sheets by: EFF  
Approved by: EFF  
Timing implemented on: 2/5/12

Phase Numbers ->	Phase							
	1	2	3	4	5	6	7	8
Ped Walk								
Ped FDW								
Min Green	4	10	4	4	4	10	4	4
Type 3 Disconnect								
Added per Vehicle								
Velh Extension	2.0	3.5	2.0	2.0	2.0	3.5	2.0	2.0
Max Gap	2.0	3.5	2.0	2.0	2.0	3.5	2.0	2.0
Min Gap	2.0	0.2	2.0	2.0	2.0	0.2	2.0	2.0
Max Limit	30	60	40	30	30	60	40	40
Max Limit 2								
Adv / Delay Walk								
PE Min Ped FDW		1		1		1		
Cond Serv Check								
Reduce Every		0.9				0.9		
Yellow Change	3.4	4.4	3.9	3.4	3.4	4.4	3.9	3.9
Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

**Phase Timing - Bank 1 <F/1+Phase+Row>**

Current Calculated Cycle Length: C/0 + B + F	A	B	C	D
Phase 1	9			
Phase 2				
Phase 3				
Phase 4				
Phase 5				
Phase 6				
Phase 7				
Phase 8				
Max Initial				
Alternate Walk				
Alternate FDW				
Alternate Initial				
Alternate Extension				

**Alternate Timing <F/1+Column+Phase>**

Free Lag 2.4 6.8 <C/1+F+0>

Flow to Set Page Access Code:  
F/1 - C + 0 + F = 1

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Permit																
Red Lock																
Yellow Lock																
Min Recall																
Ped Recall																
View Sel Peds																
Rest In Walk																
Red Rest																
Double Entry																
Max Recall																
Soft Recall																
Max 2																
Cond. Service																
Man Chnrl Calls																
Yellow Start																
First Phases																

**Preempt Timing <F/1+E+Row> Phase Functions <F/1+F+Row>**

Drop Number	Zone Number	Area Number	Area Address	QuickNet Channel	Flash Start	Red Revert	All Red Start
					0	5.0	0.0

Manual Plan  
0 = Automatic  
1-9 = Plan 1-9  
14 = Free  
15 = Flash

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

Manual Selection  
Manual Plan 14  
Manual Offset 0

Drop Number  
Zone Number  
Area Number  
Area Address  
QuickNet Channel

Flash Start  
Red Revert  
All Red Start

Start / Revert Times

Notes: Set Min Green before PE Force Off to 4 sec at F/1+0+8

62

AL3  
2/4/12

# 233 Program

## INTERSECTION: RUFFIN RD & SKY PARK CT

Row	1	2	3	4	5	6	7	8
0								
1	45							
2								
3								
4	4 6 8							
5	4 6 8							
6	4							
7								
8								
A								
B								
C								
D								
E	3.4							
F	1.0							

Overlap Assignments <E/29+Column+Row>

Row	0	1	2	3	4	5	6	7	8	A	B	C	D	E	F
0															
1															
2															
3															
4															
5															
6															
7															
8															
A															
B															
C															
D															
E															
F															

Specials <F/2+F+Row>

Row	0	1	2	3	4	5	6	7	8	A	B	C	D	E	F
0															
1															
2															
3															
4															
5															
6															
7															
8															
A															
B															
C															
D															
E															
F															

Configuration <E/125+E+Row>

- Extra 1 Flags**
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Extended Status
  - 6 = International Ped
  - 7 = Flash - Clear Outputs
  - 8 = Split Ring
- IC Select Flags**
- 1 =
  - 2 = Modem
  - 3 = 7-Wire Slave
  - 4 = Flash / Free
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Row	0	1	2	3	4	5	6	7	8	A	B	C	D	E	F
0															
1															
2															
3															
4															
5															
6															
7															
8															
A															
B															
C															
D															
E															
F															

Configuration <E/125+F+Row>

- Extra 2 Flags**
- 1 = AWB During Initial
  - 2 = LMU Installed
  - 3 = Disable Min Walk
  - 4 = Quick/4 System
  - 5 = Ignore P/P on EV
  - 6 =
  - 7 = Reserved
  - 8 =
- Flash to PE & PE Non-Lock**
- 1 = EV A 5 = RR 1
  - 2 = EV B 6 = RR 2
  - 3 = EV C 7 = SE 1
  - 4 = EV D 8 = SE 2

Row	0	1	2	3	4	5	6	7	8	A	B	C	D	E	F
0															
1															
2															
3															
4															
5															
6															
7															
8															
A															
B															
C															
D															
E															
F															

Preemption Priority

(\* RR-1 is always Highest, and RR-2 is always Second Highest)

Row	0	1	2	3	4	5	6	7	8	A	B	C	D	E	F
0															
1															
2															
3															
4															
5															
6															
7															
8															
A															
B															
C															
D															
E															
F															

Coordination Transition Minimums

Row	0	1	2	3	4	5	6	7	8	A	B	C	D	E	F
0															
1															
2															
3															
4															
5															
6															
7															
8															
A															
B															
C															
D															
E															
F															

Daylight Savings Time

Daylight Savings Date: If set to all zeros, standard dates will be used.

Row	0	1	2	3	1	3
Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-Over
2I2U	39	45 7	2	123		1.8
6J2U	40	45 7	6	123		1.8
4I6U	41	45 7	4	123	10.0	
8J6U	42	45 7	8	123	2.0	
	43	45 7	2	123		
	44	45 7	6	123		
	45	45 7	4	123		
	46	45 7	8	123		
	47	67	2	123		
	48	67	6	123		
	49	67	4	123		
	50	67	8	123		
	55	45 7	5	123		
	56	45 7	1	123		
	57	45 7	7	123		
	58	45 7	3	123		

Program Type:

1	2	3	4	5	6	7	8
Walk							
Don't Walk							
Phase Green							
Phase Yellow							
Phase Red							
Overlap Green	35						
Overlap Yellow	37						
Overlap Red							

Redirect Phase Outputs <E/127+Column+Row>

Cabinet Type	30
--------------	----

<E/125+D+0>

Enable Redirection  
(Enable Redirection = 30)

Max OFF (minutes)	5
-------------------	---

Max ON (minutes)	60
------------------	----

<D/0+0+1>  
<D/0+0+2>

Detector Failure Monitor

Row	0	1	2	3	4	5	6	7
Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-Over		
	59	45 7	5	123				
	60	45 7	1	123				
	61	45 7	7	123				
	62	45 7	3	123				
	63	45 7	2	123				
	64	45 7	6	123				
	65	45 7	4	123				
	66	45 7	8	123				
	67	2	2	123				
	68	2	6	123				
	69	2	4	123				
	70	2	8	123				
	76	45 7	2	123				
	77	45 7	6	123				
	78	45 7	4	123				
	79	45 7	8	123				

Detector Assignments <E/126+Column+Row>

Detector Attributes

- 1 = Full Time Delay
- 2 = Ped Call
- 3 =
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

Def. Assignments

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Row	0	1	2	3	4	5	6	7
Output Port 1								
Output Port 2								
Output Port 3								
Output Port 4								
Output Port 5								
Output Port 6								
Output Port 7								

Dimming <E/125+D+Row>

Row	A	B	C	D	E	F
DELAY-A	1					
DELAY-B	1					
DELAY-C	0					
DELAY-D	0					
DELAY-E	0					
DELAY-F	0					

<D/0+B+Row> (seconds)

Delay Logic Times

Omit Alarm	#NAME?
------------	--------

<C/5+F+0>

Disable Alarm Reporting

Time	0
------	---

<C/5+C+0>

Redial Time (minutes)

(View Redial Timer at E/2+D+6)

Row	D
Number of Digits	
1 st Digit	
2 ed Digit	
3 ed Digit	
4 th Digit	
5 th Digit	
6 th Digit	
7 th Digit	
8 th Digit	
9 th Digit	
10 th Digit	
11 th Digit	
12 th Digit	
13 th Digit	
14 th Digit	
15 th Digit	

Dial-Back Telephone Number <C/5+D+Row>



Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Spec. Funct. 1	NOT-3	Max 2	Prelimed	Set Monday	Dial 2 (7-Wire)	Sim Term
1	Spec. Funct. 2	NOT-4	System Det 1	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A
2	Spec. Funct. 3	OR-4 (a)	System Det 2	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B
3	Spec. Funct. 4	OR-4 (b)	System Det 3	Plan 3	Dimming	Offset 2 (7-Wire)	EV-C
4	NAND-3 (a)	OR-5 (a)	System Det 4	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D
5	NAND-3 (b)	OR-5 (b)	System Det 5	Plan 5	Stop Time	Free (7-Wire)	RR-1
6	NAND-4 (a)	OR-6 (a)	System Det 6	Plan 6	Flash Sense	Flash (7-Wire)	RR-2
7	NAND-4 (b)	OR-6 (b)	System Det 7	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1
8	OR-7 (a)	Fig 3 Diamond	System Det 8	Plan 8	Man. Advance	NOT-1	Spec. Event 2
9	OR-7 (b)	Fig 4 Diamond	System Det 9	Plan 9	External Alarm	NOT-2	External Lag
A	OR-7 (c)	AND-4 (a)	Max Inhibit (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)
B	OR-7 (d)	AND-4 (b)	Force A (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)
C	OR-8 (a)	NAND-1 (a)	Force B (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)
D	OR-8 (b)	NAND-1 (b)	C.N.A. (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)
E	OR-8 (c)	NAND-2 (a)	Hold (nema)	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)
F	OR-8 (d)	NAND-2 (b)	Max Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)
			Min Recall				

Assignable Inputs

<E/126+Column+Row>

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)
1	Phase ON - 2	Sp Evt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)
2	Phase ON - 3	Sp Evt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)
3	Phase ON - 4	Sp Evt Out 3	Fig 3 Diamond	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)
4	Phase ON - 5	Sp Evt Out 4	Fig 4 Diamond	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)
5	Phase ON - 6	Sp Evt Out 5		Plan 5	AND-2	TOD Out 6	Free (7-Wire)
6	Phase ON - 7	Sp Evt Out 6		Plan 6	AND-3	TOD Out 7	Flash (7-Wire)
7	Phase ON - 8	Sp Evt Out 7		Plan 7	NOT-2	TOD Out 8	Preempt
8	Ph. Check - 1	Sp Evt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A
9	Ph. Check - 2	Detector Fail	NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B
A	Ph. Check - 3	Spec. Funct. 1	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C
B	Ph. Check - 4	Spec. Funct. 2	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D
C	Ph. Check - 5	Central Control	OR-6	NAND-3	RR-1	DELAY-C	
D	Ph. Check - 6	Excl. Ped DW	AND-4	NAND-4	RR-2	DELAY-D	
E	Ph. Check - 7	Excl. Ped WK	NAND-1	OR-7	Spec. Event 1	DELAY-E	
F	Ph. Check - 8		NAND-2	OR-8	Spec. Event 2	DELAY-F	

Assignable Outputs

<E/127+Column+Row>

# INTERSECTION: CONVOY ST & OSTROW

Group Assignment: NONE  
 Field Master Assignment: NONE  
 N/S Name: CONVOY ST  
 EW Street Name: OSTROW

223  
 Last Database Change: 07/25/23  
 System Ref. Number: 119  
 Drawing Number: 27300-6  
 Timing Implemented On: 12/17/01

Timing Sheet By: MMH  
 Approved By: MM

CONVOY ST.  
 OSTROW ST.  
 CONVOY ST.  
 KEARNY MESA RD.

Row	1	2	3	4	5	6	7	8
0	Ped Walk	7	7	7	7	7	7	7
1	Ped FDW	17	17	17	17	17	17	23
2	Min Green	4	4	4	4	4	4	4
3	Type 3 Limit							
4	Add/Veh							
5	Veh Extn	2.0	2.0	2.0	2.0	2.0	2.0	2.0
6	Max Gap	2.0	2.0	2.0	2.0	2.0	2.0	2.0
7	Min Gap	2.0	2.0	2.0	2.0	2.0	2.0	2.0
8	Max Limit	30	30	30	30	60	30	40
9	Max Limit 2							
A	Bus Adv							
B	Call to Phs							
C	Reduce By	0.1	0.1					
D	Every	1.2	1.2					
E	Yellow	3.4 3.0	4.3 3.4 3.0	3.9 3.0	3.4 3.0	4.0 3.4 3.0	3.4 3.0	3.9 3.6
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Row	E	F
0	Permit	12345678
1	Red Lock	
2	Yellow Lock	
3	Min Recall	
4	Ped Recall	
5	Peds (View)	2 4 6 8
6	Rest In Walk	
7	Red Rest	
8	Dbl Entry	
9	Max Recall	2 6
A	Soft Recall	
B	Max 2	
C	Cond Serv	
D	Ped Lock	12345678
E	Yellow Start	2 6
F	1st Phases	4 8

Row	9	C	D	0
9	Green Clear	Yellow Change	Red Clear	Load-Switch #
A				
B				
C				
D				

Row	F	Row
0	Free Lag	2 4 6 8
1	Drop Number	9
2	Zone Number	9
3	Area Number	5
4	Area Address	38
5	QuickNet Channel	DIG134

Manual Plan  
 Manual Offset  
 Manual Selection  
 Manual Plan  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

863

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:	:		
1	:	:		
2	:	:		
3	:	:		
4	:	:		
5	:	:		
6	:	:		
7	:	:		
8	:	:		
9	:	:		
A	:	:		
B	:	:		
C	:	:		
D	:	:		
E	:	:		
F	:	:		

TOD Function

7 + ROW

<D Page>

D + F + ROW

- I.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Day of Week

- 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

Configuration

E + F + ROW

<E Page>

Row	Exclusive Phases	Configuration
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A		2 5
B		4 7
C		1 6
D		3 8
E		1 3 4 5
F		2

Configuration

E + E + ROW

For access, set F + 9 + E = 1

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

- IC.Select.Flags  
 1 =  
 2 = Modern  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

Disable Parity

0

D+B+0

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

Program Information

- C + C + 0 = program  
 C + C + F = version

Remote Download

- C + 0 + 4 = 1-255  
 w/E + E + E bit 5 on

Row	1	3
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	11I	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2	4
0	Delay	Carry-over
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Row	A	B	C	D	E	F
Detector Numbers	1 2 3 4 5 6 7 8	9 10 11 12	13 14 15 16 17 18 19 20	-- -- -- 21 22 23 24	-- -- -- 25 26 27 28	-- -- -- 29 30 31 32

Detector Numbers	E
1 2 3 4 5 6 7 8	12345678
9 10 11 12	1234
13 14 15 16 17 18 19 20	12345678
-- -- -- 21 22 23 24	5678
-- -- -- 25 26 27 28	1234
-- -- -- 29 30 31 32	2345

Active Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8
Detector #	0								

Detector #	0
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

D + X (across) + ROW

INTERSECTION: CONVOY SL & USIKOW

663 PROJEKTAUL

Coordination Timing By: MXM  
Implemented On: 10/20/98

FOR OBSERVATION ONLY

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Column #	1	2	3	4	5	6	7	8	9
Plan Name									
Cycle Length		100							
Phase 1 - ForceOff		67							
Phase 2 - ForceOff		25							
Phase 3 - ForceOff		53							
Phase 4 - ForceOff		70							
Phase 5 - ForceOff									
Phase 6 - ForceOff									
Phase 7 - ForceOff		19							
Phase 8 - ForceOff		53							
Ring Offset									
Offset A		14							
Offset B		15							
Offset C									
Permissive		10							
Hold Release		255							
Pad Shift		7							

<C Page>

Coordination

C + Plan + ROW

Row	Time	Plan	Offset	Day of Week
0	09 : 00	2	A	23456
1	11 : 30	2	B	23456
2	13 : 30	2	A	23456
3	18 : 00	E	A	1234567
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Coordination  
<9 Key with C+0+9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

700 80

Row	E	F
0		2 4 6 8
1		
2	2 6	2 4 6 8
3	EFF	
4	2/25/02	
5		
6		
7		
8		
9		
A		
B		
C		
D		
E		
F		

Lag Phases <C Page>  
C + F + FUNCTION #

Transition Type  
TBC Transition  
C + D + D

Transition Type  
0 = Shortway  
Non-zero = Lengthen



INTERSECTION: CONVOY ST & AERO DR

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1	08 : 00	3	23456	1
2	18 : 00	3	23456	2 6
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function

7 + ROW

<D Page>

D + F + ROW

- T.O.D. Functions
- 0 = Permitted Phases
  - 1 = Red Lock
  - 2 = Yellow Lock
  - 3 = Veh Min Recall
  - 4 = Ped Recall
  - 5 =
  - 6 = Rest in Walk
  - 7 = Red Rest
  - 8 = Double Entry
  - 9 = Veh Max Recall
  - A = Veh Soft Recall
  - B = Maximum 2
  - C = Conditional Service
  - D = Free Lag Phases
  - E = Bit 1 - Local Override
  - Bit 2 - Phase Bank 2
  - Bit 3 - Phase Bank 3
  - Bit 4 - Disable Detector
  - OFF Monitor
  - Bit 7 - Detector Count Monitor
  - Bit 8 - Real Time Spill Monitor
  - F = Output Bits 1 thru 4

Row	Configuration	<E Page>
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	
8	Ped 8P	7
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

E + F + ROW

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omit		
6	Overlap B - Green Omit		
7	Overlap C - Green Omit		
8	Overlap D - Green Omit		
9	Overlap Yellow Flash		
A	EV-A Phases		
B	EV-B Phases		
C	EV-C Phases		
D	EV-D Phases		8
E	Extra 1 Config. Bits		1 345
F	IC Select (Interconnect)		2

Configuration

E + E + ROW

For access, set F + 9 + E = 1

- Assign 5 Outputs
- 1 = Right Turn Overlap
  - 2 = TOD Outputs
  - 3 = EV Beacon - Steady
  - 4 = EV Beacon - Flashing
  - 5 = Special Event Outputs
  - 6 = Phase 3 & 7 Ped
  - 7 = Advanced Warning Sign
  - 8 =

- Extra 1 Flags
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Remote Download
  - 6 = Special Event
  - 7 = Prelimed Operation
  - 8 = Split Ring Operation

- IC Select Flags
- 1 = Modem
  - 2 = 7-Wire Slave
  - 3 = Flash / Free
  - 4 =
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Time and Date

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Disable Parity

Dial-Up Telephone Communications  
(If set to a non-zero value, parity will be disabled)

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

Program Information

- C + C + 0 = program
- C + C + F = version



Coordination Timing By: MC1  
 Implemented On: 1/27/2012

FOR OBSERVATION ONLY

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Row	Column #	1	2	3	4	5	6	7	8	9
0	Cycle Length		100		110	120				
1	Phase 1 - ForceOff		36		24	36				
2	Phase 2 - ForceOff		0		0	0				
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff		0							
5	Phase 5 - ForceOff		36		24	36				
6	Phase 6 - ForceOff		51		40	52				
7	Phase 7 - ForceOff		82		75	85				
8	Phase 8 - ForceOff									
9	Ring Offset		1		72	56				
A	Offset A		1							
B	Offset B									
C	Offset C									
D	Permissive		10		11	12				
E	Hold Release		255		255	255				
F	Peel Shift		10		0	10				

<C Page>

Coordination

C + Plan + ROW

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Free Lag															
1	Plan 1 - Lag															
2	Plan 2 - Lag		2	6												
3	Plan 3 - Lag															
4	Plan 4 - Lag		2	6												
5	Plan 5 - Lag		2	6												
6	Plan 6 - Lag															
7	Plan 7 - Lag															
8	Plan 8 - Lag															
9	Plan 9 - Lag															
A	Coord Max *															
B	Coord Lag *															
C																
D																
E																
F																

Lag Phases <C Page>  
 C + F + FUNCTION #

Sync Phases  
 C + E + FUNCTION #

Transition Type  
 TBC Transition  
 C + D + D

Transition Type  
 0 = Shortway  
 Non-zero = Lengthen

Row	Time	Plan	Offset	Day of Week
0	08 : 00	4	A	23456
1	11 : 30	2	B	23456
2	15 : 00	5	A	23456
3	18 : 00	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination

<9 Key with C+0+9=1>

Plan Select  
 1 thru 9 = Coordination  
 Plan 1 thru 9  
 14 or E = Free  
 15 or F = Flash

INTERSECTION: AERO & KEARNY VILLA

Row	Function	Day of Week	Column F Phases/Bits
0			F
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		6
7	Ped 4P		4
8	Ped 8P		8
9	Yellow Flash Phases		
A	Overlap A - Phases		1 8
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		1

**I.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**TOD Function**  
 7 + ROW  
 <D Page>  
 D + F + ROW

**Day of Week**  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases		E
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omit		8
6	Overlap B - Green Omit		
7	Overlap C - Green Omit		
8	Overlap D - Green Omit		
9	Overlap Yellow Flash		2 5
A	EV-A Phases		4 7
B	EV-B Phases		1 6
C	EV-C Phases		3 8
D	EV-D Phases		1 3 4 5
E	Extra 1 Config. Bits		2
F	IC Select (Interconnect)		

**Assign 5 Outputs**  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

**Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

**IC Select Flags**  
 1 = Modem  
 2 = 7-Wire Slave  
 3 = Flash / Free  
 4 = Simplex Master  
 5 = 7-Wire Master  
 6 = Offset Interrupter  
 7 =  
 8 =

**Time and Date**  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

**Program Information**  
 C + C + 0 = program  
 C + C + F = version

**Remote Download**  
 C + 0 + 4 = 1 -255  
 w/ E + E + E bit 5 on

**Configuration**  
 E + F + ROW  
 For access, set F + 0 + E = 1

Row	1	3	Carry-over
0			
1		1.8	
2			
3			
4			
5			
6			
7		1.8	
8			
9			
A			
B			
C			
D			
E	---	---	---
F	---	---	---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	A	B	C	D	E	F
1						
2						
3						
4						
5						
6						
7						
8						
9						
10						
11						
12						
13						
14						
15						
16						
17						
18						
19						
20						
21						
22						
23						
24						
25						
26						
27						
28						

Detector Numbers	E
1	
2	
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	
21	
22	
23	
24	
25	
26	
27	
28	

Active Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8
0									
1									
2									
3									
4									
5									
6									
7									
8									

System Det. #	Detector #
System Det. # 1	0
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Row	2	4	Carry-over
0			
1		1.8	
2			
3			
4			
5			
6			
7			
8		1.8	
9			
A			
B			
C			
D			
E	---	---	---
F	---	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F
Detector Failure Monitor		
Phase Number		F+C+1
Time Before Yellow		F+C+3
Advance Warning Beacon - Sign 1		
Phase Number		F+D+1
Time Before Yellow		F+D+3
Advance Warning Beacon - Sign 2		
Long Failure		F+0+6
Short Failure		F+0+7
Power Cycle Correction (Default = 0.5)	0.5	F+0+7



# INTERSECTION: AERO & AERO CT

Group Assignment: 4019  
Field Master Assignment: NONE

Street Name: AERO CT  
EW Street Name: AERO

Program  
11:43

Last Database Change: 07  
System Ref. Number: 111  
Drawing Number: 27300-8  
Timing Implemented On: 11/5/01

Timing Sheet By: MMH  
Approved By: *MM*

Row	1	2	3	4	5	6	7	8
Ped Walk	7			7			7	
Ped FDW	13			23			21	
Min Green	4			4			10	
Type 3 Limit								
Add/Veh								
Veh Extn	2.0	4.0		2.0	2.0	2.0	3.8	
Max Gap	2.0	4.0		2.0	2.0	2.0	3.8	
Min Gap	2.0	0.2		2.0	2.0	0.2	60	
Max Limit	30	60		40	30			
Max Limit 2								
Bus Adv								
Call to Phs								
Reduce By		0.1					0.1	
Every		0.8					0.8	
Yellow	3.4	3.0		3.9	3.0	3.4	3.0	4.3
Red Clear	1.0	1.0		1.0	1.0	1.0	1.0	

Phase Timing - Bank 1  
F + Phase + Row

Max Initial	0
Red Revert	5.0
All Red Start	0.0
Drop Number	12
Zone Number	12
Area Number	5
Area Address	30
QuickNet Channel	DIG134:

### Communication Addresses

C + F + O	2 4 6
Free Lag	0

Lag Phases <C Page>

Overlap A	Overlap B	Overlap C	Overlap D
9			

### Overlap Timing

Green Clear	Yellow Change	Red Clear	Load-Switch #
			0

<F Page>  
F + COLOR +

Downtime Flash 255 (minutes)

Downtime Before Auto Manual Flash

F + O + 8

Row	E	F
RR-1 Delay		12_456
RR-1 Clear		
EV-A Delay	0	
EV-A Clear	0	
EV-B Delay		
EV-B Clear		
EV-C Delay	0	2_4_6
EV-C Clear	0	
EV-D Delay		
EV-D Clear		
RR-2 Delay		
RR-2 Clear		
View EV Delay	...	
View EV Clear	...	
View RR Delay	...	
View RR Clear	...	

Preempt Timing  
F + E + Row

Phase Functions  
F + F + Row

Manual Plan	14
Manual Offset	0

### Manual Selection

Manual Plan  
0 = Automatic  
1 = Offset A  
14 = Free  
15 = Flash

Disable Ports 234

Disable Communication Ports  
D + D + 9

66

INTERSECTION: AERO & AERO CT

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:	:	:	:
1	:	:	:	:
2	:	:	:	:
3	:	:	:	:
4	:	:	:	:
5	:	:	:	:
6	:	:	:	:
7	:	:	:	:
8	:	:	:	:
9	:	:	:	:
A	:	:	:	:
B	:	:	:	:
C	:	:	:	:
D	:	:	:	:
E	:	:	:	:
F	:	:	:	:

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions

- 0 = Permitted Phases
- 1 = Red Lock
- 2 = Yellow Lock
- 3 = Veh Min Recall
- 4 = Ped Recall
- 5 =
- 6 = Rest In Walk
- 7 = Red Rest
- 8 = Double Entry
- 9 = Veh Max Recall
- A = Veh Soft Recall
- B = Maximum 2
- C = Conditional Service
- D = Free Lag Phases
- E = Bit 1 - Local Override
- Bit 2 - Phase Bank 2
- Bit 3 - Phase Bank 3
- Bit 4 - Disable Detector OFF Monitor
- Bit 7 - Detector Count Monitor
- Bit 8 - Real Time Split Monitor
- F = Output Bits 1 thru 4

Row	Function	Day of Week	Column F Phases/Bits
0	:	:	:
1	RR Overlap A - Phases	:	:
2	RR Overlap B - Phases	:	:
3	RR Overlap C - Phases	:	:
4	RR Overlap D - Phases	:	:
5	Ped 2P	2	:
6	Ped 6P	6	:
7	Ped 4P	4	:
8	Ped 8P	:	:
9	Yellow Flash Phases	:	:
A	Overlap A - Phases	:	:
B	Overlap B - Phases	:	:
C	Overlap C - Phases	:	:
D	Overlap D - Phases	:	:
E	Restricted Phases	:	:
F	Assign 5 Outputs	:	:

Configuration

E + F + ROW

<E Page>

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases	:	:
1	RR-1 Clear Phases	:	:
2	RR-2 Clear Phases	:	:
3	RR-2 Limited Service	:	:
4	Prot / Perm Phases	:	:
5	Overlap A - Green Omit	:	:
6	Overlap B - Green Omit	:	:
7	Overlap C - Green Omit	:	:
8	Overlap D - Green Omit	:	:
9	Overlap Yellow Flash	2 5	:
A	EV-A Phases	:	:
B	EV-B Phases	:	:
C	EV-C Phases	1 6	:
D	EV-D Phases	:	:
E	Extra 1 Config. Bits	1 345	:
F	IC Select (Interconnect)	2	:

Configuration

E + E + ROW

For access, set F + 9 + E = 1

- Extra 1 Flags
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Remote Download
  - 6 = Special Event
  - 7 = Prelimed Operation
  - 8 = Split Ring Operation

IC Select Flags

- 1 = Modem
- 2 = 7-Wire Slave
- 3 = Flash / Free
- 4 =
- 5 = Simplex Master
- 6 = 7-Wire Master
- 7 =
- 8 = Offset Interrupter

Day of Week

- 1 = Sunday
- 2 = Monday
- 3 = Tuesday
- 4 = Wednesday
- 5 = Thursday
- 6 = Friday
- 7 = Saturday
- 8 =

Assign 5 Outputs.

- 1 = Right Turn Overlap
- 2 = TOD Outputs
- 3 = EV Beacon - Steady
- 4 = EV Beacon - Flashing
- 5 = Special Event Outputs
- 6 = Phase 3 & 7 Ped
- 7 = Advanced Warning Sign
- 8 =

Time and Date

- B-0 Hour, Minute, Day-of-Week
- B-1 Day-of-Month, Year, Month
- B-F Seconds

Program Information

- C + C + 0 = program
- C + C + F = version

Remote Download

- C + 0 + 4 = 1 -255
- w/ E + E + E bit 5 on

Disable Parity  0

Dial-Up Telephone Communications

(If set to a non-zero value, parity will be disabled)

Row	1	3	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7	10.0		
8			
9			
A			
B	10.0		
C			
D			
E	---		---
F	---		---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	2	4	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E	---		---
F	---		---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Row	A	B	C	D	E	F
Detector Numbers	1 2 3 4 5 6 7 8	9 10 11 12	13 14 15 16 17 18 19 20	-- -- -- 21 22 23 24	-- -- -- -- -- -- --	-- 25 26 27 28 -- -- --

Detector Numbers	E
12345678	12345678
1234	1234
12345678	12345678
1234	1234
2345	2345

Active Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8
Detector #	0								
System Det. # 1									
System Det. # 2									
System Det. # 3									
System Det. # 4									
System Det. # 5									
System Det. # 6									
System Det. # 7									
System Det. # 8									

Detector #	0
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

D + X (across) + ROW

Row	1	2	3	4	5	6	7	8	Row
0	↓	→		↓	↗	←		↑	F
1		7							12_456_8
2		10							
3		4		4	4	10			2_6
4									
5	2.0	3.9		2.0	2.0	3.6		2.0	
6	2.0	3.9		2.0	2.0	3.6		2.0	
7	2.0	0.2		2.0	2.0	0.2		2.0	
8	30	60		40	30	60		40	
9									
A									
B									
C		0.1				0.1			
D		0.8				0.9			
E	3.4	4.4		3.9	3.4	4.3		3.9	12345678
F	1.0	1.0		1.0	1.0	1.0		1.0	2_6

**Phase Timing - Bank 1**  
 F + Phase + Row  
 <F Page>  
**Preempt Timing**  
 F + E + Row  
**Phase Functions <F Page>**  
 F + F + Row

Row	A	B	C	D	Manual Plan	Manual Offset
0					14	0
1						
2						
3						
4						
5						
6						
7						
8						
9						
A						
B						
C						
D						

**Overlap Timing**  
 D + 0 + OVERLAP  
 <D Page>  
**Manual Selection**  
 Manual Plan  
 0 = Automatic  
 1 = Offset A  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash  
**Timing Sheet By:** FLG  
**Approved By:** EFF  
**Drawing Number:**  
**Timing Implemented On:**

**Phase Timing - Bank 1**  
 F + Phase + Row  
 <F Page>  
**Overlap Timing**  
 D + 0 + OVERLAP  
 <D Page>  
**Manual Selection**  
 Manual Plan  
 0 = Automatic  
 1 = Offset A  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash  
**Timing Sheet By:** FLG  
**Approved By:** EFF  
**Drawing Number:**  
**Timing Implemented On:**

67

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:			
1	:			
2	:			
3	:			
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

**TOD Function**  
7 + ROW  
<D Page>  
D + F + ROW

Row	Function	Day of Week	Column F Phases/Bits
0			
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		
7	Ped 4P		
8	Ped 8P		8
9	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		

**Configuration**  
E + F + ROW  
<E Page>

- T.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest in Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row	Function	Day of Week	Column F Phases/Bits
0	Exclusive Phases		
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green/Omit		
6	Overlap B - Green/Omit		
7	Overlap C - Green/Omit		
8	Overlap D - Green/Omit		
9	Overlap Yellow Flash		
A	EV-A Phases		2 5
B	EV-B Phases		4
C	EV-C Phases		1 6
D	EV-D Phases		8
E	Extra 1 Config_Bits		1 45
F	IC Select (Interconnect)		2

**Configuration**  
E + E + ROW  
For access, set F + 9 + E = 1

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Pretimed Operation  
 8 = Split Ring Operation

- IC Select Flags**  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

- Day of Week**  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

- Assign 5 Outputs**  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

**Disable Parity** 0  
D+B+0

**Dial-Up Telephone Communications**  
(If set to a non-zero value, parity will be disabled)

**Time and Date**

- 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

**Program Information**

- C + C + 0 = program  
 C + C + F = version

**Remote Download**

- C + 0 + 4 = 1 -255  
 w/ E + E + E bit 5 on

Row	1	3	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			
8	10.0		
9			
A			
B			
C			
D			
E	---		---
F	---		---

Detector Name	332 Input File	Detector Number
	111	14
	212U	1
	212L	5
	213U	21
	213L	25
	214	9
	315	16
	416U	3
	416L	7
	417U	23
	417L	27
	418	11
	119U	18
	319L	20
---	---	---
---	---	---

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row	Detector #
0	0
1	
2	
3	
4	
5	
6	
7	
8	

System Det. #	Detector #
System Det. # 1	
System Det. # 2	
System Det. # 3	
System Det. # 4	
System Det. # 5	
System Det. # 6	
System Det. # 7	
System Det. # 8	

System Detectors <D Page>

Row	2	4	Carry-over
0			
1			1.8
2			
3			
4			
5			
6			
7			
8	10.0		
9			
A			
B			
C			
D			
E	---		---
F	---		---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	F+C+1
Time Before Yellow	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	F+D+1
Time Before Yellow	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

D + X (across) + ROW

# INTERSECTION: AERO DR & BROADSTONE DWY

Group Assignment:  
Field Master Assignment:  
System Reference Number:

N/S Street: Broadstone Dwy  
E/W Street: Aero Dr

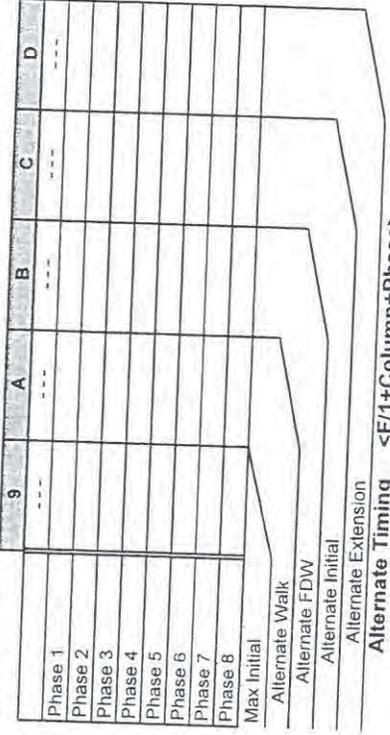
## 233 Program

Last Database Change:

Timing sheets by: **CC**  
Approved by:  
Timing implemented on: **1/29/2015**

Row	Phase							
	1	2	3	4	5	6	7	8
Ped Walk	7	7						
Ped FDW	4	7						
Min Green			4	7				
Type 3 Disconnect								
Added per Vehicle								
Veh Extension	2.0	4.7			2.0	4.7		2.0
Max Gap	2.0	4.7			2.0	4.7		2.0
Min Gap	2.0	0.2			2.0	0.2		2.0
Max Limit	30	60			30	60		40
Max Limit 2								
Adv. / Delay Walk						0.7		
PE Min Ped FDW					3.4	4.2		3.9
Cond Serv Check								
Reduce Every								
Yellow Change	3.4	4.2			1.0	1.0		1.0
Red Clear	1.0	1.0						

Current Calculated Cycle Length: C/0 + B + F



Alternate Walk  
Alternate FDW  
Alternate Initial  
Alternate Extension

Alternate Timing <F/1+Column+Phase>  
Free Lag 2 6 8 <C/1+F+0>

How to Set Page Access Code:  
F/1 -- C + 0 + F = 1  
F + 9 + E = 1

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Permit																
Red Lock																
Yellow Lock																
Min Recall																
Ped Recall																
View Ser Peds																
Rest In Walk																
Red Rest																
Double Entry																
Max Recall																
Soft Recall																
Max 2																
Cond. Service																
Man Cntrl Calls																
Yellow Start																
First Phases																

Preempt Timing <F/1+E+Row> Phase Functions <F/1+F+Row>

Drop Number	15	<C/0+0+0>
Zone Number	15 <td>&lt;C/0+0+1&gt;</td>	<C/0+0+1>
Area Number	5 <td>&lt;C/0+0+2&gt;</td>	<C/0+0+2>
Area Address	214 <td>&lt;C/0+0+3&gt;</td>	<C/0+0+3>
QuickNet Channel	COM38	(QuickNet)

Flash Start	0	<F/1+0+E>
Red Revert	5.0 <th>&lt;F/1+0+F&gt;</th>	<F/1+0+F>
All Red Start	0.0 <th>&lt;F/1+C+0&gt;</th>	<F/1+C+0>

Start / Revert Times

Notes: Drawing No. 37203-16-D

(Outputs specified in Assignable Outputs at E/17+A+E & F)

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

Exclusive Ped Phase

Manual Plan	14	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Manual Selection

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INTERSECTION: AERO DR & BROADSTONE DWY

Row	1	2	3	4	5	6	7	8
0								
1								
2								
3								
4								
5								
6								
7								
8								
A								
B								
C								
D								
E								
F								

Overlap Assignments <E/29+Column+Row>

Row	F
0	
1	
2	
3	
4	12345678
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Specials <F/2+F+Row>

Fast Green Flash Phase
Green Flash Phases
Flashing Walk Phases
Guaranteed Passage
Simultaneous Gap Term
Sequential Timing
Advance Walk Phases
Delay Walk Phases
External Recall
Start-up Overlap Green
Max Extension
Inhibit Ped Reservice
Semi-Actuated
Start-up Overlap Yellow
Start-up Vehicle Calls
Start-up Ped Calls

Row	E
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Configuration <E/125+E+Row>

- Extra 1 Flags
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Extended Status
  - 6 = International Ped
  - 7 = Flash - Clear Output
  - 8 = Split Ring
- IC Select Flags
- 1 =
  - 2 = Modern
  - 3 = 7-Wire Slave
  - 4 = Flash / Free
  - 5 =
  - 6 = Simplex Master
  - 7 = 7-Wire Master
  - 8 = Offset Interrupter

Row	C
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Preemption Priority

(\* RR-1 is always Highest, and RR-2 is always Second Highest)

Row	F
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Configuration <E/125+F+Row>

- Flash to PE & PE Non-Lock
- 1 = EVA
  - 2 = EVB
  - 3 = EVC
  - 4 = EVD
  - 5 = RR 1
  - 6 = RR 2
  - 7 = SE 1
  - 8 = SE 2
- Extra 2 Flags
- 1 = AWB During Initial
  - 2 = LMU Installed
  - 3 = Disable Min Walk
  - 4 = QuickNet/4 System
  - 5 = Ignore PIP on EV
  - 6 =
  - 7 = Reserved
  - 8 =

Row	2
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Coordination Transition Minimums

Row	0
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Begin Month	0
Begin Week	0
End Month	0
End Week	0

Daylight Savings Time

Daylight Savings Date:  
If set to all zeros, standard dates will be used.





# 233 Program

Coord Extra

- 1 = Programmed WALK Time for Sync Phases
- 2 = Always Terminate Sync Phase Peds

Plan Name	1	2	3	4	5	6	7	8	9
Cycle Length									
Phase 1 - ForceOff									
Phase 2 - ForceOff									
Phase 3 - ForceOff									
Phase 4 - ForceOff									
Phase 5 - ForceOff									
Phase 6 - ForceOff									
Phase 7 - ForceOff									
Phase 8 - ForceOff									
Ring Offset									
Offset 1									
Offset 2									
Offset 3									
Perm 1 - End									
Hold Release									
Zone Offset									

Coordination - Timing Plans <C/1+Plan+Row>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ped Adjustment																
Perm 2 - Start																
Perm 2 - End																
Perm 3 - Start																
Perm 3 - End																
Reservice Time																
Reservice Phases																
Prelimed Phases																
Max Recall																
Perm 1 Veh Phase																
Perm 1 Ped Phase																
Perm 2 Veh Phase																
Perm 2 Ped Phase																
Perm 3 Veh Phase																
Perm 3 Ped Phase																

Coordination - Parameters <C/2+Plan+Row>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Plan 1 - Sync																
Plan 2 - Sync																
Plan 3 - Sync																
Plan 4 - Sync																
Plan 5 - Sync																
Plan 6 - Sync																
Plan 7 - Sync																
Plan 8 - Sync																
Plan 9 - Sync																
NEMA Sync																
NEMA Hold																
Coord Extra																

Sync Phases <C/1+E+Row>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Free Lag																
Plan 1 - Lag																
Plan 2 - Lag																
Plan 3 - Lag																
Plan 4 - Lag																
Plan 5 - Lag																
Plan 6 - Lag																
Plan 7 - Lag																
Plan 8 - Lag																
Plan 9 - Lag																
External Lag																

Lag Phases <C/1+F+Row>

Coordination Timing By:

Version: 233 RV2  
Revision: San Diego 1

INTERSECTION: AERO DR & BROADSTONE DWY

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Spec. Funct. 1	NOT-3	Max 2	Prelimed	Set Monday	Dial 2 (7-Wire)	Sim Term
1	Spec. Funct. 2	NOT-4	System Det 1	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A
2	Spec. Funct. 3	OR-4 (a)	System Det 2	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B
3	Spec. Funct. 4	OR-4 (b)	System Det 3	Plan 3	Dimming	Offset 2 (7-Wire)	EV-C
4	NAND-3 (a)	OR-5 (a)	System Det 4	Plan 4	Stop Time	Offset 3 (7-Wire)	RR-1
5	NAND-3 (b)	OR-5 (b)	System Det 5	Plan 5	Flash Sense	Free (7-Wire)	RR-2
6	NAND-4 (a)	OR-6 (a)	System Det 6	Plan 6	Manual Enable	Flash (7-Wire)	Spec. Event 1
7	NAND-4 (b)	OR-6 (b)	System Det 7	Plan 7	Man. Advance	Excl. Ped Omit	Spec. Event 2
8	OR-7 (a)	Fig 3 Diamond	System Det 8	Plan 8	External Alarm	NOT-1	External Lag
9	OR-7 (b)	Fig 4 Diamond	Max Inhibit (nema)	Plan 9	Phase Bank 2	NOT-2	AND-1 (a)
A	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 3	OR-1 (a)	AND-1 (b)
B	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Overlap Set 2	OR-2 (a)	AND-2 (a)
C	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 3	OR-2 (b)	AND-2 (b)
D	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Detector Set 2	OR-3 (a)	AND-3 (a)
E	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 3	OR-3 (b)	AND-3 (b)
F	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F			

<E/126+Column+Row>

Assignable Inputs

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)
1	Phase ON - 2	Sp Evt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)
2	Phase ON - 3	Sp Evt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)
3	Phase ON - 4	Sp Evt Out 3	Fig 3 Diamond	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)
4	Phase ON - 5	Sp Evt Out 4	Fig 4 Diamond	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)
5	Phase ON - 6	Sp Evt Out 5		Plan 5	AND-2	TOD Out 6	Free (7-Wire)
6	Phase ON - 7	Sp Evt Out 6		Plan 6	AND-3	TOD Out 7	Flash (7-Wire)
7	Phase ON - 8	Sp Evt Out 7		Plan 7	NOT-2	TOD Out 8	Preempt
8	Ph. Check - 1	Sp Evt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A
9	Ph. Check - 2	Detector Fail	NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B
A	Ph. Check - 3	Spec. Funct. 1	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C
B	Ph. Check - 4	Spec. Funct. 2	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D
C	Ph. Check - 5	Spec. Funct. 3	OR-6	NAND-3	RR-1	DELAY-C	
D	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	
E	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	
F	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	

<E/127+Column+Row>

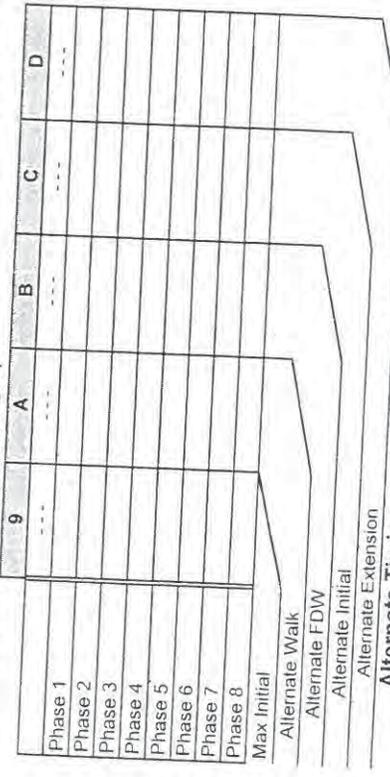
Assignable Outputs

Last Database Change:

Timing sheets by: **CAC**  
 Approved by:  
 Timing implemented on: **7/2/2015**

Row	Aero Dr		Aero Dr		Aero Dr		Sandrock Rd	
	1	2	3	4	5	6	7	8
0								
1	7	7						
2	20	18						
3	4	10	4					
4								
5	2.0	4.7			2.0	3.4	2.0	2.6
6	2.0	4.7			2.0	3.4	2.0	2.6
7	2.0	0.2			2.0	0.2	2.0	0.2
8	30	60			30	60	40	40
9								
A								
B								
C								
D								
E	3.4	4.2			3.4	4.2	3.9	1.3
F	1.0	1.0			1.0	1.0	1.0	1.0

**Phase Timing - Bank 1** <F/1+Phase+Row>  
 Current Calculated Cycle Length: C/0 + B + F



**Free Lag** 2 6 8 <C/1+F+0>

How to Set Page Access Code:  
 F/1 -- C + 0 + F = 1  
 F + 9 + E = 1

Row	E	F
Permit		12 5678
Red Lock		
Yellow Lock		
Min Recall	0	2 6
Ped Recall	0	
View Sel Peds	0	2 678
Rest In Walk	0	
Red Rest	0	
Double Entry	0	
Max Recall		
Soft Recall		
Max 2		
Cond. Service		
Man Cntrl Calls		
Yellow Start		2 6
First Phases		8

**Preempt Timing** <F/1+E+Row> **Phase Functions** <F/1+F+Row>

Drop Number	Zone Number	Area Number	Area Address	QuickNet Channel
14	14	5		DIG 34

**Communication Addresses**  
 (QuickNet)

Flash Start	Red Revert	All Red Start
0	5.0	0.0

**Start / Revert Times**

Notes: 37203-17-D

(Outputs specified in Assignable Outputs at E/127+A+E & F)

Manual Plan	Exclusive Walk	Exclusive FDW	All Red Clear
0 = Automatic	0	0	0.0
1 = Plan 1-9			
14 = Free			
15 = Flash			

**Exclusive Ped Phase**

Manual Plan	Manual Offset
0 = Automatic	14
1 = Offset A	0
2 = Offset B	
3 = Offset C	

Row	1	2	3	4	5	6	7	8
0								
1	Load Switch Number							
2	Veh Set 1 - Phases							
3	Veh Set 2 - Phases							
4	Veh Set 3 - Phases							
5	Neg Veh Phases							
6	Neg Ped Phases							
7	Green Omit Phases							
8	Green Clear Omit Phs.							
9								
A								
B								
C								
D	Green Clear							
E	Yellow Change							
F	Red Clear							

Overlap Assignments

<E/29+Column+Row>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0																
1	Fast Green Flash Phase															
2	Green Flash Phases															
3	Flashing Walk Phases															
4	Guaranteed Passage															
5	Simultaneous Gap Term															
6	Sequential Timing															
7	Advance Walk Phases															
8	Delay Walk Phases															
9	External Recall															
A	Start-up Overlap Green															
B	Max Extension															
C	Inhibit Ped Reserve															
D	Semi-Actuated															
E	Start-up Overlap Yellow															
F	Start-up Vehicle Calls															
	Start-up Ped Calls															

<F/2+F+Row>

Specials

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0																
1	Exclusive Phases															
2	RR-1 Clear Phases															
3	RR-2 Clear Phases															
4	RR-2 Limited Service															
5	Prot / Perm Phases															
6	Flash to PE Circuits															
7	Flash Entry Phases															
8	Disable Yellow Range															
9	Disable Ovp Yel Range															
A	Overlap Yellow Flash															
B	EV-A Phases															
C	EV-B Phases															
D	EV-C Phases															
E	EV-D Phases															
F	Extra 1 Config. Bits															
	IC Select (Interconnect)															

Configuration <E/125+E+Row>

- Extra 1 Flags
- 1 = TBC Type 1
  - 2 = NEMA Ext. Coord
  - 3 = Auto Daylight Savings
  - 4 = EV Advance
  - 5 = Extended Status
  - 6 = International Ped
  - 7 = Flash - Clear Outputs
  - 8 = Split Ring
- IC Select Flags
- 1 = Modem
  - 2 = 7-Wire Slave
  - 3 = Flash / Free
  - 4 = Simplex Master
  - 5 = 7-Wire Master
  - 6 = Offset Interrupter

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0																
1	Ext. Permit 1 Phases															
2	Ext. Permit 2 Phases															
3	Exclusive Ped Assign															
4	Preempt Non-Lock															
5	Ped for 2P Output															
6	Ped for 4P Output															
7	Ped for 6P Output															
8	Ped for 8P Output															
9	Yellow Flash Phases															
A	Low Priority A Phases															
B	Low Priority B Phases															
C	Low Priority C Phases															
D	Low Priority D Phases															
E	Restricted Phases															
F	Extra 2 Config. Bits															

Configuration <E/125+F+Row>

- Extra 2 Flags
- 1 = AWB During Initial
  - 2 = LMU Installed
  - 3 = Disable Min Walk
  - 4 = QuickNet/4 System
  - 5 = Ignore P/P on EV
  - 6 = Reserved
  - 7 = Reserved
  - 8 = Reserved
- Flash to PE & PE Non-Lock
- 1 = EV A 5 = RR 1
  - 2 = EV B 6 = RR 2
  - 3 = EV C 7 = SE 1
  - 4 = EV D 8 = SE 2

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0																
1	EV-A															
2	EV-B															
3	EV-C															
4	EV-D															
5	RR-1 *															
6	RR-2 *															
7	SE-1															
8	SE-2															

<E/125+C+Row>

Preemption Priority

(\* RR-1 is always Highest, and RR-2 is always Second Highest)

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0																
1	Phase 1															
2	Phase 2															
3	Phase 3															
4	Phase 4															
5	Phase 5															
6	Phase 6															
7	Phase 7															
8	Phase 8															

<C/5+2+Row>

Coordination Transition Minimums

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0																
1	Begin Month															
2	Begin Week															
3	End Month															
4	End Week															

Daylight Savings Time

Daylight Savings Date:  
If set to all zeros, standard dates will be used.

- 8-0 Hour, Minute, Day-of-Week
- 8-1 Day-of-Month, Year, Month
- 8-F Seconds

Time and Date

Row	0	1	2	3	4	5	6	7	8
0	Walk								
1	Don't Walk								
2	Phase Green								
3	Phase Yellow								
4	Phase Red								
5	Overlap Green								
6	Overlap Yellow								
7	Overlap Red								

Program Type:

Redirect Phase Outputs <E/127+Column+Row>

Cabinet Type	30
--------------	----

<E/125+D+0>

**Enable Redirection**  
(Enable Redirection = 30)

Max OFF (minutes)	20
Max ON (minutes)	60

**Detector Failure Monitor**

Dimming <E/125+D+Row>

Row	0	1	2	3	4	5	6	7
0	Output Port 1							
1	Output Port 2							
2	Output Port 3							
3	Output Port 4							
4	Output Port 5							
5	Output Port 6							
6	Output Port 7							

Row	A	B	C	D	E	F
DELAY-A	1					
DELAY-B	1					
DELAY-C	0					
DELAY-D	0					
DELAY-E	0					
DELAY-F	0					

- Disable Alarms**
- 1 = Stop Time
  - 2 = Flash Sense
  - 3 = Keyboard Entry
  - 4 = Manual Plan
  - 5 = Police Control
  - 6 = External Alarm
  - 7 = Detector Failure
  - 8 =

Delay Logic Times <D/0+B+Row> (seconds)

Omit Alarm	#NAME?
------------	--------

Disable Alarm Reporting <C/5+F+0>

Time	0
------	---

**Redial Time** (minutes)  
(View Redial Timer at E/2+D+6)

Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-Over
212U	39	45 7	2	123		1.8
6J2U	40	45 7	6	123		1.8
416U	41	45 7	4	123		
8J6U	42	45 7	8	123		1.8
212L	43	45 7	2	123		1.8
6J2L	44	45 7	6	123		1.8
416L	45	45 7	4	123		1.8
8J6L	46	45 7	8	123		
214	47	67	2	123		1.8
6J4	48	67	6	123		
418	49	67	4	123		
8J8	50	67	8	123		
5J1U	55	45 7	5	123		
111U	56	45 7	1	123		
7J5	57	45 7	7	123	15.0	
315	58	45 7	3	123		

Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-Over
5J9U	59	45 7	5	123		
119U	60	45 7	1	123		
7J9L	61	45 7	7	123		
319L	62	45 7	3	123		1.8
213U	63	45 7	2	123		1.8
6J3U	64	45 7	6	123		
417U	65	45 7	4	123		
8J7U	66	45 7	8	123		
2 PPB	67	2	2	123	15.0	
6 PPB	68	2	6	123		
4 PPB	69	2	7	123		
8 PPB	70	2	8	123		
213L	76	45 7	2	123		1.8
6J3L	77	45 7	6	123		
417L	78	45 7	4	123		
8J7L	79	45 7	8	123		

Detector Assignments <E/126+Column+Row>

Detector Attributes

- 1 = Full Time Delay
- 2 = Ped Call
- 3 =
- 4 = Count
- 5 = Extension
- 6 = Type 3
- 7 = Calling
- 8 = Alternate

Det. Assignments

- 1 = Det. Set 1
- 2 = Det. Set 2
- 3 = Det. Set 3
- 4 =
- 5 =
- 6 = Failure - Min Recall
- 7 = Failure - Max Recall
- 8 = Report on Failure

Dial-Back Telephone Number <C/5+D+Row>

Number of Digits	D
1 st Digit	
2 ed Digit	
3 ed Digit	
4 th Digit	
5 th Digit	
6 th Digit	
7 th Digit	
8 th Digit	
9 th Digit	
10 th Digit	
11 th Digit	
12 th Digit	
13 th Digit	
14 th Digit	

Row	Time	Offset	Day of Week	Time	Offset	Day of Week	Column 4 Phases/Bits	Day	Year	Month	Holiday Type	Row
0												0
1												1
2												2
3												3
4												4
5												5
6												6
7												7
8												8
9												9
A												A
B												B
C												C
D												D
E												E
F												F

**TOD Coordination <9/0.1+Row>** (Bank 1) **TOD Function <7/0.1+Row>** (Bank 1) **Holiday Dates <8/1.1+Row>** (Bank 1) **Holiday Events <9/1.1+Row>** (Bank 1)

- I.O.D. Functions:**
- 0 =
  - 1 = Red Lock
  - 2 = Yellow Lock
  - 3 = Veh Min Recall
  - 4 = Ped Recall
  - 5 =
  - 6 = Rest In Walk
  - 7 = Red Rest
  - 8 = Double Entry
  - 9 = Veh Max Recall
  - A = Veh Soft Recall
  - B = Maximum 2
  - C = Conditional Service
  - D = Free Lag Phases
  - E = Bit 1 - Local Override
  - Bit 4 - Disable Detector OFF Monitor
  - Bit 7 - Detector Count Monitor
  - Bit 8 - Real Time Split Monitor
  - F = Output Bits 1 thru 8
- Plan Select:**
- 1 thru 9 = Coordination Plan 1 thru 9
  - 14 or E = Free
  - 15 or F = Flash

Row	Time	Offset	Day of Week	Time	Offset	Day of Week	Column 4 Phases/Bits	Day	Year	Month	Holiday Type	Row
0												0
1												1
2												2
3												3
4												4
5												5
6												6
7												7
8												8
9												9
A												A
B												B
C												C
D												D
E												E
F												F

**TOD Coordination <9/0.2+Row>** (Bank 2) **TOD Function <7/0.2+Row>** (Bank 2) **Holiday Dates <8/1.2+Row>** (Bank 2) **Holiday Events <9/1.2+Row>** (Bank 2)

- Month Select:**
- 1 = January
  - 2 = February
  - 3 = March
  - 4 = April
  - 5 = May
  - 6 = June
  - 7 = July
  - 8 = August
  - 9 = September
  - A = October
  - B = November
  - C = December
- Cycle Timer:**
- Master: C/0 + A + 0
  - Ring A: C/0 + B + 0
  - Ring B: C/0 + D + 0
- Interval Timer:**
- Ring A: F/0 + A + Interval Row
  - Ring B: F/0 + B + Interval Row
- Master Plan: C/0 + A + 2**  
**Current Plan: C/0 + A + 3**  
**TOD Plan: C/0 + A + 5**

Coord Extra

1 = Programmed WALK Time for Sync Phases  
 2 = Always Terminate Sync Phase Peds

Row	Plan Name ---->	Plan												
		1	2	3	4	5	6	7	8	9				
0	Cycle Length													
1	Phase 1 - ForceOff													
2	Phase 2 - ForceOff													
3	Phase 3 - ForceOff													
4	Phase 4 - ForceOff													
5	Phase 5 - ForceOff													
6	Phase 6 - ForceOff													
7	Phase 7 - ForceOff													
8	Phase 8 - ForceOff													
9	Ring Offset													
A	Offset 1													
B	Offset 2													
C	Offset 3													
D	Perm 1 - End													
E	Hold Release													
F	Zone Offset													

Coordination - Timing Plans <C/1+Plan+Row>

Row	Plan	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Ped Adjustment																
1	Perm 2 - Start																
2	Perm 2 - End																
3	Perm 3 - Start																
4	Perm 3 - End																
5	Reservice Time																
6	Reservice Phases																
7																	
8	Prelimed Phases																
9	Max Recall																
A	Perm 1 Veh Phase																
B	Perm 1 Ped Phase																
C	Perm 2 Veh Phase																
D	Perm 2 Ped Phase																
E	Perm 3 Veh Phase																
F	Perm 3 Ped Phase																

Coordination - Parameters <C/2+Plan+Row>

Row	Plan	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Plan 1 - Sync																
1	Plan 2 - Sync																
2	Plan 3 - Sync																
3	Plan 4 - Sync																
4	Plan 5 - Sync																
5	Plan 6 - Sync																
6	Plan 7 - Sync																
7	Plan 8 - Sync																
8	Plan 9 - Sync																
9	NEMA Sync																
A	NEMA Hold																
B																	
C																	
D																	
E	Coord Extra																
F																	

Sync Phases <C/1+E+Row>

Row	Plan	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	Free Lag																
1	Plan 1 - Lag																
2	Plan 2 - Lag																
3	Plan 3 - Lag																
4	Plan 4 - Lag																
5	Plan 5 - Lag																
6	Plan 6 - Lag																
7	Plan 7 - Lag																
8	Plan 8 - Lag																
9	Plan 9 - Lag																
A	External Lag																
B																	
C																	
D																	
E																	
F																	

Lag Phases <C/1+F+Row>

Coordination Timing By:

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Spec. Funct. 1	NOT-3	Max 2	Prefimed	Set Monday	Dial 2 (7-Wire)	Sim Term
1	Spec. Funct. 2	NOT-4	System Det 1	Plan 1	Ext. Perm 1	Dial 3 (7-Wire)	EV-A
2	Spec. Funct. 3	OR-4 (a)	System Det 2	Plan 2	Ext. Perm 2	Offset 1 (7-Wire)	EV-B
3	Spec. Funct. 4	OR-4 (b)	System Det 3	Plan 3	Dimming	Offset 2 (7-Wire)	EV-C
4	NAND-3 (a)	OR-5 (a)	System Det 4	Plan 4	Set Clock	Offset 3 (7-Wire)	EV-D
5	NAND-3 (b)	OR-5 (b)	System Det 5	Plan 5	Stop Time	Free (7-Wire)	RR-1
6	NAND-4 (a)	OR-6 (a)	System Det 6	Plan 6	Flash Sense	Flash (7-Wire)	RR-2
7	NAND-4 (b)	OR-6 (b)	System Det 7	Plan 7	Manual Enable	Excl. Ped Omit	Spec. Event 1
8	OR-7 (a)	Fig 3 Diamond	System Det 8	Plan 8	Man. Advance	NOT-1	Spec. Event 2
9	OR-7 (b)	Fig 4 Diamond	Max inhibit (nema)	Plan 9	External Alarm	NOT-2	External Lag
A	OR-7 (c)	AND-4 (a)	Force A (nema)	DELAY-A	Phase Bank 2	OR-1 (a)	AND-1 (a)
B	OR-7 (d)	AND-4 (b)	Force B (nema)	DELAY-B	Phase Bank 3	OR-1 (b)	AND-1 (b)
C	OR-8 (a)	NAND-1 (a)	C.N.A. (nema)	DELAY-C	Overlap Set 2	OR-2 (a)	AND-2 (a)
D	OR-8 (b)	NAND-1 (b)	Hold (nema)	DELAY-D	Overlap Set 3	OR-2 (b)	AND-2 (b)
E	OR-8 (c)	NAND-2 (a)	Max Recall	DELAY-E	Detector Set 2	OR-3 (a)	AND-3 (a)
F	OR-8 (d)	NAND-2 (b)	Min Recall	DELAY-F	Detector Set 3	OR-3 (b)	AND-3 (b)

<E/126+Column+Row>

Assignable Inputs

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Phase ON - 1	Preempt Fail	Flasher 0	Free	NOT-1	TOD Out 1	Dial 2 (7-Wire)
1	Phase ON - 2	Sp Evt Out 1	Flasher 1	Plan 1	OR-1	TOD Out 2	Dial 3 (7-Wire)
2	Phase ON - 3	Sp Evt Out 2	Fast Flasher	Plan 2	OR-2	TOD Out 3	Offset 1 (7-Wire)
3	Phase ON - 4	Sp Evt Out 3	Fig 3 Diamond	Plan 3	OR-3	TOD Out 4	Offset 2 (7-Wire)
4	Phase ON - 5	Sp Evt Out 4	Fig 4 Diamond	Plan 4	AND-1	TOD Out 5	Offset 3 (7-Wire)
5	Phase ON - 6	Sp Evt Out 5		Plan 5	AND-2	TOD Out 6	Free (7-Wire)
6	Phase ON - 7	Sp Evt Out 6		Plan 6	AND-3	TOD Out 7	Flash (7-Wire)
7	Phase ON - 8	Sp Evt Out 7		Plan 7	NOT-2	TOD Out 8	Preempt
8	Ph. Check - 1	Sp Evt Out 8	NOT-3	Plan 8	EV-A	Adv. Warn - 1	Low Priority A
9	Ph. Check - 2		NOT-4	Plan 9	EV-B	Adv. Warn - 2	Low Priority B
A	Ph. Check - 3	Detector Fail	OR-4	Spec. Funct. 3	EV-C	DELAY-A	Low Priority C
B	Ph. Check - 4	Spec. Funct. 1	OR-5	Spec. Funct. 4	EV-D	DELAY-B	Low Priority D
C	Ph. Check - 5	Spec. Funct. 2	OR-6	NAND-3	RR-1	DELAY-C	
D	Ph. Check - 6	Central Control	AND-4	NAND-4	RR-2	DELAY-D	
E	Ph. Check - 7	Excl. Ped DW	NAND-1	OR-7	Spec. Event 1	DELAY-E	
F	Ph. Check - 8	Excl. Ped WK	NAND-2	OR-8	Spec. Event 2	DELAY-F	

<E/127+Column+Row>

Assignable Outputs

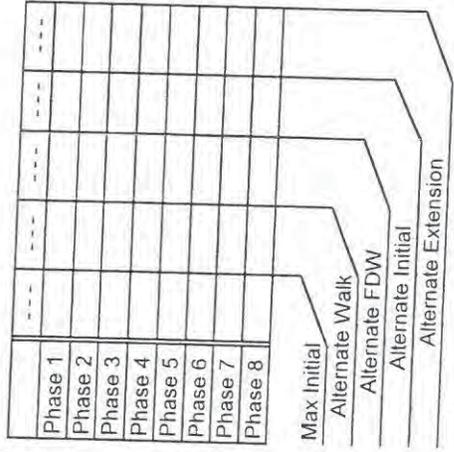
Row	Phase							
	1	2	3	4	5	6	7	8
0								
1	Ped Walk							
2	Ped FDW							
3	Min Green							
4	Type 3 Disconnect							
5	Added per Vehicle							
6	Veh Extension							
7	Max Gap							
8	Min Gap							
9	Max Limit							
A	Max Limit 2							
B	Adv. / Delay Walk							
C	PE Min Ped FDW							
D	Cond Serv Check							
E	Reduce Every							
F	Yellow Change							
	Red Clear							

Phase Timing - Bank 2 <C+0+F=2>

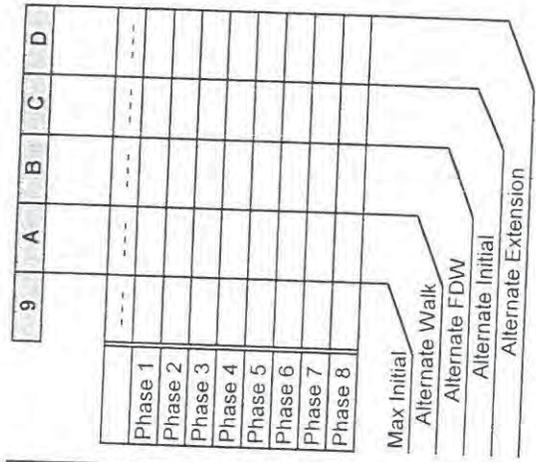
Row	Phase							
	1	2	3	4	5	6	7	8
0								
1	Ped Walk							
2	Ped FDW							
3	Min Green							
4	Type 3 Disconnect							
5	Added per Vehicle							
6	Veh Extension							
7	Max Gap							
8	Min Gap							
9	Max Limit							
A	Max Limit 2							
B	Adv. / Delay Walk							
C	PE Min Ped FDW							
D	Cond Serv Check							
E	Reduce Every							
F	Yellow Change							
	Red Clear							

Phase Timing - Bank 3 <C+0+F=3>

9	A	B	C	D
---	---	---	---	---



Alternate Timing



Alternate Timing

Transition Type  
 0.X = Shortway  
 1.X = Lengthen  
 X.1 thru X.4 = Number of cycles when lengthening

Transition Type  <C/5+1+9>

**TBC Transition**

Lag Hold Phases  <C/5+1+A>  
**Coordinated Lag Hold Phases**

Sync Output Time  <C/5+1+C>  
**7-Wire Master**

Time B4 Yellow  <F/1+C+E>  
 Phase Number  <F/1+C+F>  
**Advance Warning Beacon - Sign 1**

Time B4 Yellow  <F/1+D+E>  
 Phase Number  <F/1+D+F>  
**Advance Warning Beacon - Sign 2**

Long Failure  <F/1+0+6>  
 Short Failure  <F/1+0+7>  
**Power Cycle Correction** (Default = 0.7)

Min Time (seconds)  <F/1+0+8>  
**Min Green Before PE Force Off**

Max Time (minutes)  <F/1+0+9>  
**Max Preempt Time Before Failure**

Min Time (seconds)  <F/1+0+A>  
**Min Time Between Same Preempts**  
 (Does Not Apply To Railroad Preempt)

Low Pri. Channel  <E/125+C+8>  
**Disable Low Priority Channel**

Low Priority  
 1 = Channel A  
 2 = Channel B  
 3 = Channel C  
 4 = Channel D

# TRAFFIC SIGNAL TIMING

PROGRAM

**2005**

Aero Aero PHASE TIMING Aero Ruffin

70

INTERVAL	PHASE TIMING								PREEMPT	
	1	2	3	4	5	6	7	8		E
WALK	0	7						7	RR1 DELAY	0
FLASH D/W	1	26						20	RR1 CLEAR	1
MIN GREEN	2	4	7			7		4	EVA DELAY	0
TYPE 3 DET	3								EVA CLEAR	3
ADD/VEH	4								EVB DELAY	4
VEH EXTEN *	5	2.0	3.6			3.6		2.0	EVB CLEAR	5
MAX GAP *	6	2.0	3.6			3.6		2.0	EVC DELAY	0
MIN GAP *	7	2.0	0.2			0.2		2.0	EVC CLEAR	3
MAX EXTEN	8	40	60			60		40	EVD DELAY	8
MAX 2	9								EVD CLEAR	9
	A								RR2 DELAY	A
CALL TO PHASE	B								RR2 CLEAR	B
REDUCE BY	C		0.1			0.1			EV CLR TMR	C
REDUCE EVERY	D		0.9			0.9			EV DLY TMR	D
YELLOW	E	3.4 3.0	4.5			4.5		4.0	RR CLR TMR	E
RED CLEAR	F	1.0	1.0			1.0		1.0	RR DLY TMR	F

WH  
4/27/05

MAX INITIAL (F-0-E) = 0 KEYSTROKES: F + PHASE + LOCATION

RED REVERT (F-0-F) = 5

\* MUST BE SAME FOR NON-DENSITY OPERATION

ALL RED START (F-C-0) = 0

### PHASE FUNCTION FLAGS

KEYSTROKES: F + F + FUNCTION #

FUNCTION

		PHASE							
		1	2	3	4	5	6	7	8
PERMIT	0	X	X				X	X	
RED LOCK	1								
YELLOW LOCK	2								
VEH RECALL	3	X				X			
PED RECALL	4								
PEDS	5	X	X	X	X	X	X	X	X
REST IN WALK	6								
RED REST	7								
DOUBLE ENTRY	8								
MAX RECALL	9								
SOFT RECALL	A								
MAX 2	B								
COND SERVE	C								
RESERVED	D	X	X	X	X	X	X	X	X
STARTUP	E	X				X			
FIRST PHASES	F								X

### OVERLAP TIMING

KEYSTROKE: F + COLOR CODE + OVERLAP

9 C D  
GREEN YELLOW RED

OVERLAP A			
OVERLAP B			
OVERLAP C			
OVERLAP D			

### PHASE SEQUENCES (CFO)

LAG 0 (FREE)

PHASE							
1	2	3	4	5	6	7	8
	X				X		X

# TIME OF DAY FEATURES

## Detector Times

(Type 336 Cabinet; Citywide, except downtown)

X-	SET DELAY		SET CARRY		OBS COUNT		OBS DELAY		OBS CARRY	
	1	2	3	4	5	6	7	8	9	A
0	111U	66U								
1	22U	66U								
2	22L	66L								
3										
4										
5	21L	66L								
6	33U	77U								
7	44U	66U								
8	44L	66L								
9										
A										
B	41L	67L								
C										
D										
E										
F										

KEYSTROKES D + X + Y

### DAY OF WEEK

TIME	FUNCT	DAY OF WEEK						
		S	M	T	W	T	F	S
0								
1								
2								
3								
4								
5								
6								
7								
8								
9								
A								
B								
C								
D								
E								
F								

KEYSTROKES: 7+EVENT #

### FUNCTION BY PHASE

FUNCTION	PHASE							
	1	2	3	4	5	6	7	8
0								
1								
2								
3								
4								
5								
6								
7								
8								
9								
A								
B								
C								
D								
E								
F								

KEYSTROKES: D-F-EVENT #

### TIME OF DAY LOCAL FUNCTION CODES

0 - PERMIT	7 - RED REST	E - 1 - LOCAL OVERRIDE	F - TOO OUTPUTS
1 - RED LOCK	8 - DBL ENTRY	2 - PHASE BANK 2	1 - TOO OUT 1
2 - YELLOW LOCK	9 - VEH MAX ACL	3 - PHASE BANK 3	2 - TOO OUT 2
3 - VEH RECALL	A - SOFT RECALL	7 - DET COUNT	3 - TOO OUT 3
4 - PED RECALL	B - MAX 11 EXT	8 - SPLIT MONITOR	4 - TOO OUT 4
5 - RESERVED	C - COND SERVE		
6 - REST IN UNLK	D - TOO LAG D		

### CURRENT TIME AND DATE

- B-0 HOUR, MINUTE, DAY OF WEEK
- B-1 DAY OF MONTH, YEAR, MONTH
- B-F SECONDS

## CONFIGURATION DATA

NOTE "E" KEY ENABLED (F-9-E = 0)

### KEYSTROKES: E+E+INTERVAL

INTERVAL	PHASE							
	1	2	3	4	5	6	7	8
0 EXCLU PH								
1 RR1 GRN CL								
2 RR2 GRN CL								
3 RR2 LTD								
4								
5 OLA GOMIT			X					
6 OLB GOMIT								
7 OLC GOMIT								
8 OLD GOMIT								
9 OU FL VEL								
A EMVEH A			X					
B EMVEH B								
C EMVEH C			X			X		
D EMVEH D								
E EXTRA			X	X	X			
F IC SELECT			X					

### EXTRA (E+E+E)

- 1-TBC TYPE 1
- 2-NEMA COOD
- 3-DAYLIGHT SAV
- 4-EV ADVANCE
- 6-SPECIAL EVENT
- 7-PRETIMED
- 8-SPLIT RING

### ICSEL (E+E+E)

- 1- SIMPLEX IN
- 2-2-WAY MODEM
- 3-7 WIRE IN
- 4-FLH/FREE
- 5- SIMPLEX OUT
- 7-7 WIRE OUT

### ASSIGNS (E+F+F)

- 1-ART OVERLAP
- 2-TOO OUTPUTS
- 3-STEADY EV BEAC
- 4-FLASH EV BEAC

### KEYSTROKES: E-F+INTERVAL

INTERVAL	PHASE							
	1	2	3	4	5	6	7	8
0								
1 RR OLAP A								
2 RR OLAP B								
3 RR OLAP C								
4 RR OLAP D								
5 PED2P				X				
6 PED6P								
7 PED4P								
8 PED8P								X
9 FLH YELD								
A OVERLAP A				X				X
B OVERLAP B								
C OVERLAP C								
D OVERLAP D								
E RESTRICT								
F ASSIGNS			X					

**RSECTION: AERO DR & W. CANYON AV**

Group Assignment: None  
Field Master Assignment: None

N/S Street Name: WEST CANYON  
E/W Street Name: AERO

223 Pr  
Drawing Number: 24689

Last Change: EFF  
Timing Sheet By: MIM  
Approved By: MIM  
Timing implemented on: 3/15/01

Column #	Phase 1	Phase 2	Phase 3	Phase 4	Phase 5	Phase 6	Phase 7	Phase 8
Row 0	→	→	→	→	→	→	→	→
Row 1	7	21	10	4	10	3.8	2.0	2.0
Row 2	4	10	3.8	2.0	2.0	2.0	2.0	2.0
Row 3	30	60	0.2	30	60	0.1	0.8	5.3
Row 4	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
Row 5	3.8	3.8	0.2	30	60	0.1	0.8	5.3
Row 6	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Row 7	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Row 8	30	60	0.2	30	60	0.1	0.8	5.3
Row 9	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
Row A	3.8	3.8	0.2	30	60	0.1	0.8	5.3
Row B	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Row C	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Row D	30	60	0.2	30	60	0.1	0.8	5.3
Row E	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
Row F	3.8	3.8	0.2	30	60	0.1	0.8	5.3

Row	Phase Functions	F + E + Row	Preempt Timing	F + F + Row
0	Permit	12_56_8		
1	Red Lock			
2	Yellow Lock			
3	Min Recall	2_6_		
4	Ped Recall			
5	Peds (View)	2_8_		
6	Rest In Walk			
7	Red Rest			
8	Dbl Entry			
9	Max Recall			
A	Soft Recall			
B	Max 2			
C	Cond Serv			
D	Ped Lock	12345678		
E	Yellow Start	2_6_		
F	1st Phases	8		

Row	Manual Plan	Manual Offset	Manual Selection
0	0	C + A + 1	Manual Offset 0 = Automatic
1	0	C + B + 1	1 = Offset A
2			2 = Offset B
3			3 = Offset C

Row	Manual Plan	Manual Offset	Manual Selection
0	0	C + A + 1	Manual Offset 0 = Automatic
1	0	C + B + 1	1 = Offset A
2			2 = Offset B
3			3 = Offset C

Row	Manual Plan	Manual Offset	Manual Selection
0	0	C + A + 1	Manual Offset 0 = Automatic
1	0	C + B + 1	1 = Offset A
2			2 = Offset B
3			3 = Offset C

Disable Ports 234  
Disable Communications Ports  
D + D + 9

71  
①

INTERSECTION: AERO DR & W. CANYON AV

Row	Function	Day of Week	Column F Phases/Bits
0			
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		
7	Ped 4P		
8	Pod 8P		8
9	Yellow Flash Phases		
A	Overlap A - Phases		
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		

**I.O.D. Functions**  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

**Day of Week**  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

**Time and Date**  
 8-0 Hour, Minute, Day-of-Week  
 0-1 Day-of-Month, Year, Month  
 8-F Seconds

**Program Information**  
 C + C + 0 = program  
 C + C + F = version

**Remote Download**  
 C + 0 + 4 = 1-255  
 w/E + E + E bit 5 on

**Assign 5 Outputs**  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

**Dial-Up Telephone Communications**  
 (If set to a non-zero value, parity will be disabled)  
 (This parameter is NOT downloaded)

**Disable Parity** 0 D+B+0

**Configuration**  
 E + F + ROW  
 <E Page>

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**Day of Week**  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

**Time and Date**  
 8-0 Hour, Minute, Day-of-Week  
 0-1 Day-of-Month, Year, Month  
 8-F Seconds

**Program Information**  
 C + C + 0 = program  
 C + C + F = version

**Remote Download**  
 C + 0 + 4 = 1-255  
 w/E + E + E bit 5 on

**Assign 5 Outputs**  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

**Dial-Up Telephone Communications**  
 (If set to a non-zero value, parity will be disabled)  
 (This parameter is NOT downloaded)

**Disable Parity** 0 D+B+0

**Configuration**  
 E + F + ROW  
 <D Page>

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

**Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

**Day of Week**  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

**Time and Date**  
 8-0 Hour, Minute, Day-of-Week  
 0-1 Day-of-Month, Year, Month  
 8-F Seconds

**Program Information**  
 C + C + 0 = program  
 C + C + F = version

**Remote Download**  
 C + 0 + 4 = 1-255  
 w/E + E + E bit 5 on

**Assign 5 Outputs**  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

**Dial-Up Telephone Communications**  
 (If set to a non-zero value, parity will be disabled)  
 (This parameter is NOT downloaded)

**Disable Parity** 0 D+B+0

**Configuration**  
 E + F + ROW  
 <D Page>

**IC Select Flags**  
 1 = Modern  
 2 = 7-Wire Slave  
 3 = Flash / Free  
 4 = Simplex Master  
 5 = 7-Wire Master  
 6 = Offset Interrupter

**IC Select (Interconnect)**

**Configuration**  
 E + E + ROW

For access, set F + 9 + E = 1

Row	Delay	Carry-over	Detector Name	332 Input File	Detector Number
0					
1				111	14
2		1.8		212U	1
3				212L	5
4				213U	21
5				213L	25
6				214	9
7				315	16
8				416U	3
9				416L	7
A				417U	23
B				417L	27
C				418	11
D				119U	18
E	---	---	---	319L	20
F	---	---	---	---	---

Detector Numbers	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

Active Detectors <D Page>

Detector #	0	1	2	3	4	5	6	7	8
System Det. # 1									
System Det. # 2									
System Det. # 3									
System Det. # 4									
System Det. # 5									
System Det. # 6									
System Det. # 7									
System Det. # 8									

System Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

(These parameters are NOT downloaded.)

Row	Delay	Carry-over	Detector Name	332 Input File	Detector Number
0					
1		1.8		5J1	13
2				6J2U	2
3				6J2L	6
4				6J3U	22
5				6J3L	26
6				6J4	10
7				7J5	16
8				8J6U	4
9				8J6L	8
A				8J7U	24
B	10.0			8J7L	28
C				8J8	12
D				5J9U	17
E	---	---	---	7J9L	19
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW



**INTERSECTION: AERO DR & DALEY CTR DR/RUFFIN RD**

Group Assignment: None  
Field Master Assignment: None

N/S Street Name: DALEY CTR DR/RUFFIN RD  
E/W Street Name: AERO DR

223 P1  
Drawing Number: D-24495-16

Last Change: EFF  
Timing Sheet By: *MMZ*  
Approved By: *MMZ*  
Timing Implemented on: 3/15/01

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Phase	AERO DR															
Ped Walk	AERO DR															
Ped FDW	AERO DR															
Min Green	AERO DR															
Type 3 Limit	AERO DR															
Add/Veh	AERO DR															
Veh Extn	AERO DR															
Max Gap	AERO DR															
Min Gap	AERO DR															
Max Limit	AERO DR															
Max Limit 2	AERO DR															
Bus Adv	AERO DR															
Call to Phs	AERO DR															
Reduce By	AERO DR															
Every	AERO DR															
Yellow	AERO DR															
Red Clear	AERO DR															

Phase Timing - Bank 1  
F + Phaso + Row

Max Initial	0
Red Revort	5.0
All Red Start	0.0
Start / Revort Times	
Drup Number	
Zone Number	
Area Number	
Area Address	
QuickNet Channel	

Communication Addresses  
C + F + O  
Free Lag  
Lag Phases

Overlap A	0.0
Overlap B	0.0
Overlap C	0.0
Overlap D	0.0

Overlap Timing  
F + COLOR +  
D + 0 + OVERLAP

Green Clear	0.0
Yellow Change	0.0
Red Clear	0.0
Clear	0.0
Change	0.0
Flash	0.0

Downtime Flash  
Downtime Before Auto Manual Flash  
60 (minutes)

Manual Plan	0
Manual Offset	0
Manual Selection	
Manual Plan	
0 = Automatic	
1 = Plan 1-9	
14 = Free	
15 = Flash	

Disable Ports  
Disable Communications Ports  
D + D + 9

RR-1 Delay	0
RR-1 Clear	0
EV-A Delay	3
EV-A Clear	0
EV-B Delay	3
EV-B Clear	0
EV-C Delay	3
EV-C Clear	0
EV-D Delay	3
EV-D Clear	0
RR-2 Delay	3
RR-2 Clear	0
View EV Delay	...
View EV Clear	...
View RR Delay	...
View RR Clear	...

Preempt Timing  
F + E + Row

Manual Plan	0
Manual Offset	0
Manual Selection	
Manual Plan	
0 = Automatic	
1 = Plan 1-9	
14 = Free	
15 = Flash	

Manual Offset  
0 = Automatic  
1 = Offset A  
2 = Offset B  
3 = Offset C

72 (2)





# 223 Program

INT. SECTION: AERO DR & DALEY CTN DIM/RUFFIN RD

Coordination Timing By: EFF  
 Implemented On: 05/01/01

**FOR OBSERVATION ONLY**

- Master Plan C+A+2
- Current Plan C+A+3
- Next Plan C+A+4
- Y/D, Plan C+A+8
- Master Cycle C+A+0
- Ring A Cycle C+B+0
- Ring B Cycle C+D+0
- Min Cycle C+A+E
- Max Cycle C+B+E

Column #	1	2	3	4	5	6	7	8	9
Plan Name									
Cycle Length	110		130						
Phase 1 - ForceOff	74		89						
Phase 2 - ForceOff									
Phase 3 - ForceOff									
Phase 4 - ForceOff									
Phase 5 - ForceOff	61		85						
Phase 6 - ForceOff									
Phase 7 - ForceOff	22		49						
Phase 8 - ForceOff	40		69						
Ring Offset									
Offset A	8		104						
Offset B									
Offset C									
Permissive	10		10						
Hold Release	255		255						
Pad Shift	12		0						

Coordination C + Plan + ROW

<C Page>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Free Lag																
Plan 1 - Lag																
Plan 2 - Lag																
Plan 3 - Lag																
Plan 4 - Lag																
Plan 5 - Lag																
Plan 6 - Lag																
Plan 7 - Lag																
Plan 8 - Lag																
Plan 9 - Lag																
Coord Max																
Coord Lag																

Lag Phases C + F + FUNCTION #

<C Page>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Time	06:45															
Day of Week	23456															
Offset	A															
Plan	1															
Day of Week	23456															
Offset	A															
Plan	3															
Day of Week	1234567															
Offset	A															

TOD Coordination <9 Key with C+0+9=1>

Plan Subset  
 1 thru 9 = Coordination  
 Plan 1 thru 9  
 14 or E = Free  
 15 or F = Flash

Transition Type  
 TBC Transition  
 C+D+D

Transition Type  
 0 = Shortway  
 Non-zero = Lengthen

# INTERSECTION: AERO DR & MURPHY CANYON RD

Group Assignment:  
Field Master Assignment:

Name: MURPHY CANYON RD  
EW Name: AERO DR

223 gram

Last Database Change: N/A  
System Ref. Number:  
Drawing Number: D-24706-17  
Timing Implemented On: 03/17/01

Timing Sheet By: EFF  
Approved By: MM

Row	Phase #	1	2	3	4	5	6	7	8
0									
1	Ped Walk	7							
2	Ped FDW	21							
3	Min Green	4	10	4					
4	Type 3 Limit								
5	Add/Veh								
6	Veh Extn	2.0	3.8	2.0					
7	Max Gap	2.0	3.8	2.0					
8	Min Gap	2.0	0.2	2.0					
9	Max Limit	50	60	30					
A	Max Limit 2								
B	Bus Adv								
C	Call to Phs			8					
D	Reduce By	0.1	0.8						
E	Every	3.6	6.0	3.0					
F	Red Clear	1.5	1.5	1.0					

Phase Timing - Bank 1  
F + Phase + Row

Max Initial	0
Red Revert	5.0
All Red Start	0.0

Drop Number	C + 0 + 0
Zone Number	C + 0 + 1
Area Number	C + 0 + 2
Area Address	C + 0 + 3
QuicNet Channel	(QuicNet)

Communication Addresses	
C + F + O	F
Free Lag	2_4_6_8

Lag Phases  
Row

Overlap A	
Overlap B	
Overlap C	
Overlap D	

Overlap Timing  
F + COLOR +

Green Clear	
Yellow Change	
Red Clear	
Load-Switch #	0

<D Page>  
D + 0 + OVERLAP

Downtime Flash	255
Downtime Before Auto Manual Flash	

(minutes)  
F + 0 + 8

RR-1 Delay	
RR-1 Clear	
EV-A Delay	
EV-A Clear	
EV-B Delay	
EV-B Clear	
EV-C Delay	
EV-C Clear	
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	
View EV Clear	
View RR Delay	
View RR Clear	

Preempt Timing  
F + E + Row

Manual Plan	0
Manual Offset	0

Manual Selection  
Manual Plan  
0 = Automatic  
1 = Plan 1-9  
14 = Free  
15 = Flash

Disable Ports	234
Disable Communication Ports	

D + D + 9

Phase Functions  
F + F + Row

Permit	123_5678
Red Lock	
Yellow Lock	
Min Recall	2_6_
Ped Recall	
Peds (View)	2_67_
Rest In Walk	
Red Rest	
Dbl Entry	
Max Recall	
Soft Recall	3_8_
Max 2	
Cond Serv	
Ped Lock	12345678
Yellow Start	2_6_
1st Phases	3_8_

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3

NOTE: PHASE 3 IS A DUMMY PHASE; PHASE 7 IS EXCLUSIVE; PHOTO ENFORCED INTERSECTION

# INTERSECTION: AERO DR & MURPHY CANYON RD

Row	Time	Function	Day of Week	Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function  
7 + ROW

<D Page>  
D + F + ROW

- T.O.D. Functions  
 0 = Permitted Phases  
 1 = Red Lock  
 2 = Yellow Lock  
 3 = Veh Min Recall  
 4 = Ped Recall  
 5 =  
 6 = Rest In Walk  
 7 = Red Rest  
 8 = Double Entry  
 9 = Veh Max Recall  
 A = Veh Soft Recall  
 B = Maximum 2  
 C = Conditional Service  
 D = Free Lag Phases  
 E = Bit 1 - Local Override  
 Bit 2 - Phase Bank 2  
 Bit 3 - Phase Bank 3  
 Bit 4 - Disable Detector  
 OFF Monitor  
 Bit 7 - Detector Count Monitor  
 Bit 8 - Real Time Split Monitor  
 F = Output Bits 1 thru 4

Row	Function	Day of Week	Phases/Bits
0			
1	RR Overlap A - Phases		
2	RR Overlap B - Phases		
3	RR Overlap C - Phases		
4	RR Overlap D - Phases		
5	Ped 2P		2
6	Ped 6P		6
7	Ped 4P		
8	Ped 8P		7
9	Yellow Flash Phases		
A	Overlap A - Phases		1 8
B	Overlap B - Phases		
C	Overlap C - Phases		
D	Overlap D - Phases		
E	Restricted Phases		
F	Assign 5 Outputs		1

Configuration  
E + F + ROW

<E Page>

Row	Function	Day of Week	Phases/Bits
0	Exclusive Phases		7
1	RR-1 Clear Phases		
2	RR-2 Clear Phases		
3	RR-2 Limited Service		
4	Prot / Perm Phases		
5	Overlap A - Green Omnit		
6	Overlap B - Green Omnit		
7	Overlap C - Green Omnit		
8	Overlap D - Green Omnit		
9	Overlap Yellow Flash		
A	EV-A Phases		2 5
B	EV-B Phases		7
C	EV-C Phases		1 6
D	EV-D Phases		8
E	Extra 1 Config. Bits		1 345
F	IC Select (Interconnect)		

For access, set F + 9 + E = 1

Configuration  
E + E + ROW

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Remote Download  
 6 = Special Event  
 7 = Prelimed Operation  
 8 = Split Ring Operation

- IC Select Flags  
 1 =  
 2 = Modern  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

- Day of Week  
 1 = Sunday  
 2 = Monday  
 3 = Tuesday  
 4 = Wednesday  
 5 = Thursday  
 6 = Friday  
 7 = Saturday

- Time and Date  
 8-0 Hour, Minute, Day-of-Week  
 8-1 Day-of-Month, Year, Month  
 8-F Seconds

- Program Information  
 C + C + 0 = program  
 C + C + F = version

- Assign 5 Outputs  
 1 = Right Turn Overlap  
 2 = TOD Outputs  
 3 = EV Beacon - Steady  
 4 = EV Beacon - Flashing  
 5 = Special Event Outputs  
 6 = Phase 3 & 7 Ped  
 7 = Advanced Warning Sign  
 8 =

Disable Parity  0  D+B+0

Dial-Up Telephone Communications  
 (If set to a non-zero value, parity will be disabled)

- Remote Download  
 C + 0 + 4 = 1 -255  
 w/ E + E + E bit 5 on

Row	1	3	Carry-over
0			
1			1.8
2			1.5
3			
4			
5			
6			
7			
8			
9			
A			
B			
C			
D			
E			
F			

Detector Name	332 Input File	Detector Number
	11I	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2	4	Carry-over
0			
1			1.8
2	10.0		
3			
4			
5			
6			1.8
7			1.8
8			3.7
9	10.0		
A			
B			
C			
D			
E			
F			

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Flow	A	B	C	D	E	F
1						
2						
3						
4						
5						
6						
7						
8						

Detector Numbers	1	2	3	4	5	6	7	8	E
9	10	11	12	--	--	--	--	--	12345678
13	14	15	16	17	18	19	20		1234
--	--	--	--	21	22	23	24		12345678
--	--	--	--	--	--	--	--		1234
--	25	26	27	28	--	--	--		2345

Active Detectors <D Page>

Row	0	1	2	3	4	5	6	7	8
0									
1									
2									
3									
4									
5									
6									
7									
8									

System Det. #	1	2	3	4	5	6	7	8	Detector #
System Det. # 1									0
System Det. # 2									0
System Det. # 3									0
System Det. # 4									0
System Det. # 5									0
System Det. # 6									0
System Det. # 7									0
System Det. # 8									0

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+O+6
Short Failure	0.5	F+O+7

Power Cycle Correction (Default = 0.5)  
(These parameters are NOT downloaded)

223 Program

INTERSECTION: AERO DR & MURPHY CANYON RD

Coordination Timing By: EFF  
Implemented On: 05/01/01

FOR OBSERVATION ONLY

- Master Plan C + A + 2
- Current Plan C + A + 3
- Next Plan C + A + 4
- T.O.D. Plan C + A + 5
- Master Cycle C + A + 0
- Ring A Cycle C + B + 0
- Ring B Cycle C + D + 0
- Min Cycle C + A + E
- Max Cycle C + B + E

Flow	Column #	1	2	3	4	5	6	7	8	9
0	Plan Name									
1	Cycle Length	110		130						
2	Phase 1 - ForceOff	80	74	87						
3	Phase 2 - ForceOff	41	38	47						
4	Phase 3 - ForceOff	57	57	73						
5	Phase 4 - ForceOff									
6	Phase 5 - ForceOff									
7	Phase 6 - ForceOff	20		29						
8	Phase 7 - ForceOff	41	38	47						
9	Phase 8 - ForceOff									
A	Ring Offset	105		92						
B	Offset A									
C	Offset B									
D	Offset C									
E	Permissibe	10		10						
F	Hold Release	255		255						
	Ped Shift	20		14						

Coordination C + Plan + ROW

Row	Time	Plan	Offset	Day of Week
0	06:45	1	A	23456
1	12:00	E	A	23456
2	15:00	3	A	23456
3	17:45	E	A	1234567
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination

<9 Key with C+0-9=1>

Plan Select  
1 thru 9 = Coordination  
Plan 1 thru 9  
14 or E = Free  
15 or F = Flash

Sync Phases  
C + E + FUNCTION #

Lag Phases  
C + F + FUNCTION #

Transition Type  
TBC Transition  
C + D + D

Transition Type  
0 = Shortway  
Non-zero = Lengthen

NOTE: ALL PLANS IN COORDINATION WITH CALTRANS I-15 OFF-RAMP SIGNAL.  
SIMPLEX MASTER LOCATED AT AERO DR & MURPHY CANYON RD

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DATE: 5/19/2014

INTERVAL	PHASE TIMING								PRE-EMPTION	E	F									
	1	2	3	4	5	6	7	8			1	2	3	4	5	6	7	8		
0 WALK	1	1	1	1	1	7	1	1	9	CLK RST	0	PERMIT	1	2	3	4	5	6	7	8
1 DONT WALK	1	1	1	1	1	15	1	1		RR1 CLR	5	RED LOCK								1
2 MIN GREEN	5	15	1	5	11	1	1	1		EVA DLY	0	YEL LOCK								2
3 TYPE 3 DET	0	0	0	0	0	0	0	0		EVA CLR	5	V RECALL	2							3
4 ADD/VEH	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		EVB DLY	0	P RECALL								4
5 PASSAGE	3.0	2.0	0.9	2.0	2.0	2.0	0.9	0.9		EVB CLR	5	PED PHASES								5
6 MAX GAP	3.0	2.0	0.9	2.0	2.0	2.0	0.9	0.9		EVC DLY	0	RT OLA								6
7 MIN GAP	3.0	2.0	0.9	2.0	2.0	2.0	0.9	0.9		EVC CLR	5	RT OLB								7
8 MAX EXT	40	45	9	35	25	45	9	9		EVD DLY	0	DBL ENTRY								8
9 MAX 2		35		45					YR	EVD CLR	5	MAX 2 PHASES	2	4	6					9
A MAX 3		95							MO	MAX EV	255	LAG PHASES	READ ONLY							
B									DAY	RR2 CLR	5	RED REST								B
C REDUCE BY	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	DOW			REST-IN-WALK								C
D EVERY	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	HR			MAX 3 PHASES	2							D
E YELLOW	3.2	4.5	3.0	3.6	3.2	4.5	3.0	3.0	MIN			YEL START UP	2					6		E
F RED	1.0	2.0	1.0	1.0	1.0	2.0	1.0	1.0	SEC			FIRST PHASE					4			F
PED XING FT								53												
BIKE XING FT		113						57												

ENTRIES IN THESE LOCATIONS ARE NOT TO BE CHANGED

ENTRIES IN THESE LOCATIONS CAN BE CHANGED IN CCL FLASH ONLY

FOC LONG FAILURE	
FOD SHORT FAILURE	
FOE	0
FOF	5

FCO	3
FC1	3
FC2	10
FCA	0.0
FCB	0.0
FCC	0.0
FCD	0.0

FDO TB SELECT	1
FD3 PED SELECT	0
FD4 7 WIRE	0
FD5 PERMISSIVE	0
FD8 OS SEEKING	1

CO5 FLASH TYPE	1
CC2 DOWNLOAD	1



D	FLAGS								E	FLAGS								F	FLAGS							
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
MAX									MIN									PED								
RCL									RCL									RCL								
CP 1									CP 1			4						CP 1								
CP 2									CP 2			4						CP 2								
CP 3									CP 3			4						CP 3								
CP 4									CP 4			4						CP 4								
CP 5									CP 5									CP 5								
CP 6									CP 6									CP 6								
CP 7									CP 7									CP 7								
CP 8									CP 8									CP 8								
CP 9									CP 9									CP 9								
A																		RCL 1								
B																		RCL 2								
C																										
D																										
E																										
F																										

LAST POWER FAILURE REGISTER

HOUR = D-A-E  
 MINUTE = D-B-E  
 DAY = D-C-E

RCL 1 = TIME OF DAY MAX RECALL (1ST SELECT) PHASES  
 (CALL ACTIVE LIGHTS)  
 RCL 2 = TIME OF DAY MAX RECALL (2ND SELECT) PHASES  
 (CALL ACTIVE LIGHTS)

LAST FLASH TIME REGISTER

HOUR = D-A-F  
 MINUTE = D-B-F  
 DAY = D-C-F

D-E-E = C8 VERSION NUMBER  
 D-E-F = LITHIUM BATTERY CONDITION  
 84 = BAD  
 85 = GOOD

E	FUNCTION								F	FLAGS								FUNCTION	FLAGS							
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
0										CODE 4																
1										CODE 5																
2										C-RECALL																
3										D-RECALL																
4										EXCLUSIVE																
5										2 PED																
6										6 PED																
7										4 PED																
8										8 PED																
9																										
A										OLA NOT																
B										OLB NOT																
C										OLC NOT																
D										OLD NOT																
E																										
F																										

TIME OF DAY ACTIVITY TABLE

7+EVENT+HR+MIN+ACT+"E"+ON/OFF+DOW LTS		HR	MIN	ACT	OFF	ON	S	M	T	W	T	F	S
0	05	30	2	X									
1	09	00	2										
2	14	00	3	X									
3	19	00	3										
4													
5													
6													
7													
8													
9													
A													
B													
C													
D													
E													
F													

CONTROL PLAN TIME OF DAY

9+EVENT+HR+MIN+CP+OS+E+DOW		HR	MIN	CP	OS	S	M	T	W	T	F	S
0	06	45	2	A								
1	12	00	E	A								
2	15	00	3	A								
3	17	45	E	A								
4												
5												
6												
7												
8												
9												
A												
B												
C												
D												
E												
F												

CONTROL PLAN TIME OF DAY

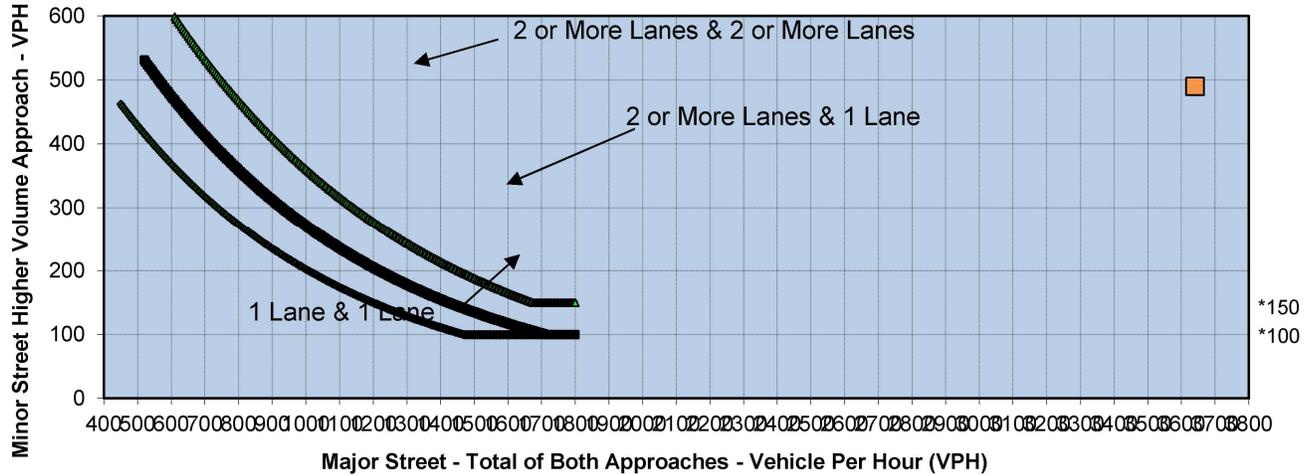
9+EVENT+HR+MIN+CP+OS+E+DOW		HR	MIN	CP	OS	S	M	T	W	T	F	S
0												
1												
2												
3												
4												
5												
6												
7												
8												
9												
A												
B												
C												
D												
E												
F												

ACTIVITY CODE

- 1 TYPE OF MAX TERMINATION
- 2 MAX 2
- 3 MAX 3
- 4 COND SERV (1ST SELECT)
- 5 COND SERV (2ND SELECT)
- 6 ENERGIZE AUX OUTPUT-RED
- 7 ENERGIZE AUX OUTPUT-GREEN
- 8 ENERGIZE AUX OUTPUT-YELLOW
- 9 TIME OF DAY MAX RECALL (1ST SELECT)
- A TRAFFIC ACT. MAX 2 OPERATION
- B TIME OF DAY MAX RECALL (2ND SELECT)
- C YELLOW YIELD COORDINATION
- D YELLOW YIELD COORDINATION
- E TIME OF DAY FREE OPERATION
- F FLASHING OPERATION



**Figure 4C-3  
Warrant 3, Peak Hour**



\* Note: 150 VPH APPLIES AS THE LOWER THRESHOLD VOLUME FOR A MINOR STREET APPROACH WITH TWO OR MORE LANES AND 100 VPH APPLIES AS THE LOWER THRESHOLD VOLUME FOR A MINOR STREET APPROACHING WITH ONE LANE.  
Source: *California Manual on Uniform Traffic Control Devices*, Caltrans, 2006

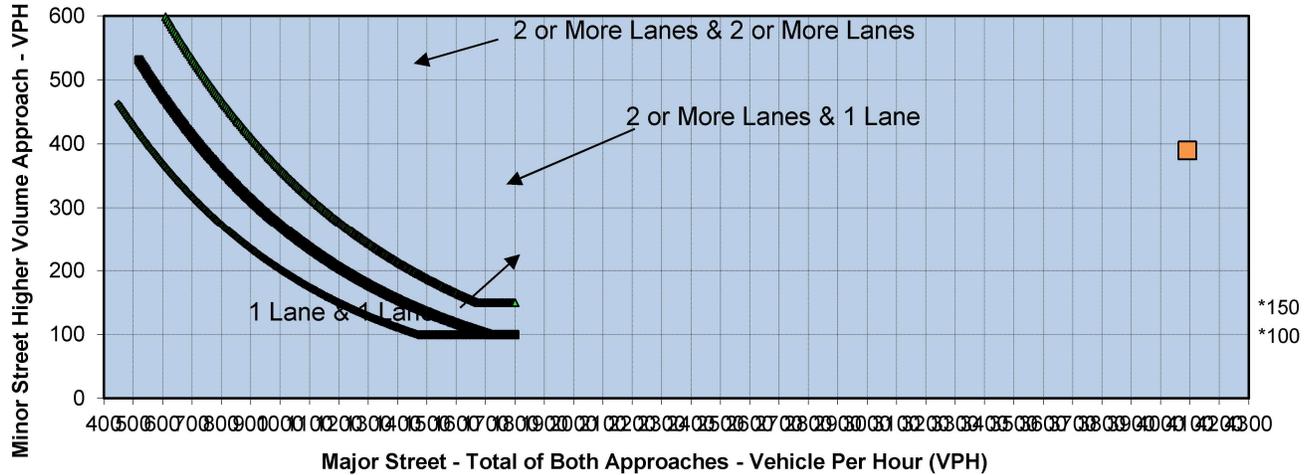
Major Street	<b>Balboa Avenue</b>	Project	<b>Kearny Mesa Proposed Plan</b>
Minor Street	<b>Ruffner Street</b>	Scenario	<b>Buildout</b>
		Peak Hour	<b>AM</b>

	NB	SB	EB	WB
Left	90	60	290	80
Through	140	140	1,660	1,250
Right	90	290	140	220
Total	320	490	2,090	1,550

	North/South
X	East/West

	Major Street	Minor Street	<u>Warrant Met</u>
	Balboa Avenue	Ruffner Street	
Number of Approach Lanes	<b>2</b>	<b>1</b>	<u>YES</u>
Traffic Volume (VPH) *	<b>3,640</b>	<b>490</b>	

**Figure 4C-3  
Warrant 3, Peak Hour**



\* Note: 150 VPH APPLIES AS THE LOWER THRESHOLD VOLUME FOR A MINOR STREET APPROACH WITH TWO OR MORE LANES AND 100 VPH APPLIES AS THE LOWER THRESHOLD VOLUME FOR A MINOR STREET APPROACHING WITH ONE LANE.  
Source: *California Manual on Uniform Traffic Control Devices*, Caltrans, 2006

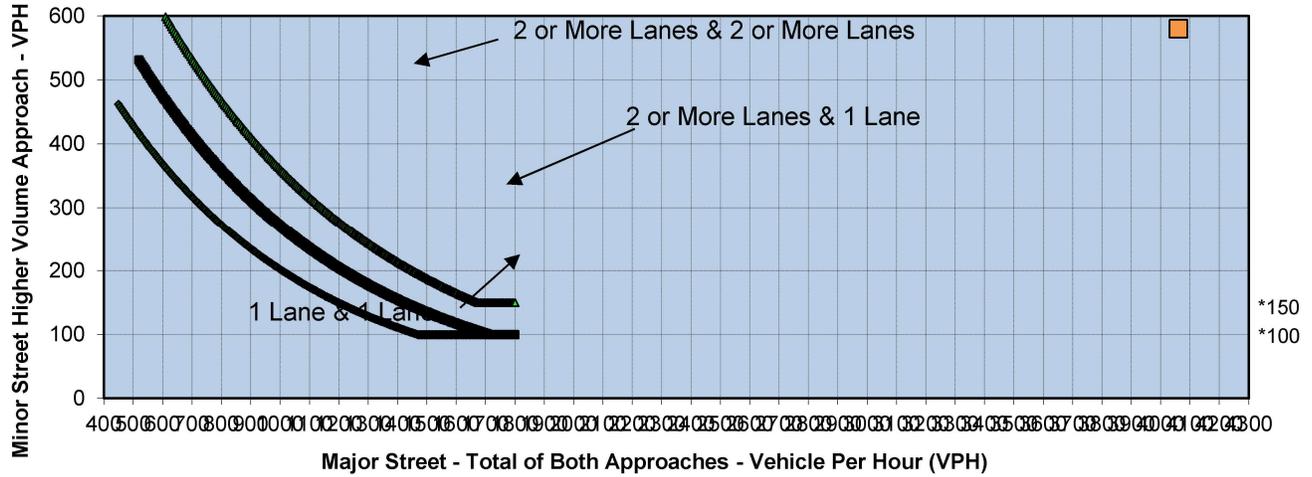
Major Street	Balboa Avenue	Project	Kearny Mesa Proposed Plan
Minor Street	Ruffner Street	Scenario	Buildout
		Peak Hour	MIDDAY

	NB	SB	EB	WB
Left	60	40	250	140
Through	40	60	1,830	1,580
Right	90	290	150	140
Total	190	390	2,230	1,860

	North/South
X	East/West

	Major Street	Minor Street	<u>Warrant Met</u>
	Balboa Avenue	Ruffner Street	
Number of Approach Lanes	2	1	YES
Traffic Volume (VPH) *	4,090	390	

**Figure 4C-3  
Warrant 3, Peak Hour**



\* Note: 150 VPH APPLIES AS THE LOWER THRESHOLD VOLUME FOR A MINOR STREET APPROACH WITH TWO OR MORE LANES AND 100 VPH APPLIES AS THE LOWER THRESHOLD VOLUME FOR A MINOR STREET APPROACHING WITH ONE LANE.  
Source: California Manual on Uniform Traffic Control Devices, Caltrans, 2006

Major Street	Balboa Avenue	Project	Kearny Mesa Proposed Plan
Minor Street	Ruffner Street	Scenario	Buildout
		Peak Hour	PM

	NB	SB	EB	WB
Left	120	130	240	210
Through	60	40	1,580	1,690
Right	120	410	250	90
Total	300	580	2,070	1,990

	North/South
X	East/West

	Major Street	Minor Street	Warrant Met
	Balboa Avenue	Ruffner Street	
Number of Approach Lanes	2	1	YES
Traffic Volume (VPH) *	4,060	580	