TERSECTION: KURTZ ST & ROSECRANS ST

223 P ram

Group Assignment:
Field Master Assignment:

N/S Street Name: ROSECRANS ST
E/W Street Name: KURTZ ST

Last Database Change:
System Ref. Number:

20

N

Row	Column 4 ---> / Phase 8 --->	1	2	3	4	5	6	7	8
		PPLT	ROSECRANS	KURTZ	KURTZ		ROSECRANS		
0	Ped Walk		7		5		7		
1	Ped FDW		8		16		8		
2	Min Green	4	10	4	4		10		
3	Type 3 Limit								
4	Add/Veh		1.6				1.6		
5	Veh Extn	2.0	6.7	3.0	3.0		6.7		
6	Max Gap	2.0	6.7	3.0	3.0		6.7		
7	Min Gap	2.0	2.0	3.0	3.0		2.0		
8	Max Limit	10	40	30	30		40		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs	4							
C	Reduce By		0.1				0.1		
D	Every		0.6				0.6		
E	Yellow	3.4	3.9	3.4	3.9		3.9		
F	Red Clear	1.0	1.0	1.0	1.0		1.0		

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

Row		F
0	Permit	1234_6__
1	Red Lock	
2	Yellow Lock	
3	Min Recall	_2___6__
4	Ped Recall	
5	Peds (View)	_2_4_6__
6	Rest In Walk	
7	Red Rest	
8	Dbl Entry	
9	Max Recall	
A	Soft Recall	
B	Max 2	
C	Cond Serv	
D	Ped Lock	12345678
E	Yellow Start	_2___6__
F	1st Phases	__3_____

Phase Functions <F Page>
F + F + Row

Max Initial	28	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	16	C + 0 + 0
Zone Number	16	C + 0 + 1
Area Number	1	C + 0 + 2
Area Address	94	C + 0 + 3
QuicNet Channel	DIGI72:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	_2_3_6__	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page> <D Page>
F + COLOR + D + 0 + OVERLAP

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 +8

Disable Ports	
Disable Comm Ports	_234____

D + D + 9

Timing Sheet By: EFF
Approved By: EFF
Drawing Number: 32226-4-D
Timing Implemented On: 10/4/2005

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1				2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9				4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1				6J2U	2
2				6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 – – – –	1234___
C	13 14 15 16 17 18 19 20	12345678
D	– – – – 21 22 23 24	___5678
E	– – – – – – – –	1234___
F	– 25 26 27 28 – – –	_2345___

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: ROSECRANS ST @ KURTZ ST

223 Program

		Plan								
	Column # --->	1	2	3	4	5	6	7	8	9
Row	Plan Name --->		MID		WKND		AM	PM		
0	Cycle Length		140		140		130	160		
1	Phase 1 - ForceOff		88		86		86	93		
2	Phase 2 - ForceOff		0		0		0	0		
3	Phase 3 - ForceOff		73		71		70	77		
4	Phase 4 - ForceOff		38		36		37	43		
5	Phase 5 - ForceOff									
6	Phase 6 - ForceOff		0		0		0	0		
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A		60		72		42	150		
B	Offset B							124		
C	Offset C									
D	Permissive		12		15		13	16		
E	Hold Release		255		255		255	255		
F	Ped Shift		0		0		0	0		

Coordination <C Page>

C + Plan + ROW

Coordination Timing By: M2S
Implemented On:

FOR OBSERVATION ONLY

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Row	Time	Plan	Offset	Day of Week
0	06 : 30	6	A	_23456_
1	09 : 00	E	A	_23456_
2	11 : 00	2	A	_23456_
3	13 : 00	7	A	_23456_
4	14 : 45	7	B	_23456_
5	16 : 45	7	A	_23456_
6	19 : 30	E	A	_23456_
7	10 : 00	4	A	1____7
8	18 : 00	E	A	1____7
9				
A				
B				
C				
D				
E				
F				

TOD Coordination

<9 Key with C+0+9=1>

	E	Row		F
		0	Free Lag	
Plan 1		1	Plan 1 - Lag	
Plan 2	_2__6__	2	Plan 2 - Lag	_23__6__
Plan 3		3	Plan 3 - Lag	
Plan 4	_2__6__	4	Plan 4 - Lag	_23__6__
Plan 5		5	Plan 5 - Lag	
Plan 6	_2__6__	6	Plan 6 - Lag	_23__6__
Plan 7	_2__6__	7	Plan 7 - Lag	_23__6__
Plan 8		8	Plan 8 - Lag	
Plan 9		9	Plan 9 - Lag	
Coord Ped*		A	Coord Max *	
NEMA Hold		B	Coord Lag *	
		C		
		D		
		E		
		F		

Sync Phases

C + E + FUNCTION #

Lag Phases <C Page>

C + F + FUNCTION #

Transition Type	0

TBC Transition
C + D + D

Plan Select:
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

Transition Type
0 = Shortway
Non-zero = Lengthen

INTERSECTION: KURTZ ST & ROSECRANS ST

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function <D Page>

7 + ROW D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration <E Page>

E + F + ROW

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	1
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2
B	EV-B Phases	4
C	EV-C Phases	1 6
D	EV-D Phases	3
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration

For access, set F + 9 + E = 1 E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications
(If set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

INTERSECTION: CAMINO DEL RIO W @ HANCOCK ST

223 Program

Group Assignment:
Field Master Assignment:

N/S Street Name: CAM DEL RIO W
E/W Street Name: HANCOCK ST

Last Database Change:
System Ref. Number:

N

JDS 12/9/09

Row	Column # —>	1	2	3	4	5	6	7	8
			CAM DEL RIO W		HANCOCK ST		CAM DEL RIO W		
	Phase # —>	1	2	3	4	5	6	7	8
0	Ped Walk		7		7.5		7		
1	Ped FDW		14		37		16		
2	Min Green		10		4	4	10		
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn		3.8		2.0	2.0	4.6		
6	Max Gap		3.8		2.0	2.0	4.6		
7	Min Gap		0.2		2.0	2.0	0.2		
8	Max Limit		60 ~~40~~		40 ~~35~~	30 ~~25~~	60 ~~40~~		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.8				0.7		
E	Yellow		3.9		3.9	3.4	3.9		
F	Red Clear		1.0		1.0	1.0	1.0		
	Grade		0.0%		0.0%		2.0%		

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	
EV-C Clear	
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

	F	Row
Permit	_2_456__	0
Red Lock		1
Yellow Lock		2
Min Recall	_2___6__	3
Ped Recall		4
Peds (View)	_2_4_6__	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	_2___6__	E
1st Phases	___4____	F

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	15	C + 0 + 0
Zone Number	15	C + 0 + 1
Area Number	1	C + 0 + 2
Area Address	92	C + 0 + 3
QuicNet Channel	DIGI72:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	_2_4_6	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	234

Disable Communication Ports
D + D + 9

Timing Sheet By: EFF/WXH
Approved By: EFF
Drawing Number:
Timing Implemented On: 2/10/2003

15

INTERSECTION: CAMINO DEL RIO W @ HANCOCK ST

223 Program

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1		1.8		2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9				4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1		1.8		6J2U	2
2				6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	0
2	System Det. # 2	0
3	System Det. # 3	0
4	System Det. # 4	0
5	System Det. # 5	0
6	System Det. # 6	0
7	System Det. # 7	0
8	System Det. # 8	0

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: CAMINO DEL RIO W @ HANC[illegible]K ST

223 Pros[illegible]n

Row	Column # --->	Plan 1	2	3	4	5	6	7	8	9
	Plan Name --->		MID		WKND		AM	PM		
0	Cycle Length		140		140		130	160		
1	Phase 1 - ForceOff									
2	Phase 2 - ForceOff		0		0		0	0		
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff		65		65		61	40		
5	Phase 5 - ForceOff		25		25		21	71		
6	Phase 6 - ForceOff		0		0		0	0		
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A		131		116		126	94		
B	Offset B									
C	Offset C									
D	Permissive		12		15		13	16		
E	Hold Release		255		255		255	255		
F	Ped Shift		0		0		0	0		

Coordination <C Page>

C + Plan + ROW

Coordination Timing By: M2S
Implemented On:

FOR OBSERVATION ONLY

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Row	Time	Plan	Offset	Day of Week
0	06 : 00	6	A	_23456_
1	11 : 00	2	A	_23456_
2	13 : 00	7	A	_23456_
3	19 : 30	E	A	_23456_
4	10 : 00	4	A	1____7
5	18 : 00	E	A	1____7
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination

<9 Key with C+0+9=1>

	E	Row		F
		0	Free Lag	
Plan 1		1	Plan 1 - Lag	
Plan 2	_2__6__	2	Plan 2 - Lag	_2_45___
Plan 3		3	Plan 3 - Lag	
Plan 4	_2__6__	4	Plan 4 - Lag	_2_45___
Plan 5		5	Plan 5 - Lag	
Plan 6	_2__6__	6	Plan 6 - Lag	_2_45___
Plan 7	_2__6__	7	Plan 7 - Lag	_2_4_6__
Plan 8		8	Plan 8 - Lag	
Plan 9		9	Plan 9 - Lag	
Coord Ped*		A	Coord Max *	
NEMA Hold		B	Coord Lag *	
		C		
		D		
		E		
		F		

Sync Phases

C + E + FUNCTION #

Lag Phases <C Page>

C + F + FUNCTION #

Transition Type	0

TBC Transition
C + D + D

Transition Type
0 = Shortway
Non-zero = Lengthen

Plan Select
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

E + F + ROW

<E Page>

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	
C	EV-C Phases	
D	EV-D Phases	
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

For access, set F + 9 + E = 1

Configuration

E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+8+0

Dial-Up Telephone Communications

(if set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

INTERSECTION: HANCOCK ST & WASHINGTON ST

223 Program

27

Group Assignment:
Field Master Assignment:

N/S Street Name: HANCOCK
E/W Street Name: WASHINGTON ST

Last Database Change:
System Ref. Number:

Row	Phase 1	Phase 2	Phase 3	Phase 4	Phase 5	Phase 6	Phase 7	Phase 8
	WASHINGTON			HANCOCK		WASHINGTON		
0 Ped Walk		7		7		7		
1 Ped FDW		18		18		15		
2 Min Green	7	10		7		10		
3 Type 3 Limit								
4 Add/Veh								
5 Veh Extn	2.0	3.8		2.0		4.2		
6 Max Gap	2.0	3.8		2.0		4.2		
7 Min Gap	2.0	0.2		2.0		0.2		
8 Max Limit	30	60		40		60		
9 Max Limit 2								
A Bus Adv								
B Call to Phs								
C Reduce By		0.1				0.1		
D Every		0.8				0.8		
E Yellow	3.4	3.9		3.9		3.9		
F Red Clear	1.0	1.0		1.0		1.0		

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	0
RR-2 Clear	20
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

	F	Row
Permit	12_4_6__	0
Red Lock		1
Yellow Lock		2
Min Recall	_2___6__	3
Ped Recall		4
Peds (View)	_2_4_6__	5
Rest in Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	_2___6__	E
1st Phases	___4____	F

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	3	C + 0 + 0
Zone Number	3	C + 0 + 1
Area Number	2	C + 0 + 2
Area Address	141	C + 0 + 3
QuicNet Channel	DIGI76:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	_2_4_6__	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	_234____

Disable Communication Ports
D + D + 9

Timing Sheet By: JBM
Approved By: EFF
Drawing Number:
Timing Implemented On: 1/29/09

2

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1		1.8		2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9				4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1		1.8		6J2U	6
2				6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234___
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	____5678
E	-- -- -- -- -- -- -- --	1234___
F	-- 25 26 27 28 -- -- --	_2345___

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	0
2	System Det. # 2	0
3	System Det. # 3	0
4	System Det. # 4	0
5	System Det. # 5	0
6	System Det. # 6	0
7	System Det. # 7	0
8	System Det. # 8	0

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: HANCOCK ST & WASHINGTON ST

223 Program

Row	Column # ---> / Plan Name --->	Plan 1	2	3	4	5	6	7	8	9
0	Cycle Length	72	72	80	90					
1	Phase 1 - ForceOff	20	20	20	34					
2	Phase 2 - ForceOff	0	0	0	0					
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff	46	46	46	60					
5	Phase 5 - ForceOff									
6	Phase 6 - ForceOff	0	0	0	0					
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	29	52	77	26					
B	Offset B									
C	Offset C									
D	Permissive	10	10	10	10					
E	Hold Release	255	255	255	255					
F	Ped Shift	0	0	0	0					

Coordination <C Page>

C + Plan + ROW

Coordination Timing By: ARS
Implemented On: 11/28/94

FOR OBSERVATION ONLY

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Row	Time	Plan	Offset	Day of Week
0	05 : 10	2	A	_23456_
1	07 : 00	4	A	1234567
2	14 : 20	3	A	_23456_
3	17 : 00	4	A	_23456_
4	18 : 00	1	A	1234567
5	22 : 00	E	A	1234567
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination

<9 Key with C+0+9=1>

	E
Plan 1	_2_6__
Plan 2	_2_6__
Plan 3	_2_6__
Plan 4	_2_6__
Plan 5	
Plan 6	
Plan 7	
Plan 8	
Plan 9	
Coord Ped*	
NEMA Hold	

Sync Phases

C + E + FUNCTION #

Row		F
0	Free Lag	
1	Plan 1 - Lag	1_4_6__
2	Plan 2 - Lag	1_4_6__
3	Plan 3 - Lag	1_4_6__
4	Plan 4 - Lag	1_4_6__
5	Plan 5 - Lag	
6	Plan 6 - Lag	
7	Plan 7 - Lag	
8	Plan 8 - Lag	
9	Plan 9 - Lag	
A	Coord Max *	
B	Coord Lag *	
C		
D		
E		
F		

Lag Phases <C Page>

C + F + FUNCTION #

Transition Type	0

TBC Transition
C + D + D

Plan Select
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

Transition Type
0 = Shortway
Non-zero = Lengthen

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.C.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

E + F + ROW

<E Page>

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	2
3	RR-2 Limited Service	1 4
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2
B	EV-B Phases	4
C	EV-C Phases	1 6
D	EV-D Phases	
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

For access, set F + 9 + E = 1

Configuration

E + E + ROW

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications
(if set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

NTERSECTION: KETTNER @ SASSAFRAS

223 gram

Group Assignment: 4019
Field Master Assignment: NONE

N/S Street Name: KETTNER
E/W Street Name: SASSAFRAS

Timing Sheet By: MBF/ JD
Approved By: MR

Last Database Change: 6/3/02 14:51
System Ref. Number: 730
Drawing Number: 28180-2-D
Timing Implemented On: 7/10/02

N

Row	Column # —> / Phase # —>	1	2	3	4	5	6	7	8
					SASSAFRAS ST		KETTNER BL		SASSAFRAS ST
					Phase				
					→		↓		←
0	Ped Walk				7		7		7
1	Ped FDW				12		11		9
2	Min Green				4		10		4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn				2.0		4.6		2.5
6	Max Gap				2.0		4.6		2.5
7	Min Gap				2.0		0.2		2.5
8	Max Limit				30		60		30
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By						0.1		
D	Every						0.7		
E	Yellow				5.7		4.3		5.7
F	Red Clear				1.0		2.0		1.0

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	
EV-A Clear	
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	20
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

Row		F
0	Permit	4 6 8
1	Red Lock	
2	Yellow Lock	
3	Min Recall	6
4	Ped Recall	
5	Peds (View)	4 6 8
6	Rest In Walk	
7	Red Rest	
8	Dbl Entry	4 8
9	Max Recall	
A	Soft Recall	
B	Max 2	
C	Cond Serv	
D	Ped Lock	
E	Yellow Start	6
F	1st Phases	4 8

Phase Functions <F Page>
F + F + Row

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page> F + COLOR +

<D Page> D + 0 + OVERLAP

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	8	C + 0 + 0
Zone Number	8	C + 0 + 1
Area Number	2	C + 0 + 2
Area Address	136	C + 0 + 3
QuicNet Channel	DIGI14:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	4 6 8	0

Lag Phases <C Page>

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	234

Disable Communication Ports
D + D + 9

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Timing Sheet By:
Approved By:
Drawing Number:
Timing Implemented On:

Row	1 Delay	3 Carry-over
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
A	10.0	
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2 Delay	4 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <0 Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 – – – –	1234____
C	13 14 15 16 17 18 19 20	12345678
D	– – – – 21 22 23 24	____5678
E	– – – – – – – –	1234____
F	– 25 26 27 28 – – –	_2345___

Active Detectors <0 Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <0 Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: KETTNER @ SASSAFRAS

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function <D Page>

7 + ROW D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration <E Page>

E + F + ROW

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	4
3	RR-2 Limited Service	6 8
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	
B	EV-B Phases	4
C	EV-C Phases	6
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Protimod Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications

(If set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1-255
w/ E + E + E bit 5 on

INTERSECTION: Kettner Bl @ Laurel St

Group Assignment:
Field Master Assignment: NONE

N/S Street Name: Kettner Bl
E/W Street Name: Laurel St

Last Database Change 03/22/2005 0:00
System Ref. Number:

30

Column 2 -->	Laurel St	Laurel St		Kettner Bl		Laurel St		
Phase # -->	1	2	3	4	5	6	7	8
Row								
0 Ped Walk		7		7		7		
1 Ped FDW		11		12		11		
2 Min Green	4	20		25		20		
3 Type 3 Limit								
4 Add/Veh								
5 Veh Extn	2.0	1.0		1.0		1.0		
6 Max Gap	2.0	1.0		1.0		1.0		
7 Min Gap	2.0	1.0		1.0		1.0		
8 Max Limit	30	50		40		50		
9 Max Limit 2								
A Bus Adv								
B Call to Phs								
C Reduce By								
D Every								
E Yellow	3.4	3.9		4.4		5.6		
F Red Clear	1.0	1.0		1.0		1.0		
Grade				-1.0%		-10.0%		

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	
EV-D Clear	
RR-2 Delay	0
RR-2 Clear	20
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

		Row
Permit	12_4_6__	0
Red Lock		1
Yellow Lock		2
Min Recall	_2_4_6__	3
Ped Recall	_2_4_6__	4
Peds (View)	_2_4_6__	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	_2___6__	E
1st Phases	___4____	F

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	2	C + 0 + 0
Zone Number	2	C + 0 + 1
Area Number	0	C + 0 + 2
Area Address	209	C + 0 + 3
QuicNet Channel	DIGI14:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	_2_4_6__	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	234

Disable Communication Ports
D + D + 9

Timing Sheet By: BL
Approved By: AA
Drawing Number: 31921-5-D
Timing Implemented On: 3/22/05

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1				2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9				4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1				6J2U	2
2				6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 – – – –	1234
C	13 14 15 16 17 18 19 20	12345678
D	– – – – 21 22 23 24	5678
E	– – – – – – – –	1234
F	– 25 26 27 28 – – –	2345

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	0
2	System Det. # 2	0
3	System Det. # 3	0
4	System Det. # 4	0
5	System Det. # 5	0
6	System Det. # 6	0
7	System Det. # 7	0
8	System Det. # 8	0

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number	0	F+C+1
Time Before Yellow	0.0	F+C+3

Advance Warning Beacon - Sign 1

Phase Number	0	F+D+1
Time Before Yellow	0.0	F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: Kettner Bl @ Laurel St

223 Program

Coordination Timing By: SAE
Implemented On: 03/22/2005

Row	Column # --> / Plan Name -->	Plan 1	2	3	4	5	6	7	8	9
0	Cycle Length	104				80				
1	Phase 1 - ForceOff	52				52				
2	Phase 2 - ForceOff	0				0				
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff	32				32				
5	Phase 5 - ForceOff									
6	Phase 6 - ForceOff	0				0				
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A	75				75				
B	Offset B									
C	Offset C									
D	Permissive	10				10				
E	Hold Release	255				255				
F	Ped Shift	0				0				

Coordination <C Page>

C + Plan + ROW

FOR OBSERVATION ONLY

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Row	Time	Plan	Offset	Day of Week
0	06 : 00	5	A	1234567
1	10 : 00	1	A	_23456_
2	18 : 00	5	A	1234567
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination

<9 Key with C+0+9=1>

	E	Row		F
		0	Free Lag	
Plan 1	_2__6__	1	Plan 1 - Lag	_2_4_6__
Plan 2		2	Plan 2 - Lag	
Plan 3		3	Plan 3 - Lag	
Plan 4		4	Plan 4 - Lag	
Plan 5	_2__6__	5	Plan 5 - Lag	_2_4_6__
Plan 6		6	Plan 6 - Lag	
Plan 7		7	Plan 7 - Lag	
Plan 8		8	Plan 8 - Lag	
Plan 9		9	Plan 9 - Lag	
Coord Ped*		A	Coord Max *	__4___
NEMA Hold		B	Coord Lag *	
		C		
		D		
		E		
		F		

Sync Phases

C + E + FUNCTION #

Lag Phases <C Page>

C + F + FUNCTION #

Transition Type	0

TBC Transition
C + D + D

Plan Select
1 thru 9 = Coordination
Plan 1 thru 9
14 or E = Free
15 or F = Flash

Transition Type
0 = Shortway
Non-zero = Lengthen

INTERSECTION: Kettner Bl @ Laurel St

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

<E Page>

E + F + ROW

Day of Week

1 = Sunday
2 = Monday
3 = Tuosday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	2
3	RR-2 Limited Service	1 4
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2
B	EV-B Phases	4
C	EV-C Phases	1 6
D	EV-D Phases	
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration

For access, set F + 9 + E = 1

E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+8+0

Dial-Up Telephone Communications

(If set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1-255
w/ E + E + E bit 5 on

INTERSECTION: PACIFIC SB @ WASHINGTON

2 Program

Group Assignment: 4019
Field Master Assignment: NONE

N/S Street Name: PACIFIC SB
E/W Street Name: WASHINGTON

Last Database Change: 10/3..01 13:43
System Int Number: 733
Drawing Number: 19475-D
Timing Implemented On: 1/9/02

32

Row	Phase # --->	1	2	3	4	5	6	7	8
	Column # --->						PACIFIC HWY	WASHINGTON	WASHINGTON
0	Ped Walk						7	7	
1	Ped FDW						22	7	
2	Min Green						10	4	4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn						2.0	2.0	3.3
6	Max Gap						2.0	2.0	3.3
7	Min Gap						0.2	2.0	0.2
8	Max Limit						35	60	60
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By						0.1		0.1
D	Every						1.7		1.0
E	Yellow						5.2	3.0	3.3
F	Red Clear						1.0	1.0	1.0

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	12
EV-A Delay	
EV-A Clear	
EV-B Delay	0
EV-B Clear	0
EV-C Delay	
EV-C Clear	
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	16
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

Row		F
0	Permit	678
1	Red Lock	
2	Yellow Lock	
3	Min Recall	8
4	Ped Recall	
5	Peds (View)	67
6	Rest In Walk	
7	Red Rest	
8	Dbl Entry	
9	Max Recall	
A	Soft Recall	
B	Max 2	
C	Cond Serv	
D	Ped Lock	12345678
E	Yellow Start	6
F	1st Phases	78

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	1	C + 0 + 0
Zone Number	1	C + 0 + 1
Area Number	2	C + 0 + 2
Area Address	139	C + 0 + 3
QuicNet Channel	DIGI76:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	6 8	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page> F + COLOR +

<D Page> D + 0 + OVERLAP

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan	Manual Offset
0 = Automatic	0 = Automatic
1-9 = Plan 1-9	1 = Offset A
14 = Free	2 = Offset B
15 = Flash	3 = Offset C

Downtime Flash	255 (minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	234

Disable Communication Ports
D + D + 9

INTERSECTION: PACIFIC SB @ WASHINGTON

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:			
1	:			
2	:			
3	:			
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Digital Detector Off Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	67
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	
6	Ped 6P	6
7	Ped 4P	
8	Ped 8P	7
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	67
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	1

Configuration

<E Page>

E + F + ROW

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	6
2	RR-2 Clear Phases	8
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	6
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	
B	EV-B Phases	7
C	EV-C Phases	
D	EV-D Phases	8
E	Extra 1 Config. Bits	1_345
F	IC Select (Interconnect)	2

Configuration

For access, set F + 9 + E = 1

E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications
(if set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1				2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9				4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1				6J2U	2
2		1.8		6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8		1.8		8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 – – – –	1234___
C	13 14 15 16 17 18 19 20	12345678
D	– – – – 21 22 23 24	___5678
E	– – – – – – – –	1234___
F	– 25 26 27 28 – – –	_2345__

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: PACIFIC NB @ WASHINGTON

223 Diagram

Group Assignment: 4019
Field Master Assignment: NONE

N/S Street Name: PACIFIC NB
E/W Street Name: WASHINGTON

Timing Sheet By: JD/MR
Approved By: mm

Last Date Time Change: 10/17/2001 14:29
System Ref. Number: 734
Drawing Number: 26962-65
Timing Implemented On: 12/6/01

33

Row	Phase # --->	1	2	3	4	5	6	7	8
	Column # --->		WASHINGTON ST				WASHINGTON ST		PACIFIC HWY NB
0	Ped Walk		7				7		7
1	Ped FDW		11				25		19
2	Min Green		10			4	10	4	4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn		2.0			3.5	3.5	2.0	2.0
6	Max Gap		2.0			3.5	3.5	2.0	2.0
7	Min Gap		2.0			3.5	0.2	2.0	2.0
8	Max Limit		60			30	60	40	40
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By						0.1		
D	Every						0.9		
E	Yellow		3.0			3.0	3.4	3.0	4.4
F	Red Clear		1.0			1.5	1.0	1.0	2.0

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	20
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	18
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

	F	Row
Permit	_2__5678	0
Red Lock		1
Yellow Lock		2
Min Recall	_2_____	3
Ped Recall		4
Peds (View)	_2___6_8	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall	_____6__	A
Max 2		B
Cond Serv		C
Ped Lock		D
Yellow Start	_2___6__	E
1st Phases	_____7_	F

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	2	C + 0 + 0
Zone Number	2	C + 0 + 1
Area Number	2	C + 0 + 2
Area Address	140	C + 0 + 3
QuicNet Channel	DIGI76:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	2 6 8	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page> F + COLOR +

<D Page> D + 0 + OVERLAP

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	_234____

Disable Communication Ports
D + D + 9

INTERSECTION: PACIFIC NB @ WASHINGTON

223 gram

Group Assignment: 4019
Field Master Assignment: NONE

N/S Street Name: PACIFIC NB
E/W Street Name: WASHINGTON

Last Database Change: 10/17/2001 14:29
System Ref. Number: 734
Drawing Number: 26962-65
Timing Sheet By: JD/MBF
Approved By: mm
Timing Implemented On: 12/6/01

Row	Phase 1	Phase 2 (WASHINGTON ST)	Phase 3	Phase 4	Phase 5 (WASHINGTON ST)	Phase 6 (WASHINGTON ST)	Phase 7	Phase 8 (PACIFIC HWY NB)
0	Ped Walk	7				7		7
1	Ped FDW	11				25		19
2	Min Green	10			4	10	4	4
3	Type 3 Limit							
4	Add/Veh							
5	Veh Extn	2.0			3.5	3.5	2.0	2.0
6	Max Gap	2.0			3.5	3.5	2.0	2.0
7	Min Gap	2.0			3.5	0.2	2.0	2.0
8	Max Limit	60			30	60	40	40
9	Max Limit 2							
A	Bus Adv							
B	Call to Phs							
C	Reduce By					0.1		
D	Every					0.9		
E	Yellow	3.0			3.0	3.4	3.0	4.4
F	Red Clear	1.0			1.5	1.0	1.0	2.0

Phase Timing - Bank 1
F + Phase + Row
<F Page>

	E	Row
RR-1 Delay		
RR-1 Clear	20	
EV-A Delay	0	
EV-A Clear	0	
EV-B Delay	0	
EV-B Clear	0	
EV-C Delay	0	
EV-C Clear	0	
EV-D Delay	0	
EV-D Clear	0	
RR-2 Delay		
RR-2 Clear	18	
View EV Delay	---	
View EV Clear	---	
View RR Delay	---	
View RR Clear	---	

Preempt Timing
F + E + Row

	F	Row
Permit	_2__5678	0
Red Lock		1
Yellow Lock		2
Min Recall	_2_____	3
Ped Recall		4
Peds (View)	_2___6_8	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall	_____6__	A
Max 2		B
Cond Serv		C
Ped Lock		D
Yellow Start	_2___6__	E
1st Phases	_____7_	F

Phase Functions
F + F + Row
<F Page>

Max Initial	0
Red Revert	5.0
All Red Start	0.0

F + 0 + E
F + 0 + F
F + C + O

Start / Revert Times

Drop Number	2	C + 0 + 0
Zone Number	2	C + 0 + 1
Area Number	2	C + 0 + 2
Area Address	140	C + 0 + 3
QuicNet Channel	DIGI75:	(QuicNet)

Communication Addresses

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

C + F + O	F	Row
Free Lag	2 6 8	0

Lag Phases <C Page>

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + B

Disable Ports	_234____

Disable Communication Ports
D + D + 9

INTERSECTION: PACIFIC NB @ WASHINGTON

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:			
1	:			
2	:			
3	:			
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

E + F + ROW

<E Page>

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	2 5
2	RR-2 Clear Phases	6
3	RR-2 Limited Service	5 8
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	7
C	EV-C Phases	6
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Preimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications
(If set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

INTERSECTION: PACIFIC NB @ WASHINGTON

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:			
1	:			
2	:			
3	:			
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

<E Page>

E + F + ROW

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	2 5
2	RR-2 Clear Phases	6
3	RR-2 Limited Service	5 8
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	7
C	EV-C Phases	6
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration

For access, set F + 9 + E = 1

E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Prelimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications

(If set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

INTERSECTION: PACIFIC NB @ WASHINGTON

223 Program

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1				2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9				4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1		1.8		6J2U	2
2	10.0			6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	5678
E	-- -- -- -- -- -- -- --	1234
F	-- 25 26 27 28 -- -- --	2345

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1				2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9				4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1		1.8		6J2U	2
2	10.0			6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234__
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	___5678
E	-- -- -- -- -- -- -- --	1234__
F	-- 25 26 27 28 -- -- --	_2345__

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: PACIFIC & SASSAFRAS

Group Assignment: 4019
Field Master Assignment: NONE

N/S Street Name: PACIFIC
E/W Street Name: SASSAFRAS

34

Timing Sheet By: MBF/JD
Approved By: mm

2 Program

Last Database Change: 09/26/2001 14:21
System Ref. Number: 731
Drawing Number: 29123-6-D
Timing Implemented On: 09/15/01

Row	Column # --->	PACIFIC HWY			PORT DWY		PACIFIC HWY		SASSAFRAS ST
	Phase # --->	1	2	3	4	5	6	7	8
0	Ped Walk		7		7		7		7
1	Ped FDW		13		24		11		24
2	Min Green	4	10		4	4	10		4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	4.8		2.0	2.0	3.7		3.0
6	Max Gap	2.0	4.8		2.0	2.0	3.7		3.0
7	Min Gap	2.0	0.2		2.0	2.0	0.2		2.0
8	Max Limit	30	60		30	30	60		40
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		0.6				0.8		
E	Yellow	3.0	4.4		3.0	3.0	5.1		3.7
F	Red Clear	1.0	1.0		1.0	1.0	1.0		1.0

Phase Timing - Bank 1
F + Phase + Row

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	20
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

	F	Row
Permit	12_456_8	0
Rod Lock		1
Yellow Lock		2
Min Recall		3
Ped Recall		4
Peds (View)	_2_4_6_8	5
Rest in Walk		6
Red Rest		7
Dbl Entry	___4___8	8
Max Recall		9
Soft Recall	_2___6__	A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	_2___6__	E
1st Phases	___4___8	F

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	9	C + 0 + 0
Zone Number	9	C + 0 + 1
Area Number	2	C + 0 + 2
Area Address	137	C + 0 + 3
QuicNet Channel	DIGI14:	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	_2_4_6_8	0
	Lag Phases	<C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page> F + COLOR + <D Page> D + 0 + OVERLAP

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	_234____

Disable Communication Ports
D + D + 9

INTERSECTION: PACIFIC & SASSAFRAS

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0	:			
1	:			
2	:			
3	:			
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	8
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

<E Page>

E + F + ROW

Day of Week

1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	8
3	RR-2 Limited Service	2 56
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4
C	EV-C Phases	1 6
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 3456
F	IC Select (Interconnect)	2

Configuration

For access, set F + 9 + E = 1

E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications
(If set to a non-zero value, parity will be disabled)

Program Information

C + C + 0 = program
C + C + F = version

Remote Download

C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

Row	1 Delay	3 Carry-over	Detector Name	332 Input File	Detector Number
0				1I1	14
1		1.8		2I2U	1
2				2I2L	5
3				2I3U	21
4				2I3L	25
5				2I4	9
6				3I5	16
7				4I6U	3
8				4I6L	7
9	10.0			4I7U	23
A				4I7L	27
B				4I8	11
C				1I9U	18
D				3I9L	20
E	---	---	---	---	---
F	---	---	---	---	---

Row	2 Delay	4 Carry-over	Detector Name	332 Input File	Detector Number
0				5J1	13
1		1.8		6J2U	2
2				6J2L	6
3				6J3U	22
4				6J3L	26
5				6J4	10
6				7J5	15
7				8J6U	4
8				8J6L	8
9				8J7U	24
A				8J7L	28
B				8J8	12
C				5J9U	17
D				7J9L	19
E	---	---	---	---	---
F	---	---	---	---	---

Detector Delay & Carryover <U Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234____
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	____5678
E	-- -- -- -- -- -- -- --	1234____
F	-- 25 26 27 28 -- -- --	_2345___

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0										
1	8	20			12_456		8	8	8	
2		5				8	2 6	2 6 8	8	
3										
4										
5										
6										
7										
8										
9										
A					8		2 6	1 2 56		
B								MK		
C								7/1/92		
D										
E										
F										

Notes:
C1 Pin #75

10 C+5+F
Limited Service Interval

Special Event Schedule -- Table 1 <C Page with F+9+F=27>

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Notes:

10 C+5+F
Limited Service Interval

Special Event Schedule -- Table 2 <C Page with F+9+F=28>

INTERSECTION: LAUREL ST & PACIFIC HY

33

233 Program

Group Assignment:
Field Master Assignment:
System Reference Number:

N/S Street: PACIFIC HY
E/W Street: LAUREL

Last Database Change:

Timing sheets by: EFF
Approved by: EFF
Timing implemented on: 5/29/09

Row	Phase Numbers --->	1	2	3	4	5	6	7	8
		PACIFIC HY			LAUREL		PACIFIC HY		LAUREL
								6+7	
0	Ped Walk		7		7		7		7
1	Ped FDW		14		23		22		24
2	Min Green	4	10	4	7	4	10	4	7
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	3.3	2.0	3.9	2.0	4.1	2.0	2.7
6	Max Gap	2.0	3.3	2.0	3.9	2.0	4.1	2.0	2.7
7	Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
8	Max Limit	30	40	30	45	30	40	~~30~~ 20	45
9	Max Limit 2							calc	4/12/11
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every		1.0		0.8		0.8		1.2
E	Yellow Change	3.4	3.9	3.4	4.2	3.4	4.0	3.4	4.8
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 1 <F/1+Phase+Row>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	0
RR-2 Clear	20
View EV Delay	- - -
View EV Clear	- - -
View RR Delay	- - -
View RR Clear	- - -

Preempt Timing <F/1+E+Row>

	F	Row
Permit	12345678	0
Red Lock	[illegible]	1
Yellow Lock	[illegible]	2
Min Recall	_2_3_6_	3
Ped Recall		4
View Set Peds		5
Rest In Walk		6
Red Rest		7
Double Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond. Service		C
Man Cntrl Calls		D
Yellow Start	_2__6__	E
First Phases	__4__8	F

Phase Functions <F/1+F+Row>

Current Calculated Cycle Length: C/0 + B + F

	9	A	B	C	D
	- - -	- - -	- - -	- - -	- - -
Phase 1					
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing <F/1+Column+Phase>

Drop Number	1	<C/0+0+0>
Zone Number	1	<C/0+0+1>
Area Number	0	<C/0+0+2>
Area Address	208	<C/0+0+3>
QuicNet Channel	COM18	(QuicNet)

Communication Addresses

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

(Outputs specified in Assignable Outputs at E/127+A+E & F)

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

Exclusive Ped Phase

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>

Start / Revert Times

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Manual Plan	14	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Manual Selection

Free Lag	2 4 6 8	<C/1+F+0>

How to Set Page Access Code:
F/1 – C + 0 + F = 1

Notes: Used 233 program for right turn overlap with frequent RR pre-emptions.
Phase Bank 2 used as TOD function to provide LPI on phase 6.

INTERSECTION: LAUREL ST & PACIFIC HY

233 Program

Row	Detector Name	0 C1 Pin Number	1 Attributes	2 Phase(s)	3 Assign	1 Delay	3 Carry-Over
0	2I2U	39					1.8
1	6J2U	40					1.8
2		41					
3	8J6U	42					1.8
4		43					
5	6J2L	44					1.8
6	4I6L	45					1.8
7		46					
8		47					
9		48					
A		49					
B		50					
C		55					
D		56					
E		57					
F		58					

Row	Detector Name	4 C1 Pin Number	5 Attributes	6 Phase(s)	7 Assign	2 Delay	4 Carry-Over
0		59					
1		60					
2		61					
3		62					
4		63					
5		64					
6		65					
7		66					
8		67					
9		68					
A		69					
B		70					
C		76					
D		77					
E		78					
F		79					

Detector Assignments <E/126+Column+Row> <D/0+Column+Row>

Program Type:

Detector Attributes
1 = Full Time Delay
2 = Ped Call
3 =
4 = Count
5 = Extension
6 = Type 3
7 = Calling
8 = Alternate

Det. Assignments
1 = Det. Set 1
2 = Det. Set 2
3 = Det. Set 3
4 =
5 =
6 = Failure - Min Recall
7 = Failure - Max Recall
8 = Report on Failure

	Ped / Phase / Overlap 1	2	3	4	5	6	7	8	Row
Walk									0
Don't Walk									1
Phase Green									2
Phase Yellow									3
Phase Red									4
Overlap Green	35								5
Overlap Yellow	37								6
Overlap Red									7

Redirect Phase Outputs <E/127+Column+Row>

Cabinet Type	30

<E/125+D+0>

Enable Redirection
(Enable Redirection = 30)

Max OFF (minutes)	5	<D/0+0+1>
Max ON (minutes)	60	<D/0+0+2>

Detector Failure Monitor

	D	Row
		0
Output Port 1		1
Output Port 2		2
Output Port 3		3
Output Port 4		4
Output Port 5		5
Output Port 6		6
Output Port 7		7

Dimming <E/125+D+Row>

	D
Number of Digits	
1 st Digit	
2 ed Digit	
3 ed Digit	
4 th Digit	
5 th Digit	
6 th Digit	
7 th Digit	
8 th Digit	
9 th Digit	
10 th Digit	
11 th Digit	
12 th Digit	
13 th Digit	
14 th Digit	
15 th Digit	

<C/5+D+Row>

Dial-Back Telephone Number

Disable Alarms
1 = Stop Time
2 = Flash Sense
3 = Keyboard Entry
4 = Manual Plan
5 = Police Control
6 = External Alarm
7 = Detector Failure
8 =

	B	Row
DELAY-A	1	A
DELAY-B	1	B
DELAY-C	0	C
DELAY-D	0	D
DELAY-E	0	E
DELAY-F	0	F

<D/0+B+Row> (seconds)

Delay Logic Times

Omit Alarm	#NAME?

<C/5+F+0>

Disable Alarm Reporting

Time	0	<C/5+C+0>

Redial Time (minutes)
(View Redial Timer at E/2+D+6)

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Spec. Funct. 1		NOT-3		Max 2		Pretimed		Set Monday		Dial 2 (7-Wire)		Sim Term		0
1	Spec. Funct. 2		NOT-4		System Det 1		Plan 1		Ext. Perm 1		Dial 3 (7-Wire)		EV-A		1
2	Spec. Funct. 3		OR-4 (a)		System Det 2		Plan 2		Ext. Perm 2		Offset 1 (7-Wire)		EV-B		2
3	Spec. Funct. 4		OR-4 (b)		System Det 3		Plan 3		Dimming		Offset 2 (7-Wire)		EV-C		3
4	NAND-3 (a)		OR-5 (a)		System Det 4		Plan 4		Set Clock		Offset 3 (7-Wire)		EV-D		4
5	NAND-3 (b)		OR-5 (b)		System Det 5		Plan 5		Stop Time		Free (7-Wire)		RR-1		5
6	NAND-4 (a)		OR-6 (a)		System Det 6		Plan 6		Flash Sense	81	Flash (7-Wire)		RR-2		6
7	NAND-4 (b)		OR-6 (b)		System Det 7		Plan 7		Manual Enable		Excl. Ped Omit		Spec. Event 1		7
8	OR-7 (a)		Fig 3 Diamond		System Det 8		Plan 8		Man. Advance		NOT-1		Spec. Event 2		8
9	OR-7 (b)		Fig 4 Diamond		Max Inhibit (nema)		Plan 9		External Alarm		NOT-2		External Lag		9
A	OR-7 (c)		AND-4 (a)		Force A (nema)		DELAY-A		Phase Bank 2	200	OR-1 (a)		AND-1 (a)		A
B	OR-7 (d)		AND-4 (b)		Force B (nema)		DELAY-B		Phase Bank 3		OR-1 (b)		AND-1 (b)		B
C	OR-8 (a)		NAND-1 (a)		C.N.A. (nema)		DELAY-C		Overlap Set 2		OR-2 (a)		AND-2 (a)		C
D	OR-8 (b)		NAND-1 (b)		Hold (nema)		DELAY-D		Overlap Set 3		OR-2 (b)		AND-2 (b)		D
E	OR-8 (c)		NAND-2 (a)		Max Recall		DELAY-E		Detector Set 2		OR-3 (a)		AND-3 (a)		E
F	OR-8 (d)		NAND-2 (b)		Min Recall		DELAY-F		Detector Set 3		OR-3 (b)		AND-3 (b)		F

Assignable Inputs <E/126+Column+Row>

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Phase ON - 1		Preempt Fail		Flasher 0		Free		NOT-1		TOD Out 1	200	Dial 2 (7-Wire)		0
1	Phase ON - 2		Sp Evnt Out 1		Flasher 1		Plan 1		OR-1		TOD Out 2		Dial 3 (7-Wire)		1
2	Phase ON - 3		Sp Evnt Out 2		Fast Flasher		Plan 2		OR-2		TOD Out 3		Offset 1 (7-Wire)		2
3	Phase ON - 4		Sp Evnt Out 3		Fig 3 Diamond		Plan 3		OR-3		TOD Out 4		Offset 2 (7-Wire)		3
4	Phase ON - 5		Sp Evnt Out 4		Fig 4 Diamond		Plan 4		AND-1		TOD Out 5		Offset 3 (7-Wire)		4
5	Phase ON - 6		Sp Evnt Out 5				Plan 5		AND-2		TOD Out 6		Free (7-Wire)		5
6	Phase ON - 7		Sp Evnt Out 6				Plan 6		AND-3		TOD Out 7		Flash (7-Wire)		6
7	Phase ON - 8		Sp Evnt Out 7				Plan 7		NOT-2		TOD Out 8		Preempt		7
8	Ph. Check - 1		Sp Evnt Out 8		NOT-3		Plan 8		EV-A		Adv. Warn - 1		Low Priority A		8
9	Ph. Check - 2				NOT-4		Plan 9		EV-B		Adv. Warn - 2		Low Priority B		9
A	Ph. Check - 3		Detector Fail		OR-4		Spec. Funct. 3		EV-C		DELAY-A		Low Priority C		A
B	Ph. Check - 4		Spec. Funct. 1		OR-5		Spec. Funct. 4		EV-D		DELAY-B		Low Priority D		B
C	Ph. Check - 5		Spec. Funct. 2		OR-6		NAND-3		RR-1		DELAY-C				C
D	Ph. Check - 6		Central Control		AND-4		NAND-4		RR-2		DELAY-D				D
E	Ph. Check - 7		Excl. Ped DW		NAND-1		OR-7		Spec. Event 1		DELAY-E				E
F	Ph. Check - 8		Excl. Ped WK		NAND-2		OR-8		Spec. Event 2		DELAY-F				F

Assignable Outputs <E/127+Column+Row>

Row		Phase 1	2	3	4	5	6	7	8
0	Ped Walk		7		7		12		7
1	Ped FDW		14		23		22		24
2	Min Green	4	10	4	7	4	10	4	7
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension	2.0	3.3	2.0	3.9	2.0	4.1	2.0	2.7
6	Max Gap	2.0	3.3	2.0	3.9	2.0	4.1	2.0	2.7
7	Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
8	Max Limit	30	40	30	45	30	40	30	45
9	Max Limit 2							50	50
A	Adv. / Delay Walk						5		
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every		1.0		0.8		0.8		1.2
E	Yellow Change	3.4	3.9	3.4	4.2	3.4	4.0	3.4	4.8
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
	---	---	---	---	---
Phase 1					
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Transition Type
0.X = Shortway
1.X = Lengthen
X.1 thru X.4 = Number of cycles when lengthing

Transition Type	0.0	<C/5+1+9>

TBC Transition

Lag Hold Phases		<C/5+1+A>

Coordinated Lag Hold Phases

Sync Output Time	0.0	<C/5+1+C>

7-Wire Master

Time B4 Yellow	0.0	<F/1+C+E>
Phase Number	0	<F/1+C+F>

Advance Warning Beacon - Sign 1

Time B4 Yellow	0.0	<F/1+D+E>
Phase Number	0	<F/1+D+F>

Advance Warning Beacon - Sign 2

Row		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
	---	---	---	---	---
Phase 1					
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Long Failure	0.5	<F/1+0+6>
Short Failure	0.5	<F/1+0+7>

Power Cycle Correction (Default = 0.7)

Min Time (seconds)	0	<F/1+0+8>

Min Green Before PE Force Off

Max Time (minutes)	0	<F/1+0+9>

Max Preempt Time Before Failure

Min Time (seconds)	0	<F/1+0+A>

Min Time Between Same Preempts
(Does Not Apply To Railroad Preempt)

Low Priority
1 = Channel A
2 = Channel B
3 = Channel C
4 = Channel D

Low Pri. Channel	#NAME?	<E/125+C+8>

Disable Low Priority Channel

Row	Time	Plan	Offset	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination <9/0.1+Row>
(Bank 1)

Time	Funct.	Day of Week
05 : 00	F	_23456_
09 : 00	F	_23456_
12 : 00	F	_23456_
18 : 00	F	_23456_

TOD Function <7/0.1+Row>

Column 4 Phases/Bits
1_____
1_____
3

<E/27+4+Row>

Row	Day	Year	Month	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Dates <8/1.1+Row>
(Bank 1)

Time	Plan	Offset	Holiday Type

Holiday Events <9/1.1+Row>
(Bank 1)

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

T.O.D. Functions:
0 =
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest in Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 8

Plan Select:
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

Month Select:
1 = January
2 = February
3 = March
4 = April
5 = May
6 = June
7 = July
8 = August
9 = September
A = October
B = November
C = December

Cycle Timer:
Master: C/0 + A + 0
Ring A: C/0 + B + 0
Ring B: C/0 + D + 0

Interval Timer:
Ring A: F/0 + A + Interval Row
Ring B: F/0 + B + Interval Row

Master Plan: C/0 + A + 2
Current Plan: C/0 + A + 3
TOD Plan: C/0 + A + 5

Row	Time	Plan	Offset	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination <9/0.2+Row>
(Bank 2)

Time	Funct.	Holiday Type

Holiday TOD Function <7/0.2+Row>

Column 4 Phases/Bits

<E/28+Row>

Row	Day	Year	Month	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Dates <8/1.2+Row>
(Bank 2)

Time	Plan	Offset	Holiday Type

Holiday Events <9/1.2+Row>
(Bank 2)

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

INTERSECTION: LAUREL ST & PACIFIC HY

233 Program

Row		Overlap 1	2	3	4	5	6	7	8
0	Load Switch Number								
1	Veh Set 1 - Phases	67							
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases	12 56 8							
5	Neg Ped Phases	2 6 8							
6	Green Omit Phases	6							
7	Green Clear Omit Phs.								
8									
9									
A									
B									
C									
D	Green Clear								
E	Yellow Change	3.4							
F	Red Clear	1.0							

Overlap Assignments <E/29+Column+Row>

Row		F
0	Fast Green Flash Phase	
1	Green Flash Phases	
2	Flashing Walk Phases	
3	Guaranteed Passage	
4	Simultaneous Gap Term	12345678
5	Sequential Timing	
6	Advance Walk Phases	6
7	Delay Walk Phases	
8	External Recall	
9	Start-up Overlap Green	
A	Max Extension	
B	Inhibit Ped Reservice	
C	Semi-Actuated	
D	Start-up Overlap Yellow	
E	Start-up Vehicle Calls	
F	Start-up Ped Calls	

Specials <F/2+F+Row>

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	3 8
3	RR-2 Limited Service	2 567
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4 7
C	EV-C Phases	1 6
D	EV-D Phases	3 8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration <E/125+E+Row>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	12345678
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	4
Ped for 8P Output	8
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	

Configuration <E/125+F+Row>

	C
EV-A	
EV-B	
EV-C	
EV-D	
RR-1 *	---
RR-2 *	---
SE-1	0
SE-2	0

<E/125+C+Row>

Preemption Priority

(* RR-1 is always Highest, and RR-2 is always Second Highest)

	2
Phase 1	0
Phase 2	0
Phase 3	0
Phase 4	0
Phase 5	0
Phase 6	0
Phase 7	0
Phase 8	0

<C/5+2+Row>

Coordination Transition Minimums

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Extended Status
6 = International Ped
7 = Flash - Clear Outputs
8 = Split Ring

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Extra 2 Flags
1 = AWB During Initial
2 = LMU Installed
3 = Disable Min Walk
4 = QuicNet/4 System
5 = Ignore P/P on EV
6 =
7 = Reserved
8 =

Flash to PE & PE Non-Lock
1 = EV A 5 = RR 1
2 = EV B 6 = RR 2
3 = EV C 7 = SE 1
4 = EV D 8 = SE 2

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Time and Date

Begin Month	0	<C/5+2+A>
Begin Week	0	<C/5+2+B>
End Month	0	<C/5+2+C>
End Week	0	<C/5+2+D>

Daylight Savings Time

Daylight Savings Date:
If set to all zeros, standard dates will be used.

PACIFIC HIGHWAY @ ROSECRANS ST/TAYLOR ST

233 Program

36

Group Assignment:
Field Master Assignment:
System Reference Number:

N/S Street: Pacific Hy
E/W Street: Rosecrans St/Taylor St

Last Database Change:

Timing sheets by: SE4
Approved by: BL
Timing implemented on:

	Rosecrans		Pacific Hy		Taylor St		Pacific Hy	
Phase Numbers---->	1	2	3	4	5	6	7	8
Row	OLA	OLC	OLB					
0 Ped Walk		7		7		7		7
1 Ped FDW		25		29		22		24
2 Min Green	4	10	4	7	4	10	4	7
3 Type 3 Disconnect								
4 Added per Vehicle								
5 Veh Extension	2.0	2.1	2.0	4.5	2.0	2.3	2.0	4.5
6 Max Gap	2.0	2.1	2.0	4.5	2.0	2.3	2.0	4.5
7 Min Gap	2.0	0.1	2.0	0.2	2.0	0.2	2.0	0.2
8 Max Limit	30	50	30	40	30	50	30	40
9 Max Limit 2								
A Adv. / Delay Walk								
B PE Min Ped FDW								
C Cond Serv Check								
D Reduce Every		1.6		0.7		1.4		0.7
E Yellow Change	3.4	3.9	3.4	3.9	3.4	3.9	3.4	3.9
F Red Clear	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0

Phase Timing - Bank 1 <F/1+Phase+Row>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	0
RR-2 Clear	20
View EV Delay	- - -
View EV Clear	- - -
View RR Delay	- - -
View RR Clear	- - -

Preempt Timing <F/1+E+Row>

	F	Row
Permit	12345678	0
Red Lock		1
Yellow Lock		2
Min Recall	_2___6__	3
Ped Recall		4
View Set Peds	- - - - -	5
Rest In Walk		6
Red Rest		7
Double Entry		8
Max Recall		9
Soft Recall		A
Max 2		B
Cond. Service		C
Man Cntrl Calls		D
Yellow Start	_2___6__	E
First Phases	___4___8	F

Phase Functions <F/1+F+Row>

Current Calculated Cycle Length: C/0 + B + F

	9	A	B	C	D
	- - -	- - -	- - -	- - -	- - -
Phase 1					
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing <F/1+Column+Phase>

Drop Number		<C/0+0+0>
Zone Number		<C/0+0+1>
Area Number		<C/0+0+2>
Area Address		<C/0+0+3>
QuicNet Channel		(QuicNet)

Communication Addresses

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

(Outputs specified in Assignable Outputs at E/127+A+E & F)

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

Exclusive Ped Phase

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	0.0	<F/1+C+0>

Start / Revert Times

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Manual Plan	14	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Manual Selection

Notes: Phase 02: Bus Priority
OLC: Vehicle Phase

Free Lag	_2 4 6 8	<C/1+F+0>

How to Set Page Access Code:
F/1 -- C + 0 + F = 1

PACIFIC HIGHWAY @ ROSECRANS ST/TAYLOR ST

233 Program

Row	Detector Name	0 C1 Pin Number	1 Attributes	2 Phase(s)	3 Assign	1 Delay	3 Carry-Over
0							
1	2I2U						1.8
2	2I2L						
3	2I3U						1.8
4	2I3L						1.8
5	2I4						
6	3I5						
7	4I6U						1.8
8	4I6L						
9	4I7U						
A	4I7L						
B							
C							
D							
E							
F							

Program Type:

	Ped / Phase / Overlap 1	2	3	4	5	6	7	8	Row
Walk									0
Don't Walk									1
Phase Green									2
Phase Yellow									3
Phase Red									4
Overlap Green	35	36	96						5
Overlap Yellow	36	38	95						6
Overlap Red			94						7

Redirect Phase Outputs <E/127+Column+Row>

Cabinet Type	30

<E/125+D+0>

Enable Redirection
(Enable Redirection = 30)

Max OFF (minutes)	5	<D/0+0+1>
Max ON (minutes)	60	<D/0+0+2>

Detector Failure Monitor

	D	Row
		0
Output Port 1		1
Output Port 2		2
Output Port 3		3
Output Port 4		4
Output Port 5		5
Output Port 6		6
Output Port 7		7

Dimming <E/125+D+Row>

Row	Detector Name	4 C1 Pin Number	5 Attributes	6 Phase(s)	7 Assign	2 Delay	4 Carry-Over
0							
1	6J2U						1.8
2	6J2L						
3	6J3U						
4	6J3L						
5	6J4						
6	7J5						
7	8J6U						1.8
8	8J6L					10.0	
9	8J7U						
A	8J7L						
B							
C							
D							
E							
F							

Detector Assignments <E/126+Column+Row> <D/0+Column+Row>

Detector Attributes
1 = Full Time Delay
2 = Ped Call
3 =
4 = Count
5 = Extension
6 = Type 3
7 = Calling
8 = Alternate

Det. Assignments
1 = Det. Set 1
2 = Det. Set 2
3 = Det. Set 3
4 =
5 =
6 = Failure - Min Recall
7 = Failure - Max Recall
8 = Report on Failure

	D
Number of Digits	
1 st Digit	
2 ed Digit	
3 ed Digit	
4 th Digit	
5 th Digit	
6 th Digit	
7 th Digit	
8 th Digit	
9 th Digit	
10 th Digit	
11 th Digit	
12 th Digit	
13 th Digit	
14 th Digit	
15 th Digit	

<C/5+D+Row>

Dial-Back Telephone Number

Disable Alarms
1 = Stop Time
2 = Flash Sense
3 = Keyboard Entry
4 = Manual Plan
5 = Police Control
6 = External Alarm
7 = Detector Failure
8 =

	B	Row
DELAY-A	0	A
DELAY-B	0	B
DELAY-C	0	C
DELAY-D	0	D
DELAY-E	0	E
DELAY-F	0	F

<D/0+B+Row> (seconds)

Delay Logic Times

Omit Alarm	_____

<C/5+F+0>

Disable Alarm Reportin

Time	0	<C/5+C+0>

Redial Time (minutes)
(View Redial Timer at E/2+D+

PACIFIC HIGHWAY @ ROSECRANS ST/TAYLOR ST

233 Program

Coord Extra
1 = Programmed WALK Time for Sync Phases
2 = Always Terminate Sync Phase Peds

Row	Plan Name ---->	Plan 1	2	3	4	5	6	7	8	9	Row
0	Cycle Length										0
1	Phase 1 - ForceOff										1
2	Phase 2 - ForceOff										2
3	Phase 3 - ForceOff										3
4	Phase 4 - ForceOff										4
5	Phase 5 - ForceOff										5
6	Phase 6 - ForceOff										6
7	Phase 7 - ForceOff										7
8	Phase 8 - ForceOff										8
9	Ring Offset										9
A	Offset 1										A
B	Offset 2										B
C	Offset 3										C
D	Perm 1 - End										D
E	Hold Release										E
F	Zone Offset										F

Coordination - Timing Plans <C/1+Plan+Row>

	E	Row
		0
Plan 1 - Sync		1
Plan 2 - Sync		2
Plan 3 - Sync		3
Plan 4 - Sync		4
Plan 5 - Sync		5
Plan 6 - Sync		6
Plan 7 - Sync		7
Plan 8 - Sync		8
Plan 9 - Sync		9
NEMA Sync		A
NEMA Hold		B
		C
		D
Coord Extra		E
		F

Sync Phases <C/1+E+Row>

Row											Row
0	Ped Adjustment										0
1	Perm 2 - Start										1
2	Perm 2 - End										2
3	Perm 3 - Start										3
4	Perm 3 - End										4
5	Reservice Time										5
6	Reservice Phases										6
7											7
8	Pretimed Phases										8
9	Max Recall										9
A	Perm 1 Veh Phase										A
B	Perm 1 Ped Phase										B
C	Perm 2 Veh Phase										C
D	Perm 2 Ped Phase										D
E	Perm 3 Veh Phase										E
F	Perm 3 Ped Phase										F

Coordination - Parameters <C/2+Plan+Row>

	F	Row
Free Lag		0
Plan 1 - Lag		1
Plan 2 - Lag		2
Plan 3 - Lag		3
Plan 4 - Lag		4
Plan 5 - Lag		5
Plan 6 - Lag		6
Plan 7 - Lag		7
Plan 8 - Lag		8
Plan 9 - Lag		9
External Lag		A
		B
		C
		D
		E
		F

Lag Phases <C/1+F+Row>

Coordination Timing By:

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0		0								
1		20		1 6	2345 78		1 6	1 6	2 4 6 8	
2		14			1 6		2	2	2	
3		10				2	345 78	345 78		
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 1 <C+0+E=27>

Notes
Data Implemented on 03/07/06
by SE4 & EFF

3 <E/27+5+F>
Limited Service Interval

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 2 <C+0+E=28>

Notes.

0 <E/28+5+F>
Limited Service Interval

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Spec. Funct. 1		NOT-3		Max 2		Pretimed		Set Monday		Dial 2 (7-Wire)		Sim Term		0
1	Spec. Funct. 2		NOT-4		System Det 1		Plan 1		Ext. Perm 1		Dial 3 (7-Wire)		EV-A	71	1
2	Spec. Funct. 3		OR-4 (a)		System Det 2		Plan 2		Ext. Perm 2		Offset 1 (7-Wire)		EV-B	72	2
3	Spec. Funct. 4		OR-4 (b)		System Det 3		Plan 3		Dimming		Offset 2 (7-Wire)		EV-C	73	3
4	NAND-3 (a)		OR-5 (a)		System Det 4		Plan 4		Set Clock		Offset 3 (7-Wire)		EV-D	74	4
5	NAND-3 (b)		OR-5 (b)		System Det 5		Plan 5		Stop Time	82	Free (7-Wire)		RR-1	51	5
6	NAND-4 (a)		OR-6 (a)		System Det 6		Plan 6		Flash Sense	81	Flash (7-Wire)		RR-2		6
7	NAND-4 (b)		OR-6 (b)		System Det 7		Plan 7		Manual Enable		Excl. Ped Omit		Spec. Event 1	52	7
8	OR-7 (a)		Fig 3 Diamond		System Det 8		Plan 8		Man. Advance		NOT-1		Spec. Event 2		8
9	OR-7 (b)		Fig 4 Diamond		Max Inhibit (nema)		Plan 9		External Alarm		NOT-2		External Lag		9
A	OR-7 (c)		AND-4 (a)		Force A (nema)		DELAY-A		Phase Bank 2		OR-1 (a)		AND-1 (a)		A
B	OR-7 (d)		AND-4 (b)		Force B (nema)		DELAY-B		Phase Bank 3		OR-1 (b)		AND-1 (b)		B
C	OR-8 (a)		NAND-1 (a)		C.N.A. (nema)		DELAY-C		Overlap Set 2	52	OR-2 (a)		AND-2 (a)		C
D	OR-8 (b)		NAND-1 (b)		Hold (nema)		DELAY-D		Overlap Set 3		OR-2 (b)		AND-2 (b)		D
E	OR-8 (c)		NAND-2 (a)		Max Recall		DELAY-E		Detector Set 2		OR-3 (a)		AND-3 (a)		E
F	OR-8 (d)		NAND-2 (b)		Min Recall		DELAY-F		Detector Set 3		OR-3 (b)		AND-3 (b)		F

Assignable Inputs <E/126+Column+Row>

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Phase ON - 1		Preempt Fail		Flasher 0		Free		NOT-1		TOD Out 1		Dial 2 (7-Wire)		0
1	Phase ON - 2		Sp Evnt Out 1		Flasher 1		Plan 1		OR-1		TOD Out 2		Dial 3 (7-Wire)		1
2	Phase ON - 3		Sp Evnt Out 2		Fast Flasher		Plan 2		OR-2		TOD Out 3		Offset 1 (7-Wire)		2
3	Phase ON - 4		Sp Evnt Out 3		Fig 3 Diamond		Plan 3		OR-3		TOD Out 4		Offset 2 (7-Wire)		3
4	Phase ON - 5		Sp Evnt Out 4		Fig 4 Diamond		Plan 4		AND-1		TOD Out 5		Offset 3 (7-Wire)		4
5	Phase ON - 6		Sp Evnt Out 5				Plan 5		AND-2		TOD Out 6		Free (7-Wire)		5
6	Phase ON - 7		Sp Evnt Out 6				Plan 6		AND-3		TOD Out 7		Flash (7-Wire)	81	6
7	Phase ON - 8		Sp Evnt Out 7				Plan 7		NOT-2		TOD Out 8		Preempt		7
8	Ph. Check - 1		Sp Evnt Out 8		NOT-3		Plan 8		EV-A		Adv. Warn - 1		Low Priority A		8
9	Ph. Check - 2				NOT-4		Plan 9		EV-B		Adv. Warn - 2		Low Priority B		9
A	Ph. Check - 3		Detector Fail		OR-4		Spec. Funct. 3		EV-C		DELAY-A		Low Priority C		A
B	Ph. Check - 4		Spec. Funct. 1		OR-5		Spec. Funct. 4		EV-D		DELAY-B		Low Priority D		B
C	Ph. Check - 5		Spec. Funct. 2		OR-6		NAND-3		RR-1		DELAY-C				C
D	Ph. Check - 6		Central Control		AND-4		NAND-4		RR-2		DELAY-D				D
E	Ph. Check - 7		Excl. Ped DW		NAND-1		OR-7		Spec. Event 1		DELAY-E				E
F	Ph. Check - 8		Excl. Ped WK		NAND-2		OR-8		Spec. Event 2		DELAY-F				F

Assignable Outputs <E/127+Column+Row>

Row	Time	Plan	Offset	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination <9/0.1+Row>
(Bank 1)

Time	Funct.	Day of Week	Column 4 Phases/Bits

TOD Function <7/0.1+Row> <E/27+4+Row>

Row	Day	Year	Month	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Dates <8/1.1+Row>
(Bank 1)

Time	Plan	Offset	Holiday Type	Row
				0
				1
				2
				3
				4
				5
				6
				7
				8
				9
				A
				B
				C
				D
				E
				F

Holiday Events <9/1.1+Row>
(Bank 1)

T.O.D. Functions:
0 =
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 8

Plan Select:
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

Month Select:
1 = January
2 = February
3 = March
4 = April
5 = May
6 = June
7 = July
8 = August
9 = September
A = October
B = November
C = December

Cycle Timer:
Master: C/0 + A + 0
Ring A: C/0 + B + 0
Ring B: C/0 + D + 0

Interval Timer:
Ring A: F/0 + A + Interval Row
Ring B: F/0 + B + Interval Row

Master Plan: C/0 + A + 2
Current Plan: C/0 + A + 3
TOD Plan: C/0 + A + 5

Row	Time	Plan	Offset	Day of Week
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination <9/0.2+Row>
(Bank 2)

Time	Funct.	Holiday Type	Column 4 Phases/Bits

Holiday TOD Function <7/0.2+Row> <E/28+Row>

Row	Day	Year	Month	Holiday Type
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

Holiday Dates <8/1.2+Row>
(Bank 2)

Time	Plan	Offset	Holiday Type	Row
				0
				1
				2
				3
				4
				5
				6
				7
				8
				9
				A
				B
				C
				D
				E
				F

Holiday Events <9/1.2+Row>
(Bank 2)

PACIFIC HIGHWAY @ ROSECRANS ST/TAYLOR ST

233 Program

Row		Overlap 1	2	3	4	5	6	7	8
		OLA	OLB	OLC					
0	Load Switch Number	9	10	11					
1	Veh Set 1 - Phases	1_____8	_23_____	_2_____					
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases	_234__7_	1__45___	1_34__78					
5	Neg Ped Phases	_2_4__8	_2_4__8	___4__8					
6	Green Omit Phases	_____8							
7	Green Clear Omit Phs.								
8									
9									
A									
B									
C									
D	Green Clear								
E	Yellow Change	3.9	3.9	3.9					
F	Red Clear	2.0	2.0	2.0					

Overlap Assignments <E/29+Column+Row>

	F	Row
Fast Green Flash Phase		0
Green Flash Phases		1
Flashing Walk Phases		2
Guaranteed Passage		3
Simultaneous Gap Term	12345678	4
Sequential Timing		5
Advance Walk Phases		6
Delay Walk Phases		7
External Recall		8
Start-up Overlap Green		9
Max Extension		A
Inhibit Ped Reservice		B
Semi-Actuated		C
Start-up Overlap Yellow		D
Start-up Vehicle Calls		E
Start-up Ped Calls		F

Specials <F/2+F+Row>

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	1____6__
3	RR-2 Limited Service	__345_78
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	_2__5___
B	EV-B Phases	___4__7_
C	EV-C Phases	1____6__
D	EV-D Phases	__3____8
E	Extra 1 Config. Bits	1_34____
F	IC Select (Interconnect)	_2_____

Configuration <E/125+E+Row>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	12345678
Ped for 2P Output	_2_____
Ped for 6P Output	_____6__
Ped for 4P Output	___4____
Ped for 8P Output	_____8
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	

Configuration <E/125+F+Row>

	C
EV-A	
EV-B	
EV-C	
EV-D	
RR-1 *	---
RR-2 *	---
SE-1	0
SE-2	0

<E/125+C+Row>

Preemption Priority

(* RR-1 is always Highest, and RR-2 is always Second Highest)

Row
0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F

	2	Row
		0
Phase 1	0	1
Phase 2	0	2
Phase 3	0	3
Phase 4	0	4
Phase 5	0	5
Phase 6	0	6
Phase 7	0	7
Phase 8	0	8
		9
		A
		B
		C
		D
		E
		F

<C/5+2+Row>

Coordination Transition Minimums

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Extended Status
6 = International Ped
7 = Flash - Clear Outputs
8 = Split Ring

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Extra 2 Flags
1 = AWB During Initial
2 = LMU Installed
3 = Disable Min Walk
4 = QuicNet/4 System
5 = Ignore PIP on EV
6 =
7 = Reserved
8 =

Flash to PE & PE Non-Lock
1 = EV A 5 = RR 1
2 = EV B 6 = RR 2
3 = EV C 7 = SE 1
4 = EV D 8 = SE 2

8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Time and Date

Begin Month	0	<C/5+2+A>
Begin Week	0	<C/5+2+B>
End Month	0	<C/5+2+C>
End Week	0	<C/5+2+D>

Daylight Savings Time

Daylight Savings Date:
If set to all zeros, standard dates will be used.

INTERSECTION: CONGRESS ST & TAYLOR ST

223 Pre[illegible]m

Group Assignment:
Field Master Assignment:

N/S Street Name: CONGRESS ST
E/W Street Name: TAYLOR ST

Last Database Change:
System Ref. Number:

Row	Phase # --->	1	2	3	4	5	6	7	8
	Column # --->	TAYLOR ST			CONGRESS ST PED		TAYLOR ST		CONGRESS ST
0	Ped Walk		7		7				
1	Ped FDW		14		18				
2	Min Green	4	10		4		10		4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	2.9				2.0		2.0
6	Max Gap	2.0	2.9				2.0		2.0
7	Min Gap	2.0	0.2				0.2		2.0
8	Max Limit	30	60				60		40
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		1.1				1.7		
E	Yellow	3.4	3.9		3.0		3.9		3.9
F	Red Clear	1.0	1.0		1.0		1.0		1.0

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	
EV-B Clear	
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	0
RR-2 Clear	17
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

Row		F
0	Permit	12_4_6_8
1	Red Lock	
2	Yellow Lock	
3	Min Recall	_2___6__
4	Ped Recall	
5	Peds (View)	
6	Rest In Walk	
7	Red Rest	
8	Dbl Entry	___4___8
9	Max Recall	
A	Soft Recall	
B	Max 2	
C	Cond Serv	
D	Ped Lock	12345678
E	Yellow Start	_2___6__
F	1st Phases	___4___8

Phase Functions <F Page>
F + F + Row

Max Initial	0
Red Revert	5.0
All Red Start	0.0

F + 0 + E
F + 0 + F
F + C + O

Start / Revert Times

Drop Number	12	C + 0 + 0
Zone Number	12	C + 0 + 1
Area Number	2	C + 0 + 2
Area Address	163	C + 0 + 3
QuicNet Channel	COM84	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	_2_4_6_8	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	234

Disable Communication Ports
D + D + 9

Timing Sheet By: JDS
Approved By: FLG
Drawing Number: 26877-94-D
Timing Implemented On: 4/21/09

INTERSECTION: CONGRESS ST & TAYLOR ST

223 Program

Row	1 Delay	3 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2 Delay	4 Carry-over
0		
1		
2		
3		1.8
4		
5		
6		
7	10.0	
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234____
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	____5678
E	-- -- -- -- -- -- -- --	1234____
F	-- 25 26 27 28 -- -- --	__2345__

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

45

200 SA 1.e

PHASE TIMING

INTERVAL		1	2	3	4	5	6	7	8	PREEMPT	E	
			TAYLOR		JUAN		TAYLOR		JUAN			
WALK	0		7		7		7		7	RR 1 DELAY		0
FLASH D/W	1		8		18		17		15	RR 1 CLEAR		1
MIN GREEN	2	4	10		4	4	10		4	EVA DELAY	0	2
TYPE 3 DET	3									EVA CLEAR	3	3
ADD PER VEH	4									EVB DELAY	0	4
VEH EXTN *	5	2.0	2.5		2.0	2.0	2.5		2.0	EVB CLEAR	3	5
MAX GAP *	6	2.0	2.5		2.0	2.0	2.5		2.0	EVC DELAY	0	6
MIN GAP *	7	2.0	0.2		2.0	2.0	0.2		2.0	EVC CLEAR	3	7
MAX EXTN	8	30	50		30	30	50		30	EVD DELAY	0	8
MAX 2	9									EVD CLEAR	3	9
BUS LEAD	A									RR2 DELAY	0	A
CALL TO PHS	B	4				4				RR 2 CLEAR	20	B
REDUCE BY	C		0.1				0.1			EV CLR TMR		C
EVERY	D		1.30				1.30			EV DLY TMR		D
YELLOW	E	3.0	3.5		3.0	3.0	3.5		3.0	RR CLR TMR		E
RED CLEAR	F	1.0	1.0		1.0	1.0	1.0		1.0	RR DLY TMR		F

* Must be the same for non-density operation

F + PHASE + LOCATION

MAX INITIAL (F-0-E) 0 RED REVERT (F-0-F) 5.0 ALL-RED START (F-C-0) 0.0

PHASE FUNCTION FLAGS

		PHASE 1	2	3	4	5	6	7	8
PERMIT	0	X	X		X	X	X		X
RED LOCK	1								
YELLOW LOCK	2								
VEHICLE RECALL	3		X				X		
PED RECALL	4								
PEDS (VIEW)	5								
REST IN WALK	6								
RED REST	7								
DOUBLE ENTRY	8				X				X
MAX RECALL	9								
SOFT RECALL	A		X				X		
MAX 2	B								
COND SERVICE	C								
	D								
START-UP	E		X				X		
FIRST PHASES	F				X				X

F + F + FUNCTION #

OVERLAP TIMING

	LOAD SWITCH	GREEN 9	YELLOW C	RED D
OLAP A				
OLAP B				
OLAP C				
OLAP D				

D + 0 + OVERLAP

F + COLOR + OVERLAP

PHASE SEQUENCE

	PHASE 1	2	3	4	5	6	7	8
LAG 0 (FREE)		X		X		X		X

C + F + 0

MANUAL (C-A-1) 14

ADDRESS (C-0-0)

PPLT TO 1-15-03

10/10a FLG

TURN-ON DATE: 09-17-1963

TIME:

DRAWING NO. D- 26877-97

LAST CHANGE: 07-30-1996

BY: MPS DRM

LOCATION JUAN STREET & TAYLOR STREET

DETECTOR TIMES

Y \ X	SET DELAY 1	2	SET CARRY 3	4	OBS COUNT 5	6	OBS DELAY 7	8	OBS CARRY 9	A
0	1U1	5J1	1R	5J1	1R	5J1	1U1	5J1	1R	5J1
1	2J2U	6J2U	2I2U 1.8	6J2U 1.8	2I2U	6J2U	2I2U	6J2U	2I2U	6J2U
2	2I2L	6J2L	2I2L	6J2L	2I2L	6J2L	2I2L	6J2L	2I2L	6J2L
3	2I3U	6J3U	2I3U	6J3U	2I3U	6J3U	2I3U	6J3U	2I3U	6J3U
4	2I3L	6J3L	2I3L	6J3L	2I3L	6J3L	2I3L	6J3L	2I3L	6J3L
5	2I4	6J4	2I4	6J4	2I4	6J4	2I4	6J4	2I4	6J4
6	3I5	7J5	3I5	7J5	3I5	7J5	3I5	7J5	3I5	7J5
7	4I6U	8J6U	4I6U	8J6U	4I6U	8J6U	4I6U	8J6U	4I6U	8J6U
8	4I6L	8J6L	4I6L	8J6L	4I6L	8J6L	4I6L	8J6L	4I6L	8J6L
9	4I7U	8J7U	4I7U	8J7U	4I7U	8J7U	4I7U	8J7U	4I7U	8J7U
A	4I7L	8J7L	4I7L	8J7L	4I7L	8J7L	4I7L	8J7L	4I7L	8J7L
B	4I8	8J8	4I8	8J8	4I8	8J8	4I8	8J8	4I8	8J8
C	1I9U	5J9U	1I9U	5J9U	1I9U	5J9U	1I9U	5J9U	1I9U	5J9U
D	3I9L	7J9L	3I9L	7J9L	3I9L	7J9L	3I9L	7J9L	3I9L	7J9L

D + X (ACROSS) + Y (DOWN)

TO ACCESS CONFIGURATION DATA, SET F-9-E = 1

TIME OF DAY FEATURES

EVENT	TIME	FUNC	DAY OF WEEK S 1	M 2	T 3	W 4	T 5	F 6	S 7
0									
1									
2									
3									
4									
5									
6									
7									
8									
9									

7 + EVENT #

PHASE OR FUNCTION

1	2	3	4	5	6	7	8

D + F + EVENT

TIME OF DAY FUNCTION CODES

- 0-PERMIT
- 1-RED LOCK
- 2-YELLOW LOCK
- 3-VEH RECALL
- 4-PED RECALL
- 5-(RESERVED)
- 6-REST IN WALK
- 7-RED REST
- 8-DBL ENTRY
- 9-VEH MAX RCL
- A-SOFT RCL
- B-MAX 2 EXT
- C-COND SERV
- D-TOD LAG PHS

- E-L-LOCAL OVERRIDE
- 2-PHASE BANK 2
- 3-PHASE BANK 3
- 7-DET COUNT
- 8-SPLIT MONITOR

- F - TOD OUTPUTS
- 1- TOD OUTPUT 1
- 2- TOD OUTPUT 2
- 3- TOD OUTPUT 3
- 4- TOD OUTPUT 4

TIME AND DATE

8 - 0 HOUR, MINUTE, DAY-OF-WEEK
8 - 1 DAY-OF-MONTH, YEAR, MONTH
8 - F SECONDS

CONFIGURATION DATA

		PHASE 1	2	3	4	5	6	7	8
EXCLUSIVE	0								
RR1 GN CLR	1								
RR2 GRN CLR	2		X			X			
RR2 LTD SRV	3	X	X		X	X	X		X
PRO/PERM	4	X				X			
OLA GN OMIT	5								
OLB GN OMIT	6								
OLC GN OMIT	7								
OLD GN OMIT	8								
OV FL YELLOW	9								
EMVEH A	A		X			X			
EMVEH B	B				X				
EMVEH C	C	X					X		
EMVEH D	D								X
EXTRA	E	X		X	X	X			
IC SELECT	F		X						

E + E + INTERVAL

EXTRA (E+E+E)
- 1-TBC TYPE 1
- 2-NEMA COORD
- 3-DAYLIGHT SAVINGS
- 4-EV ADVANCE
- 5-REMOTE EVP
- 6-SPECIAL EVENT
- 7-PRETIMED
- 8-SPLIT RING

ICSELECT (E+E+F)
- 1-SIMPLEX IN
- 2-2 WAY MODEM
- 3-7 WIRE IN
- 4-FLH/FREE
- 5-SIMPLEX OUT
- 6-7 WIRE OUT

ASSIGNS (E+F+F)
- 1-RT OVERLAP
- 2-TOD OUTPUTS
- 3-STEADY EV BEACON
- 4-FLASH EV BEACON

CONFIGURATION DATA

		PHASE 1	2	3	4	5	6	7	8
	0								
RR OLAP A	1								
RR OLAP B	2								
RR OLAP C	3								
RR OLAP D	4								
PED 2P	5		X						
PED 6P	6						X		
PED 4P	7				X				
PED 8P	8								X
FLH YELLOW	9								
OVERLAP A	A								
OVERLAP B	B								
OVERLAP C	C								
OVERLAP D	D								
RESTRICT	E								
ASSIGNS	F								

E + F + INTERVAL

LOCATION JUAN STREET & TAYLOR STREET

44

TRAFFIC SIGNAL ~~TIMING~~ - BITS 152

N

PHASE TIMING

			S.D. AV. ↓		←		S.D. AV. ↑		O.T. AV. →	PREEMPT		
PHASE # →		1	2	3	4	5	6	7	8		E	
WALK	0		7		7		7		7	RR1 DELAY		0
FLASH D/W	1		~~13~~ 14		~~11~~ 13		12		13	RR1 CLEAR		1
MIN GREEN	2		~~10~~ 4		4		~~10~~ 4		4	EV A DELAY	0	2
TYPE 3 DET	3									EV A CLEAR	3	3
ADD/VEH	4									EV B DELAY		4
VEH EXTEN	5		~~2.7~~ 4.4		2		~~2.7~~ 4.4		2	EV B CLEAR		5
MAX GAP	6		~~2.7~~ 4.4		2		~~2.7~~ 4.4		2	EV C DELAY	0	6
MIN GAP	7		0.2		2		0.2		2	EV C CLEAR	3	7
MAX EXTEN	8		40		30		40		30	EV D DELAY		8
MAX 2	9		~~40~~		~~30~~		~~40~~		~~30~~	EV D CLEAR		9
	A									RR2 DELAY		A
	B									RR2 CLEAR		B
REDUCE BY	C		0.1				0.1			EV CLR TMR		C
REDUCE EVERY	D		0.8				0.8			EV DLY TMR		D
YELLOW	E		3		3		3		3	RR CLR TMR		E
RED CLEAR	F		1.0		1.0		1.0		1.0	RR DLY TMR		F

LOCATION

4.7 JD 7/19/02

MAX INITIAL (F-0-E) = _____ KEYSTROKES: F + PHASE + LOCATION RED REVERT (F-0-F) = _____

PHASE FUNCTION FLAGS

		PHASE 1	2	3	4	5	6	7	8
PERMIT	0		X		X		X		X
RED LOCK	1								
YELLOW LOCK	2								
VEH RECALL	3		X				X		
PED RECALL	4								
PEDS	5								
REST IN WALK	6								
RED REST	7								
DOUBLE ENTRY	8				X				X
MAX RECALL	9								
OVERLAP A	A								
OVERLAP B	B								
OVERLAP C	C								
OVERLAP D	D								
STARTUP	E		X				X		
(RESERVED)	F								

KEYSTROKES: F + F + FUNCTION #

PHASE SEQUENCES

		PHASE 1	2	3	4	5	6	7	8
LAG 0 (FREE)	0		X		X		X		X
LAG 1	1								
LAG 2	2								
LAG 3	3								
LAG 4	4								
LAG 5	5								
LAG 6	6								
LAG 7	7								
LAG 8	8								
LAG 9	9								
COOR MAX RECALL	A								
COOR LAG RECALL	B								
SYNC PHASES	C								
HOLD	D								
NEXT PHASE	E								
FORCE OFF	F								

KEYSTROKES: C + F + FUNCTION #

REMARKS:

LOC: OLD TOWN AV @ SAN DIEGO AV DATE:

INTERVAL	F / A	1	2	3	4	5	6	7	8
			↓		←		↑		→
WALK	0		7		7		7		7
DONT WALK	1		7		7		6		7
MIN GREEN	2		6		2		6		2
TYPE 3 DET	3								
ADD/VEH	4								
PASSAGE	5		2.7		2		2.7		2
MAX GAP	6		2.7		2		2.7		2
MIN GAP	7		0.2		2		0.2		2
MAX EXT	8		40		30		40		30
MAX EXT TWO	9		30		30		40		30
	A								
	b								
REDUCE BY	C		.1				.1		
REDUCE EVERY	d		.8				.8		
YELLOW	E		3		3		3		3
RED	F				-		-		-
PED-XING - FT.			40'		45'		36'		40'

	FUNCTIONS	1	2	3	4	5	6	7	8
0	PERMIT FF0		X		X		X		X
1	RED LOCK FF1								
2	YEL LOCK FF2								
3	VEH RECALL FF3		X				X		
4	PED RECALL FF4								
5	PED PHASES FF5		X		X		X		X
6	OVERLAP - A FF6								
7	OVERLAP - B FF7								
8	DBL ENTRY FF8				X				X
9	MAX TWO FF9								
A	LAG PHASES FFA		X		X		X		X
b	RED REST FFB								
C	SEMI ACT FFC								
d	TYPE 3 DET FFD								
E	STARTUP FFE		X				X		

	PREEMPTION	
0	RR 1 DELAY FE0	
1	RR 1 TRK CLR FE1	
2	EV A DELAY FE2	0
3	EV A HOLD FE3	2
4	EV B DELAY FE4	
5	EV B HOLD FE5	
6	EV C DELAY FE6	0
7	EV C HOLD FE7	2
8	EV D DELAY FE8	
9	EV D HOLD FE9	
A	RR 2 DELAY FEA	
b	RR 2 TRK CLR FEB	
C		
d		
E		

PZ 8 P6 Conversion [illegible]
PZ 8 P8 [illegible]

Dwg. 20367-1-0

DATE:

LOCATION: Old Town Ave & San Diego Ave

CITY OF SAN DIEGO, CALIFORNIA

MEMORANDUM

DD-503 (REV. 6-70)

1. FROM (Name and Dept.): Larry Legrand Via John Tsiknas

2. DATE: 1/17/83

3. TO (In order indicated below, for purpose checked at right):

	ACTION	PREPARE REPLY	INFORMATION	TALK TO ME ABOUT IT	WRITTEN COMMENT	SIGNATURE NEEDED	RETURN TO ME	COPY FOR YOU	FILE
1. Andy Orag Via Ernie Shaffer									
2.									
3.									
4.									
5.									

4. SUBJECT: Timing Schedule: Old Town & San Diego

5. COMMENTS: Attached is the timing schedule for the intersection of Old Town Av. and San Diego Av. The scheduled turn-on date for this signal is 1/27/83.

Larry

DETECTOR TIMES (332 CABINET)

X → SET DELAY SET CARRY OBS COUNT OBS DELAY OBS CARRY

Y ↓

Y	1	2	3	4	5	6	7	8	9	A
0	1I1	5J1	1I1	5J1	1I1	5J1	1I1	5J1	1I1	5J1
1	2I2U	6J2U	2I2U 1.8	6J2U 1.8	2I2U	6J2U	2I2U	6J2U	2I2U	6J2U
2	2I2L	6J2L	2I2L	6J2L	2I2L	6J2L	2I2L	6J2L	2I2L	6J2L
3	2I3U	6J3U	2I3U	6J3U	2I3U	6J3U	2I3U	6J3U	2I3U	6J3U
4	2I3L	6J3L	2I3L	6J3L			2I3L	6J3L	2I3L	6J3L
5	2I4	6J4	2I4	6J4			2I4	6J4	2I4	6J4
6	3I5	7J5	3I5	7J5	3I5	7J5	3I5	7J5	3I5	7J5
7	4I6U 10"	8J6U 10"	4I6U	8J6U	4I6U	8J6U	4I6U	8J6U	4I6U	8J6U
8	4I6L	8J6L	4I6L	8J6L	4I6L	8J6L	4I6L	8J6L	4I6L	8J6L
9	4I7U	8J7U	4I7U	8J7U	4I7U	8J7U	4I7U	8J7U	4I7U	8J7U
A	4I7L	8J7L	4I7L	8J7L			4I7L	8J7L	4I7L	8J7L
B	4I8	8J8	4I8	8J8			4I8	8J8	4I8	8J8
C	1I9U	5J9U	1I9U	5J9U	1I9U	5J9U	1I9U	5J9U	1I9U	5J9U
D	3I9L	7J9L	3I9L	7J9L	3I9L	7J9L	3I9L	7J9L	3I9L	7J9L
E										
F										

I.D. TIME

KEYSTROKES: D + X + Y

TIME OF DAY FUNCTION

	TIME (24 HOUR)	FUN	DAY OF WEEK 1	2	3	4	5	6	7
0									
1									
2									
3									
4									
5									
6									
7									
8									
9									
A									
B									
C									
D									
E									
F									

KEYSTROKES: 7 + EVENT NUMBER

PEDESTRIAN ASSIGNMENTS

LOC	SWITCH-PACK	INPUT CI PIN	PHASE 1	2	3	4	5	6	7	8
EF5	2P	67		X						
EF6	6P	68						X		
EF7	4P	69				X				
EF8	8P	70								X

EMERGENCY VEHICLE PREEMPT ASSIGNMENTS

LOC	PREEM	INPUT CI PIN	PHASE 1	2	3	4	5	6	7	8
EEA	EVA	71		X				X		
EEB	EVB	72								
EEC	EVC	73								X
EED	EVD	74								

RAILROAD PREEMPTION ASSIGNMENTS

LOC	PHASE	1	2	3	4	5	6	7	8
EE1	RR1 CLEAR								
EE2	RR2 CLEAR								
EE3	RR2 LMT SVC								

CONDITIONAL SERVICE D-F-O _____

LONG POWER FAIL CORRECTION F-O-6 _____

SHORT POWER FAIL CORRECTION F-O-7 _____

CURRENT TIME AND DATE

8-0 HOUR, MINUTE, DAY OF WEEK

8-1 DAY OF MONTH, YEAR, MONTH

C-4-9 SECONDS

INTERSECTION: MOORE ST @ OLD TOWN AVE

223 Program

Group Assignment:
Field Master Assignment:

N/S Street Name: MOORE
E/W Street Name: OLD TOWN

Row	Phase # ---->	1	2	3	4	5	6	7	8
	Column # ---->		OLD TOWN		MOORE		L OWN		MOORE
			↑		↓	PPLT ↑	←		↑
0	Ped Walk		7						7
1	Ped FDW		10						10
2	Min Green		10		4	15	10		4
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn		2.0		2.0	1.0	2.0		2.0 ~~5.2~~
6	Max Gap		2.0		2.0	1.0	2.0		2.0 ~~5.2~~
7	Min Gap		2.0		2.0	1.0	2.0		2.0 ~~0.2~~
8	Max Limit		40		20	0	40		20
9	Max Limit 2		60		17	0	60		17
A	Bus Adv								
B	Call to Phs								
C	Reduce By								0.1
D	Every								0.6
E	Yellow		3.9		3.9	3.4	3.9		3.9
F	Red Clear		1.0		1.0	1.0	1.0		1.0

Phase Timing - Bank 1
F + Phase + Row

<F Page>

MS2 06/02/09

	E		F	Row
RR-1 Delay		Permit	_2_456_8	0
RR-1 Clear		Red Lock		1
EV-A Delay	0	Yellow Lock		2
EV-A Clear	0	Min Recall	_2___6__	3
EV-B Delay	0	Ped Recall		4
EV-B Clear	0	Peds (View)	_2_____8	5
EV-C Delay	0	Rest In Walk		6
EV-C Clear	0	Red Rest		7
EV-D Delay	0	Dbl Entry	___4___8	8
EV-D Clear	0	Max Recall		9
RR-2 Delay		Soft Recall		A
RR-2 Clear		Max 2		B
View EV Delay	---	Cond Serv		C
View EV Clear	---	Ped Lock	12345678	D
View RR Delay	---	Yellow Start	___4___8	E
View RR Clear	---	1st Phases	_2___6__	F

F + E + Row

F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number		C + 0 + D
Zone Number		C + 0 + 1
Area Number		C + 0 + 2
Area Address		C + 0 + 3
QuicNet Channel		(QuicNet)

Communication Addresses

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	14	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

C + F + O	F	Row
Free Lag	_2_4_6_8	0

Lag Phases <C Page>

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	_234____

Disable Communication Ports
D + D + 9

Timing Sheet By: MS2
Approved By: FLG
Drawing Number: 25167-2-D
Timing Implemented On: 06/02/09

* DS port is a T.B.D. [illegible] TM test.

INTERSECTION: MOORE ST @ OLD TOWN AVE

223 Program

Row	1 Delay	3 Carry-over
0		
1	10.0	
2		
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2 Delay	4 Carry-over
0		
1		
2		
3		
4		
5		
6		
7		1.8
8	5.0	
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234____
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	____5678
E	-- -- -- -- -- -- -- --	1234____
F	-- 25 26 27 28 -- -- --	_2345___

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: MOORE ST @ OLD TOWN AVE

223 Program

Row	Time	Function	Day of Week	Column F Phases/Bits
0	15 : 00	B	1234567	_2_4_6_8
1	18 : 00	B	1234567	
2	14 : 30	3	_23456_	2__5__
3	17 : 30	3	_23456_	_2__6__
4	* 12 : 00	3	____6_	_2__5__
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function

7 + ROW

<D Page>

D + F + ROW

M52 disabling

* M55 disabling

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest in Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	_2_____
6	Ped 6P	
7	Ped 4P	
8	Ped 8P	_____8
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration

E + F + ROW

<E Page>

Day of Week
1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 = Bus Advance

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	____5___
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	_2_____
B	EV-B Phases	____4___
C	EV-C Phases	_____6__
D	EV-D Phases	_____8
E	Extra 1 Config. Bits	1_345___
F	IC Select (interconnect)	_2_____

Configuration

E + E + ROW

For access, set F + 9 + E = 1

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Time and Date
8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+8+0

Dial-Up Telephone Communications
(If set to a non-zero value, parity will be disabled)

Program Information
C + C + 0 = program
C + C + F = version

Remote Download
C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

INTERSECTION: HUGO/N HARBOR @ ROSECRANS

223 Program

49

Group Assignment: ROSECRANS
Field Master Assignment: HUGO/N. HARBOR ROSECRANS

N/S Street Name: ROSECRANS
E/W Street Name: HUGO/N HARBOR

Last Database Change:
Ref. Number:

N

Row	Phase # --->	1	2	3	4	5	6	7	8
	Column # --->	Phase							
0	Ped Walk		7		7		7		
1	Ped FDW		15		18		14		
2	Min Green	4	10		7	4	10		
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	2.1		2.0	2.0	2.1		
6	Max Gap	2.0	2.1		2.0	2.0	2.1		
7	Min Gap	2.0	0.2		2.0	2.0	0.2		
8	Max Limit	20	45		40	20	45		
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1				0.1		
D	Every		1.5				1.5		
E	Yellow	3.4	3.9		3.9	3.4	3.9		
F	Red Clear	1.0	1.0		1.0	1.0	1.0		
	Grade								

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	
EV-A Clear	
EV-B Delay	
EV-B Clear	
EV-C Delay	
EV-C Clear	
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

Row		F
0	Permit	12_456__
1	Red Lock	
2	Yellow Lock	
3	Min Recall	
4	Ped Recall	
5	Peds (View)	_2_4_6__
6	Rest In Walk	
7	Red Rest	
8	Dbl Entry	
9	Max Recall	
A	Soft Recall	_2___6__
B	Max 2	
C	Cond Serv	
D	Ped Lock	12345678
E	Yellow Start	_2___6__
F	1st Phases	___4____

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	7	C + 0 + 0
Zone Number	7	C + 0 + 1
Area Number	1	C + 0 + 2
Area Address	84	C + 0 + 3
QuicNet Channel	~~CM1%DIGI72:~~	(QuicNet)

Communication Addresses

C + F + O	F	Row
Free Lag	_2_4_6__	0

Lag Phases <C Page>

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	_234____

Disable Communication Ports
D + D + 9

Timing Sheet By: EFF/WXH
Approved By: EFF
Drawing Number:
Timing Implemented On: 2/10/2003

JDS 12/9/09

Row	1 Delay	3 Carry-over
0		
1		1.8
2		1.8
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2 Delay	4 Carry-over
0		
1		1.8
2		1.8
3		
4		
5		
6		
7		
8		
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234____
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	____5678
E	-- -- -- -- -- -- -- --	1234____
F	-- 25 26 27 28 -- -- --	_2345___

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: HUGO ST/N. HARBOR DR @ ROSECRANS ST

Coordination Timing By: MES
Implemented On: 3/24/2010

Row	Column # ---->	1	2	3	4	5	6	7	8	9
	Plan Name ---->		MID		WKND		AM	PM		
0	Cycle Length		70		70		116	150		
1	Phase 1 - ForceOff		36		36		46	51		
2	Phase 2 - ForceOff		0		0		0	0		
3	Phase 3 - ForceOff									
4	Phase 4 - ForceOff		20		20		30	35		
5	Phase 5 - ForceOff		36		36		46	51		
6	Phase 6 - ForceOff		0		0		0	0		
7	Phase 7 - ForceOff									
8	Phase 8 - ForceOff									
9	Ring Offset									
A	Offset A		48		30		113	115		
B	Offset B									
C	Offset C									
D	Permissive		12		12		12	8		
E	Hold Release		255		255		255	255		
F	Ped Shift		10		10		0	0		

Plan

Coordination <C Page>

C + Plan + ROW

FOR OBSERVATION ONLY

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Row	Time	Plan	Offset	Day of Week
0	06 : 30	6	A	23456
1	11 : 00	2	A	23456
2	13 : 30	7	A	23456
3	18 : 30	E	A	23456
4	10 : 00	4	A	1 7
5	18 : 00	E	A	1 7
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination

<9 Key with C+0+9=1>

	E	Row		F
		0	Free Lag	
Plan 1		1	Plan 1 - Lag	
Plan 2	2 6	2	Plan 2 - Lag	2 4 6
Plan 3		3	Plan 3 - Lag	
Plan 4	2 6	4	Plan 4 - Lag	2 4 6
Plan 5		5	Plan 5 - Lag	
Plan 6	2 6	6	Plan 6 - Lag	2 4 6
Plan 7	2 6	7	Plan 7 - Lag	2 4 6
Plan 8		8	Plan 8 - Lag	
Plan 9		9	Plan 9 - Lag	
Coord Ped*		A	Coord Max *	
NEMA Hold		B	Coord Lag *	
		C		
		D		
		E		
		F		

Sync Phases

C + E + FUNCTION #

Lag Phases <C Page>

C + F + FUNCTION #

Transition Type	0

TBC Transition
C + D + D

Transition Type
0 = Shortway
Non-zero = Lengthen

Plan Select
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

RS 12/8/07

Row	Time	Function	Day of Week	Column F Phases/Bits
0	00:01	E	1	1
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function
7 + ROW

<D Page>
D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest in Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	
9	Yellow Flash Phases	
A	Overlap A - Phases	
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	

Configuration
E + F + ROW

<E Page>

Day of Week
1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	
B	EV-B Phases	
C	EV-C Phases	
D	EV-D Phases	
E	Extra 1 Config. Bits	1 3 5
F	IC Select (Interconnect)	2

For access, set F + 9 + E = 1

Configuration
E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date
8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications
(If set to a non-zero value, parity will be disabled)

Program Information
C + C + 0 = program
C + C + F = version

Remote Download
C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on

INTERSECTION: NIMITZ @ ROSECRANS

60

223 Program

Group Assignment:
Field Master Assignment:

N/S Street Name: ROSECRANS
E/W Street Name: NIMITZ

Last Database Change:
System Ref. Number:

N

Column # --->		ROSECRANS		NIMITZ		ROSECRANS		NIMITZ	
		Phase							
Row	Phase #--->	1	2	3	4	5	6	7	8
		1+8							
0	Ped Walk		7		5		7		5
1	Ped FDW		18		17		13		24
2	Min Green	4	10	6 4	10	2 4	10	8 4	10
3	Type 3 Limit								
4	Add/Veh								
5	Veh Extn	2.0	4.2	2.0	2.6	2.0	3.0	2.0	4.0
6	Max Gap	2.0	4.2	2.0	2.6	2.0	3.0	2.0	4.0
7	Min Gap	2.0	0.2	2.0	0.2	2.0	0.2	2.0	0.2
8	Max Limit	30	60	20	30	40	60	20	30
9	Max Limit 2								
A	Bus Adv								
B	Call to Phs								
C	Reduce By		0.1		0.1		0.1		0.1
D	Every		0.7		1.3		1.1		0.8
E	Yellow	3.4	3.9	3.4	3.9	3.4	4.4	3.4	4.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
	Grade		0.0%				0.0%		

Phase Timing - Bank 1
F + Phase + Row

<F Page>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	0
EV-A Clear	0
EV-B Delay	0
EV-B Clear	0
EV-C Delay	0
EV-C Clear	0
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing
F + E + Row

	F	Row
Permit	12345678	0
Red Lock		1
Yellow Lock		2
Min Recall		3
Ped Recall		4
Peds (View)	_2_4_6_8	5
Rest In Walk		6
Red Rest		7
Dbl Entry		8
Max Recall		9
Soft Recall	_2___6__	A
Max 2		B
Cond Serv		C
Ped Lock	12345678	D
Yellow Start	_2___6__	E
1st Phases	___4___8	F

Phase Functions <F Page>
F + F + Row

Max Initial	0	F + 0 + E
Red Revert	5.0	F + 0 + F
All Red Start	0.0	F + C + O

Start / Revert Times

Drop Number	8	C + 0 + 0
Zone Number	8	C + 0 + 1
Area Number	1	C + 0 + 2
Area Address	85	C + 0 + 3
QuicNet Channel	DIGI72:	(QuicNet)

Communication Addresses

Overlap Timing

	Row	9 Green Clear	C Yellow Change	D Red Clear	0 Load-Switch #
Overlap A	A				
Overlap B	B				
Overlap C	C				
Overlap D	D				

<F Page>
F + COLOR +

<D Page>
D + 0 + OVERLAP

Manual Plan	0	C + A + 1
Manual Offset	0	C + B + 1

Manual Selection

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

C + F + O	F	Row
Free Lag	_2_4_6_8	0

Lag Phases <C Page>

Downtime Flash	255	(minutes)

Downtime Before Auto Manual Flash
F + 0 + 8

Disable Ports	_234____

Disable Communication Ports
D + D + 9

Timing Sheet By: EFF/WXH
Approved By: EFF
Drawing Number:
Timing Implemented On: 2/10/03

Row	1 Delay	3 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		1.8
8		1.8
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	1I1	14
	2I2U	1
	2I2L	5
	2I3U	21
	2I3L	25
	2I4	9
	3I5	16
	4I6U	3
	4I6L	7
	4I7U	23
	4I7L	27
	4I8	11
	1I9U	18
	3I9L	20
---	---	---
---	---	---

Row	2 Delay	4 Carry-over
0		
1		1.8
2		
3		
4		
5		
6		
7		1.8
8		1.8
9		
A		
B		
C		
D		
E	---	---
F	---	---

Detector Name	332 Input File	Detector Number
	5J1	13
	6J2U	2
	6J2L	6
	6J3U	22
	6J3L	26
	6J4	10
	7J5	15
	8J6U	4
	8J6L	8
	8J7U	24
	8J7L	28
	8J8	12
	5J9U	17
	7J9L	19
---	---	---
---	---	---

Detector Delay & Carryover <D Page>

D + X (across) + ROW

Row	Detector Numbers	E
A	1 2 3 4 5 6 7 8	12345678
B	9 10 11 12 -- -- -- --	1234____
C	13 14 15 16 17 18 19 20	12345678
D	-- -- -- -- 21 22 23 24	____5678
E	-- -- -- -- -- -- -- --	1234____
F	-- 25 26 27 28 -- -- --	_2345___

Active Detectors <D Page>

Row		0 Detector #
0		
1	System Det. # 1	
2	System Det. # 2	
3	System Det. # 3	
4	System Det. # 4	
5	System Det. # 5	
6	System Det. # 6	
7	System Det. # 7	
8	System Det. # 8	

System Detectors <D Page>

Max ON (min)	5	D+A+E
Max OFF (min)	60	D+A+F

Detector Failure Monitor

Phase Number		F+C+1
Time Before Yellow		F+C+3

Advance Warning Beacon - Sign 1

Phase Number		F+D+1
Time Before Yellow		F+D+3

Advance Warning Beacon - Sign 2

Long Failure	0.5	F+0+6
Short Failure	0.5	F+0+7

Power Cycle Correction (Default = 0.5)

INTERSECTION: NIMITZ BLVD@ ROSECRANS ST

223 Program

Coordination Timing By: M2S
Implemented On: 3/22/2010

Row	Column # --->	Plan 1	2	3	4	5	6	7	8	9
	Plan Name --->		MID		WKND		AM	PM	AM (PEAK)	
0	Cycle Length		140		140		116	150	116	
1	Phase 1 - ForceOff		88		88		77	94	71	
2	Phase 2 - ForceOff		0		0		0	0	0	
3	Phase 3 - ForceOff		20		20		16	21	16	
4	Phase 4 - ForceOff		56		56		56	63	50	
5	Phase 5 - ForceOff		94		94		77	103	71	
6	Phase 6 - ForceOff		0				0	0	0	
7	Phase 7 - ForceOff		56		56		56	63	50	
8	Phase 8 - ForceOff		34		34		34	32	28	
9	Ring Offset									
A	Offset A		60		98		85	93	75	
B	Offset B									
C	Offset C									
D	Permissive		10		10		12	15	12	
E	Hold Release		255		255		255	255	255	
F	Ped Shift		5		5		0	0	3	

Coordination <C Page>
C + Plan + ROW

FOR OBSERVATION ONLY

Master Plan	C + A + 2
Current Plan	C + A + 3
Next Plan	C + A + 4
T.O.D. Plan	C + A + 5
Master Cycle	C + A + 0
Ring A Cycle	C + B + 0
Ring B Cycle	C + D + 0
Min Cycle	C + A + E
Max Cycle	C + B + E

Row	Time	Plan	Offset	Day of Week
0	06 : 30	8	A	_23456_
1	08 : 00	6	A	_23456_
2	11 : 00	2	A	_23456_
3	13 : 30	7	A	_23456_
4	18 : 30	E	A	_23456_
5	10 : 00	4	A	1____7
6	18 : 00	E	A	1____7
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Coordination
<9 Key with C+0+9=1>

	E	Row		F
		0	Free Lag	
Plan 1		1	Plan 1 - Lag	
Plan 2	_2__6__	2	Plan 2 - Lag	_2_4_67_
Plan 3		3	Plan 3 - Lag	
Plan 4	_2__6__	4	Plan 4 - Lag	_2_4_67_
Plan 5		5	Plan 5 - Lag	
Plan 6	_2__6__	6	Plan 6 - Lag	_2_4_67_
Plan 7	_2__6__	7	Plan 7 - Lag	_2_4_67_
Plan 8	_2__6__	8	Plan 8 - Lag	_2_4_67_
Plan 9		9	Plan 9 - Lag	
Coord Ped*		A	Coord Max *	
NEMA Hold		B	Coord Lag *	
		C		
		D		
		E		
		F		

Sync Phases
C + E + FUNCTION #

Lag Phases <C Page>
C + F + FUNCTION #

Transition Type	0

TBC Transition
C + D + D

Plan Select
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

Transition Type
0 = Shortway
Non-zero = Lengthen

Row	Time	Function	Day of Week	Column F Phases/Bits
0				
1				
2				
3				
4				
5				
6				
7				
8				
9				
A				
B				
C				
D				
E				
F				

TOD Function

7 + ROW

<D Page>

D + F + ROW

T.O.D. Functions
0 = Permitted Phases
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 2 - Phase Bank 2
Bit 3 - Phase Bank 3
Bit 4 - Disable Detector OFF Monitor
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 4

Row		F
0		
1	RR Overlap A - Phases	
2	RR Overlap B - Phases	
3	RR Overlap C - Phases	
4	RR Overlap D - Phases	
5	Ped 2P	2
6	Ped 6P	6
7	Ped 4P	4
8	Ped 8P	8
9	Yellow Flash Phases	
A	Overlap A - Phases	1 8
B	Overlap B - Phases	
C	Overlap C - Phases	
D	Overlap D - Phases	
E	Restricted Phases	
F	Assign 5 Outputs	1

Configuration

E + F + ROW

<E Page>

Day of Week
1 = Sunday
2 = Monday
3 = Tuesday
4 = Wednesday
5 = Thursday
6 = Friday
7 = Saturday

Row		E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	
5	Overlap A - Green Omit	8
6	Overlap B - Green Omit	
7	Overlap C - Green Omit	
8	Overlap D - Green Omit	
9	Overlap Yellow Flash	
A	EV-A Phases	2 5
B	EV-B Phases	4 7
C	EV-C Phases	1 6
D	EV-D Phases	3 8
E	Extra 1 Config. Bits	1 345
F	IC Select (Interconnect)	2

Configuration

For access, set F + 9 + E = 1

E + E + ROW

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = EV Advance
5 = Remote Download
6 = Special Event
7 = Pretimed Operation
8 = Split Ring Operation

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Assign 5 Outputs
1 = Right Turn Overlap
2 = TOD Outputs
3 = EV Beacon - Steady
4 = EV Beacon - Flashing
5 = Special Event Outputs
6 = Phase 3 & 7 Ped
7 = Advanced Warning Sign
8 =

Time and Date
8-0 Hour, Minute, Day-of-Week
8-1 Day-of-Month, Year, Month
8-F Seconds

Disable Parity	0	D+B+0

Dial-Up Telephone Communications
(If set to a non-zero value, parity will be disabled)

Program Information
C + C + 0 = program
C + C + F = version

Remote Download
C + 0 + 4 = 1 -255
w/ E + E + E bit 5 on